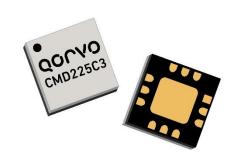
# **CMD225C3**

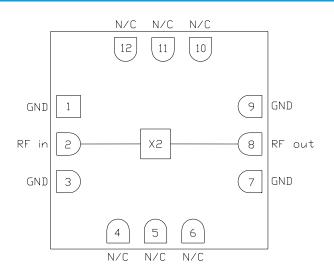
# Passive Frequency Doubler, 4-8 GHz Input

### **Product Overview**

The CMD225C3 die is a broadband MMIC GaAs x2 passive frequency multiplier housed in a leadless surface mount package. When driven by a +15 dBm signal, the multiplier provides 13 dB conversion loss at an output frequency of 12 GHz. The Fo and 3Fo isolations are >47 dBc and >54 dBc respectively. The CMD225C3 is a 50 ohm matched design eliminating the need for RF port matching.



### **Functional Block Diagram**



### **Key Features**

- Low Conversion Loss
- Excellent Fo Isolation
- Broadband Performance
- No Bias Required
- Pb-Free RoHS Compliant 3x3 mm SMT Package

# **Ordering Information**

Part No.	Description	
CMD225C3	100 Piece 7" Reel	
CMD225C3-EVB	1 Piece Bag	

# **Electrical Performance** (T<sub>A</sub> = 25° C, Pin = +15 dBm, Fin = 6 GHz)

Parameter	Min	Тур	Max	Units
Frequency Range, Input	4 - 8			GHz
Frequency Range, Output	8 - 16			GHz
Conversion Loss		13		dB
Fo Isolation (with respect to input level)		47		dB
3Fo Isolation (with respect to input level)		54		dB
4Fo Isolation (with respect to input level)		50		dB



# **Absolute Maximum Ratings**

Parameter	Rating
RF Input Power	+23 dBm
Operating Temperature	-55 to 85° C
Storage Temperature	-55 to 150° C
Thermal Resistance, Q <sub>JC</sub>	549° C/W

Exceeding any one or combination of the maximum ratings may cause permanent damage to the device.

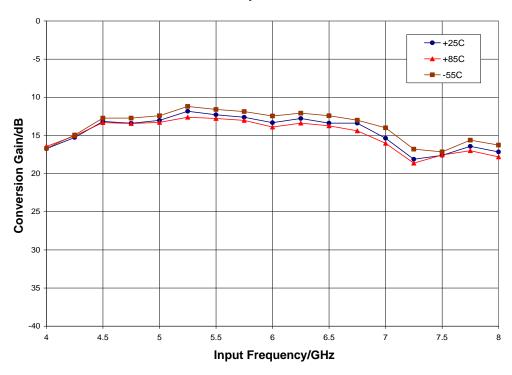
# **Electrical Specifications** (T<sub>A</sub> = 25° C, Pin = +15 dBm)

Parameter	Min	Тур	Max	Min	Тур	Max	Units
Frequency Range, Input	4 - 8		5 - 7			GHz	
Frequency Range, Output	8 - 16			10 - 14			GHz
Conversion Loss		13	20		13	17	dB
Fo Isolation (with respect to input level)	33	48		38	48		dB
3Fo Isolation (with respect to input level)	41	50		41	50		dB
4Fo Isolation (with respect to input level)	23	50		33	50		dB

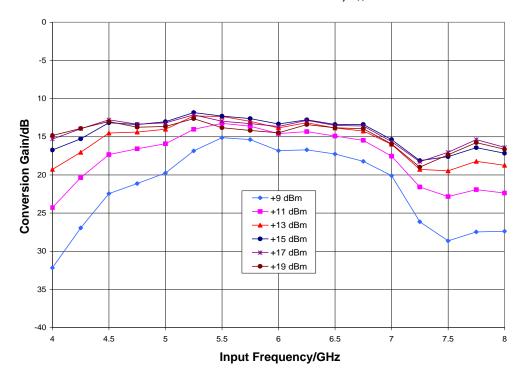


# **Typical Performance**

#### Conversion Gain vs. Temperature @ +15 dBm Drive Level



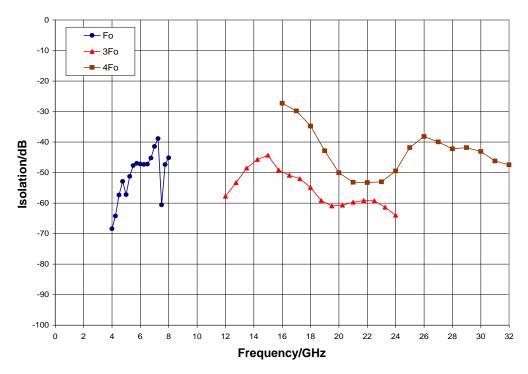
#### Conversion Gain vs. Drive Level, T<sub>A</sub> = 25° C



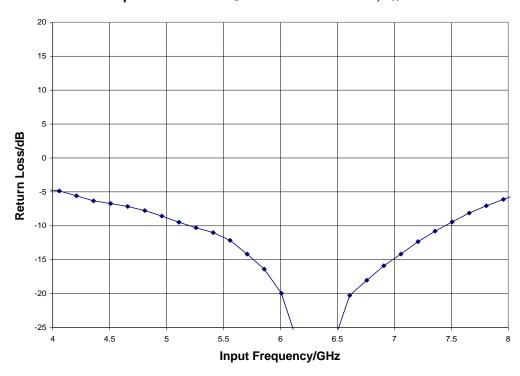


# **Typical Performance**

### Isolation (with respect to input level) @ +15 dBm Drive Level, $T_A = 25^{\circ}$ C



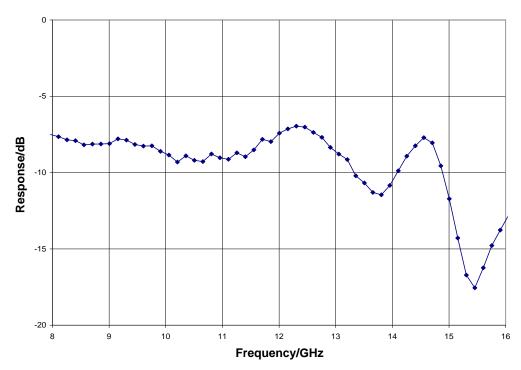
### Input Return Loss @ +15 dBm Drive Level, T<sub>A</sub> = 25° C





# **Typical Performance**

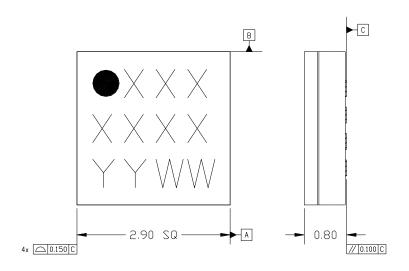
### Output Return Loss @ +15 dBm Drive Level, F = 6 GHz Input, T<sub>A</sub> = 25° C

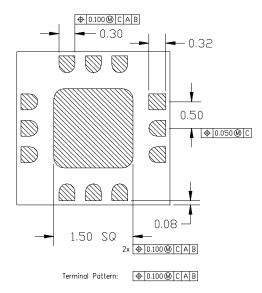




### **Mechanical Information**

#### **Package Information and Dimensions**





#### Notes:

- 1. All dimensions shown in mm.
- 2. Material: Black alumina
- Lead finish
  - 3.1. Ni: 8.89um max, 1.27um min
  - 3.2. Pd:0.17um max, 0.07um min
  - 3.3. Au: 0.254um max, 0.03um min
- 4. Marking
  - 4.1. Line 1: Part number
    - 4.1.1. Example: CMD177C3 shall be marked as 177
  - 4.2. Line 2: Lot number
  - 4.3. Line 3: Date code Last 2 digits of the year of manufacture followed by a 2 digit week code
- 5. Alternate pin #1 identifier is a single square pad
- 6. Alternate die paddle may have chamfered corners

#### **Recommended PCB Land Pattern**

Qorvo recommends that the user develop the land pattern that will provide the best design for proper solder reflow and device attach for their specific application. Please review Qorvo Application Note AN 105 for a recommended land pattern approach.

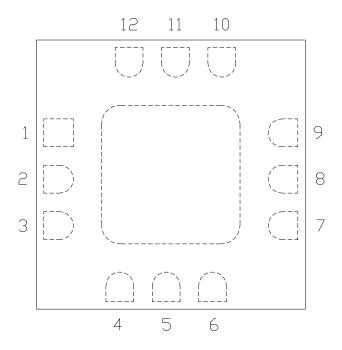
#### **Recommended Solder Reflow Profile**

Qorvo recommends screen printing with belt furnace reflow to ensure proper solder reflow and device attach. Please review Qorvo Application Note AN 102 for a recommended solder reflow profile.



# **Pin Description**

### Pin Diagram



### **Functional Description**

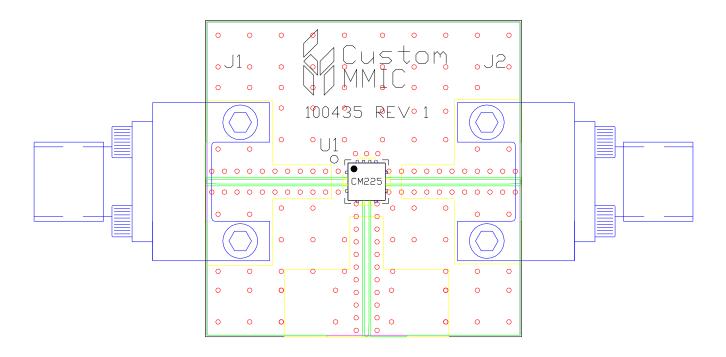
Pad	Function	Description	Schematic
1, 3, 7, 9 and die paddle	Ground	Connect to RF / DC ground	GND =
2	RF in	Pin is DC coupled and 50 ohm matched	RF in O
4 - 6, 10 - 12	N/C	No connection required These pins may be connected to RF / DC ground	
8	RF out	Pin is DC coupled and 50 ohm matched	RF out



# **Applications Information**

#### **Evaluation Board**

The circuit board shown has been developed for optimized assembly at Qorvo. A sufficient number of via holes should be used to connect the top and bottom ground planes. As surface mount processes vary, careful process development is recommended.



#### **Bill of Material**

Designator	Value	Description
J1 - J2		SMA End Launch Connector
U1		CMD225C3 Frequency Doubler
PCB		100435 Evaluation PCB

GaAs MMIC devices are susceptible to damage from Electrostatic Discharge. Proper precautions should be observed during handling, assembly and test.

### **Handling Precautions**

Parameter	Rating	Standard
ESD-Human Body Model (HBM)	Class 1A	ESDA/JEDEC JS-001-2012
MSL-Moisture Sensitivity Level	Level 1	JEDEC standard IPC/JEDEC J-STD-020



Caution! ESD-Sensitive Device

# **RoHS Compliance**

This part is compliant with 2011/65/EU RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment) as amended by Directive 2015/863/EU.

This product also has the following attributes:

- Lead Free
- Antimony Free
- TBBP-A (C<sub>15</sub>H<sub>12</sub>Br<sub>4</sub>O<sub>2</sub>) Free
- SVHC Free
- Halogen Free
- PFOS Free

### **Contact Information**

For the latest specifications, additional product information, worldwide sales and distribution locations:

Web: <u>www.qorvo.com</u> Tel: 1-844-890-8163

Email: customer.support@qorvo.com



# CMD225C3 Passive Frequency Doubler, 4-8 GHz Input

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