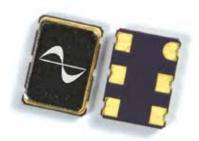
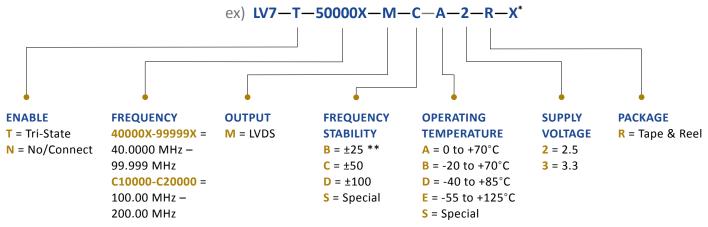
LV7-Series Specifications





7.00L x 5.00W x 1.80H (mm)

PDI Model *LV7-Series* provides precision timing in a hermetically sealed package. These high performance clock oscillators offer very low integrated phase jitter and superior phase noise. PDI provides quick-turn sampling for your proto-typing needs, mass production capability, and competitive pricing.



^{** -} Not available -55 to +125°C

^{* -} for standard or assigned for customization.

	** - Not available -55 to +	125°C * - f	or standard or assign	ed for customizati
Parameter		Supply Voltage*1 (±5%)		Units
		3.3	2.5	V
Frequency Range ^{*1}		40.000000 to	200.000000	MHz
Frequency Stability*1	All Causes (Maximum)*2	Per Option		ppm
Temperature Range ^{*1}	Operating	Per Option		°C
	Storage	-55 to +125		°C
Supply Current (Maximum)		40		mA
Output		Differential LVDS Squarewave		
Load (Typical)	Between Output and Comp Output	100		Ω
Duty Cycle (Maximum)	@ 50% Vcc	45/5	55	%
Rise/Fall Times (Maximum)	Rise Time (20% to 80% Vcc)	0.4		nS
Rise/Fail Tillies (Waxiillulli)	Fall Time (80% to 20% Vcc)	0.4		
Start up Time (Maximum)		10.0		mS
Output Voltage Levels	High Voh (RL = 100Ω)	1.43 Typical , 1.60 Maximum 0.90 Minimum , 1.10 Typical		V
Output voitage Levels	Low Vol (RL = 100Ω)			
Differential Voltage		250 Min , 350 T	yp , 450 Max	mV
Pin 1 (Tri-State) (Option)	High (See below) or Open	Enable		
	Low (See below)*3	Disable		
	High Value (Minimum)	70		% Vcc
	Low Value (Maximum)*3	30		% Vcc
	Disable Current (Typical)*3	10		uA
	Enable Delay Time (Maximum)	2.0)	mS
	Disable Delay Time (Maximum)	0.2	2	uS
Integrated Phase Jitter (RMS)	12KHz to 20MHz	0.3 Typical , 1.0 Maximum		pS
Phase Noise (Typical)	@ 10KHz Offset	-13	-138 -144 dBc/Hz	
	@ 100KHz Offset	-14		

^{*1 -} Not all Frequency/Temperature/Voltage combinations are available.

	rev: NA	SIZE: A	CAGE: 0S4G1	1 of 4
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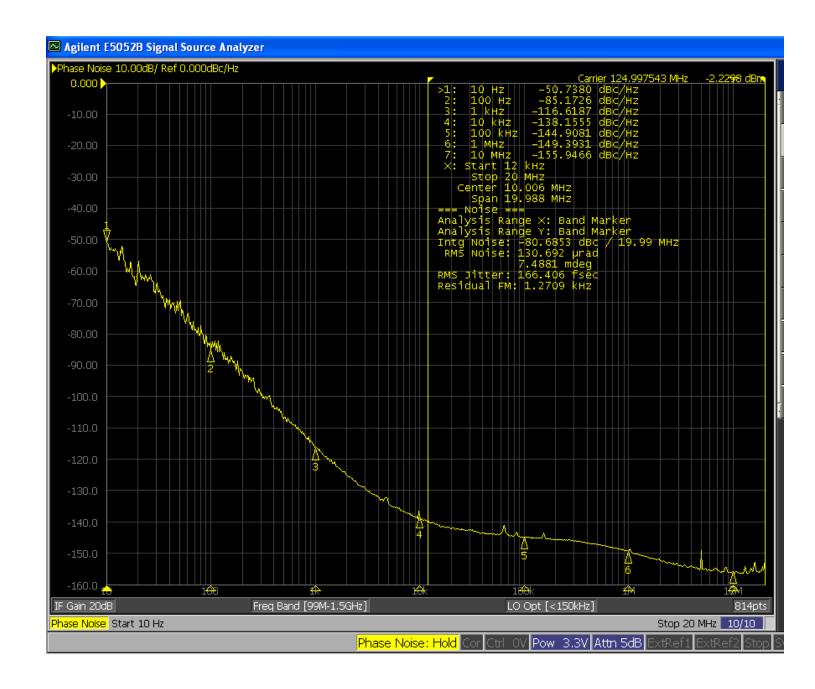
^{*2 -} Inclusive of Tolerance @25°C, Operating Temperature, Supply Voltage, Load, Aging, Shock and Vibration.

^{*3 -} Internal crystal oscillation halted.





TYPICAL PHASE NOISE PLOT



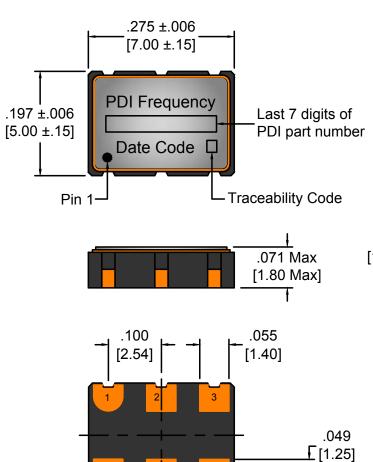


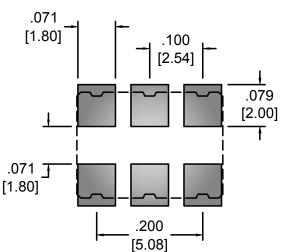
LV7-Series 7.00 x 5.00 x 1.80 (mm)

PACKAGE DIMENSIONS

Decimal XXX = \pm .008, XX = \pm .02 Metric [XXX = \pm .20], [XX = \pm .50]

PIN	CONNECTION	
1	Tri-State or No Connect	
2	No Connect	
3	Ground/Case	
4	Output	
5	Complementary Output	
6	Supply Voltage	





Recommended Land Pattern (Top View)





.200

[5.08]



.004 [0.10]

NOTES:

Terminals are Au.

Other options are available, please consult factory.

All product is supplied *RoHS* and *REACH* compliant.

Product can be supplied on Tape and Reel, on reels of 1,000 units.

Specifications subject to change without notice, last updated 4/1/13.

REV: NA SIZE: A CAGE: 0S4G1 3 of 4



LV7-Series 7.00 x 5.00 x 1.80 (mm)

- 1. Material: Black Conductive Polystyrene or equivalent.
- 2. 10 Sprocket Hole pitch cumulative tolerance of ±.008.
- 3. Camber in compliance with EIA 481.
- 4. Empty pockets: Trailing end (Minimum) 200 mm. and Leading end (Minimum) 400 mm.
- **5**. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.

