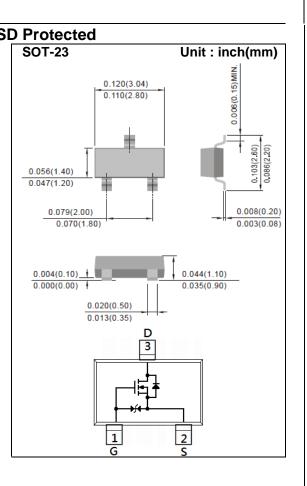


#### Features

- $R_{DS(ON)}$ ,  $V_{GS}@10V$ ,  $I_D@500mA<1.6\Omega$
- R<sub>DS(ON)</sub>, V<sub>GS</sub>@4.5V, I<sub>D</sub>@200mA<2.5Ω</li>
- R<sub>DS(ON)</sub>, V<sub>GS</sub>@2.5V, I<sub>D</sub>@100mA<4.5Ω •
- Advanced Trench Process Technology
- Specially Designed for Battery Operated Systems, Solid-State Relays Drivers: Relay, Displays, Memories, etc
- ESD Protected 2KV HBM
- AEC-Q101 gualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

- Case : SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams



## Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

500mA

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V <sub>DS</sub>	50	- v	
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 20		
Continuous Drain Current (Note 4)		I <sub>D</sub>	500	mA	
Pulsed Drain Current (Note 1)		I <sub>DM</sub>	1200		
Power Dissipation	T <sub>A</sub> =25°C	P <sub>D</sub>	500	mW	
	Derate above 25°C		4	mW/°C	
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,T <sub>STG</sub>	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient <sup>(Note 3,4)</sup>		$R_{ extsf{ heta}JA}$	250	°C/W	





### **Electrical Characteristics** ( $T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS		
Static								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	50	-	-	N		
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250$ uA	0.8	1	1.5	V		
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =500mA	-	0.96	1.6	Ω		
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =200mA	-	1.25	2.5			
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =100mA	-	2.73	4.5			
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ =50V, $V_{GS}$ =0V	-	-	1	uA		
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 20V, V <sub>DS</sub> =0V	-	-	<u>+</u> 10			
Dynamic (Note 5)								
Total Gate Charge	Qg	$V_{DS}$ =25V, I <sub>D</sub> =250mA, $V_{GS}$ =4.5V <sup>(Note 1,2)</sup>	-	0.63	1	nC		
Gate-Source Charge	$Q_gs$		-	0.2	-			
Gate-Drain Charge	$Q_gd$		-	0.23	-			
Input Capacitance	Ciss	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V,	-	25	50	pF		
Output Capacitance	Coss		-	9.5	20			
Reverse Transfer Capacitance	Crss	f=1MHZ	-	2.1	5			
Turn-On Delay Time	td <sub>(on)</sub>		-	2.2	5	ns		
Turn-On Rise Time	tr	$V_{DD}=25V, I_{D}=500mA,$ $V_{GS}=10V,$ $R_{G}=6\Omega^{(Note 1,2)}$	-	19.2	38			
Turn-Off Delay Time	td <sub>(off)</sub>		-	6.2	12			
Turn-Off Fall Time	tf	κ <sub>G</sub> =0Ω	-	23	50			
Drain-Source Diode								
Maximum Continuous Drain-Source Diode Forward Current	I <sub>S</sub>		-	-	500	mA		
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =500mA, V <sub>GS</sub> =0V	-	0.86	1.5	V		

NOTES:

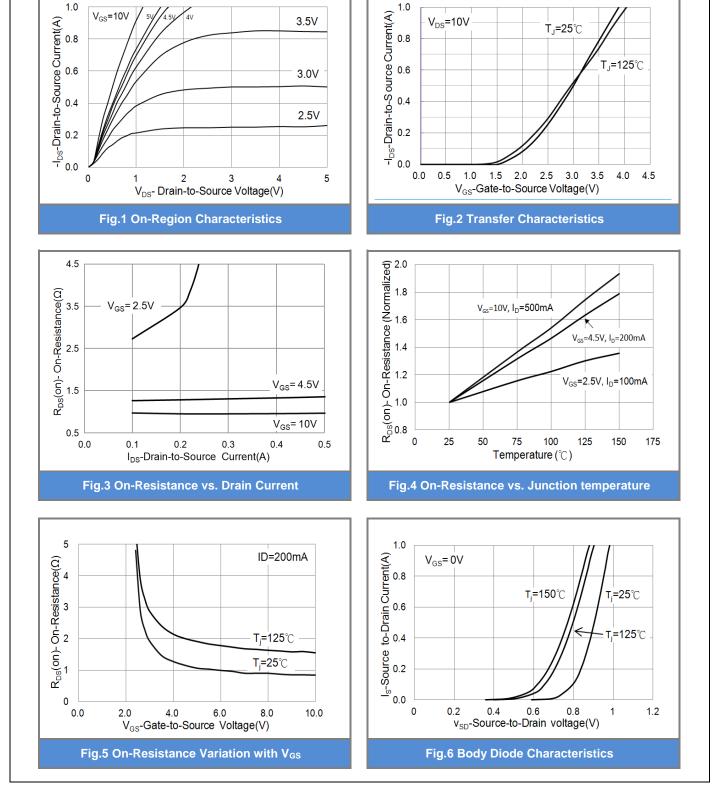
1. Pulse width</br>

2. Essentially independent of operating temperature typical characteristics.

3.  $R_{\Theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.

- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.

May 12,2017-REV.01

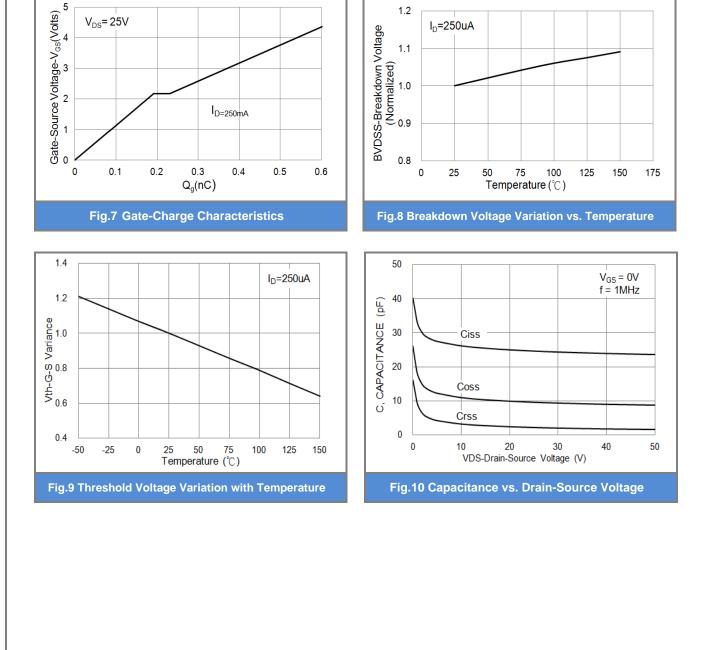


## PJA138K-AU

**TYPICAL CHARACTERISTIC CURVES** 







**TYPICAL CHARACTERISTIC CURVES** 

## 



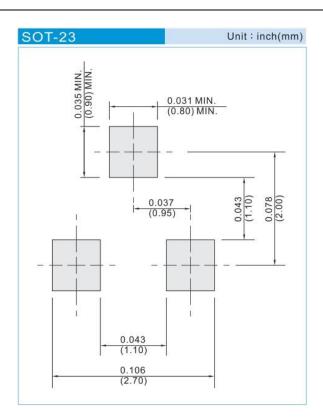




#### Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version	
PJA138K-AU_R1_000A1	SOT-23	3K pcs / 7" reel	8K3	Halogen free	

#### **Mounting Pad Layout**





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