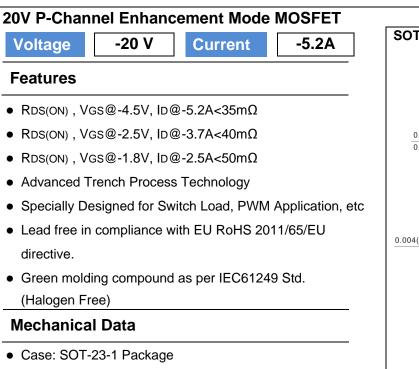
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	SEMI CONDUCTOR

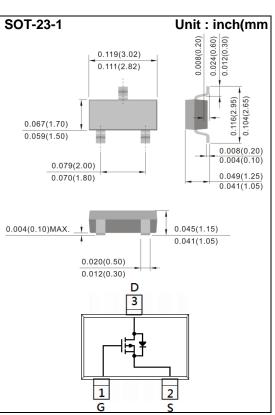
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PJA3417



- Terminals : Solderable per MIL-STD-750, Method 2026 •
- Marking: A17



Maximum Ratings and Thermal Characteristics (T_A=25[°]C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	-20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 8	V
Continuous Drain Current		I _D	-5.2	А
Pulsed Drain Current		I _{DM}	-20.8	А
Power Dissipation	T _a =25°C	P _D	1.25	W
	Derate above 25°C		10	mW/°C
Dperating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient ^(Note 3)		R _{θJA}	100	°C/W



Electrical Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

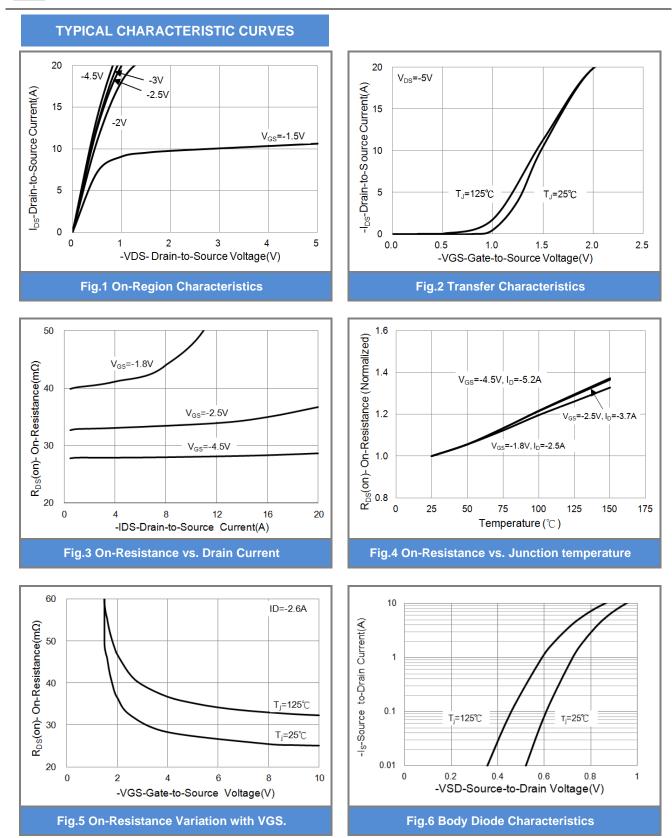
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V, I _D =-250uA	-20	-	-	V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250$ uA	-0.35	-0.59	-0.9	V
Drain-Source On-State Resistance		V _{GS} =-4.5V, I _D =-5.2A	-	29	35	mΩ
	$R_{DS(on)}$	V _{GS} =-2.5V, I _D =-3.7A	-	33	40	
		V _{GS} =-1.8V, I _D =-2.5A	-	40	50	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V	-	-0.01	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 8V, V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic (Note 5)						
Total Gate Charge	Qg	V_{DS} =-10V, I _D =-5.2A, V_{GS} =-4.5V ^(Note 1,2)	-	18.9	-	nC
Gate-Source Charge	Q_gs		-	2.8	-	
Gate-Drain Charge	Q_gd		-	4.2	-	
Input Capacitance	Ciss	V _{DS} =-10V, V _{GS} =0V, f=1.0MHZ	-	1758	-	pF
Output Capacitance	Coss		-	153	-	
Reverse Transfer Capacitance	Crss		-	125	-	
Turn-On Delay Time	td _(on)	V_{DD} =-10V, I _D =-5.2A, V_{GS} =-4.5V, R_{G} =6 Ω ^(Note 1,2)	-	12	-	
Turn-On Rise Time	tr		-	68	-	
Turn-Off Delay Time	td _(off)		-	82	-	ns
Turn-Off Fall Time	tf		-	35	-	
Drain-Source Diode						
Maximum Continuous Drain-Source					-1.5	Α
Diode Forward Current	۱ _S		-	-	-1.5	A
Diode Forward Voltage	V_{SD}	I _S =-1.0A, V _{GS} =0V	-	-0.69	-1.2	V

NOTES :

1. Pulse width</br>

- 2. Essentially independent of operating temperature typical characteristics.
- 3. ReJA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited
- 5. Guaranteed by design, not subject to production testing.





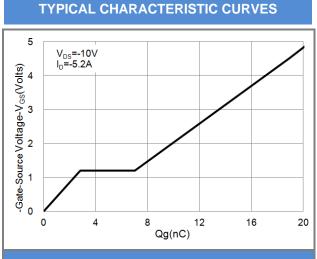


Fig.7 Gate-Charge Characteristics

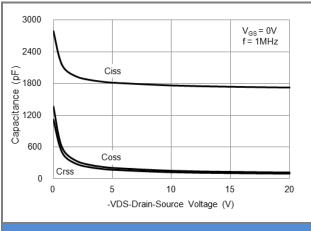


Fig.9 Capacitance vs. Drain-Source Voltage.

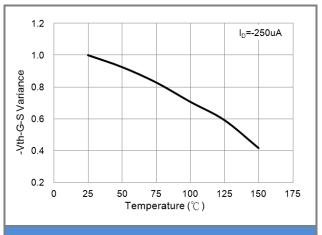


Fig.8 Threshold Voltage Variation with Temperature

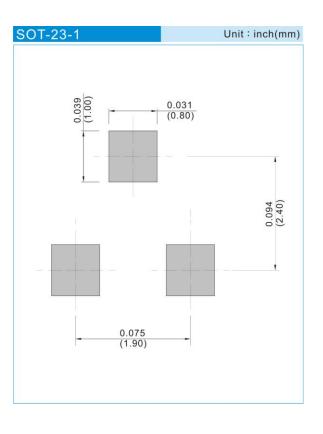




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJA3417_R1_00001	SOT-23-1	3K pcs / 7" reel	A17	Halogen free
PJA3417_R2_00001	SOT-23-1	12K pcs / 13" reel	A17	Halogen free

MOUNTING PAD LAYOUT







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