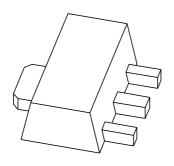
## **DISCRETE SEMICONDUCTORS**

## DATA SHEET



# BCV28; BCV48 PNP Darlington transistors

Product specification Supersedes data of 1999 Apr 08

2004 Dec 06





Philips Semiconductors

## **PNP Darlington transistors**

## **BCV28; BCV48**

#### **FEATURES**

- Very high DC current gain (min. 10000)
- High current (max. 500 mA)
- Low voltage (max. 60 V).

#### **APPLICATIONS**

• Where very high amplification is required.

#### **DESCRIPTION**

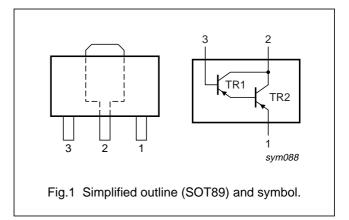
PNP Darlington transistor in a SOT89 plastic package. NPN complements: BCV29 and BCV49.

#### **MARKING**

TYPE NUMBER	MARKING CODE
BCV28	ED
BCV48	EE

#### **PINNING**

PIN	DESCRIPTION	
1	emitter	
2	collector	
3	base	



#### **ORDERING INFORMATION**

TYPE NUMBER		PACKAGE	
I TPE NOWIBER	NAME	DESCRIPTION	VERSION
BCV28	SC-62	plastic surface mounted package; collector pad for good heat	SOT89
BCV48		transfer; 3 leads	

## **PNP Darlington transistors**

BCV28; BCV48

#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter			
	BCV28		_	-40	V
	BCV48		_	-80	V
V <sub>CES</sub>	collector-emitter voltage	V <sub>BE</sub> = 0 V			
	BCV28		_	-30	V
	BCV48		_	-60	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	-10	V
I <sub>C</sub>	collector current (DC)		_	-500	mA
I <sub>CM</sub>	peak collector current		_	-800	mA
I <sub>B</sub>	base current (DC)		_	-100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	_	1.3	W
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	ambient temperature		-65	+150	°C

#### Note

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	note 1	96	K/W
$R_{th(j-s)}$	thermal resistance from junction to soldering point		16	K/W

#### Note

1. Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>. For other mounting conditions, see "Thermal considerations for SOT89 in the General Part of associated Handbook".

Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 6 cm<sup>2</sup>.
 For other mounting conditions, see "Thermal considerations for SOT89 in the General Part of associated Handbook".

## PNP Darlington transistors

BCV28; BCV48

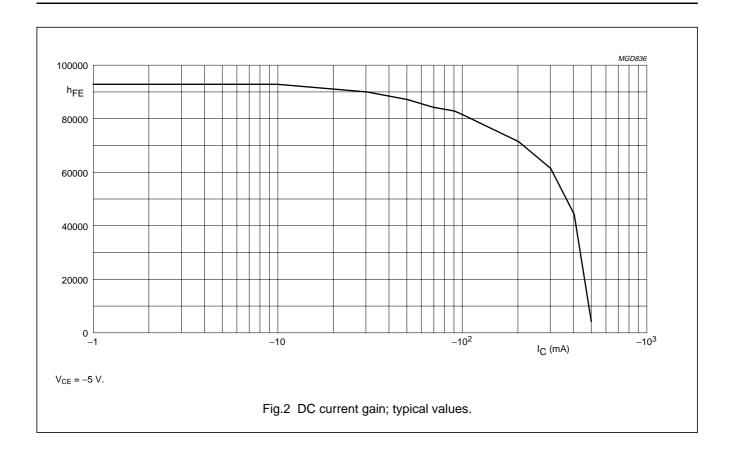
#### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector-base cut-off current					
	BCV28	$I_E = 0 \text{ A}; V_{CB} = -30 \text{ V}$	-	_	-100	nA
	BCV48	$I_E = 0 \text{ A}; V_{CB} = -60 \text{ V}$	-	_	-100	nA
I <sub>EBO</sub>	emitter-base cut-off current	I <sub>C</sub> = 0 A; V <sub>BE</sub> = -10 V	_	_	-100	nA
h <sub>FE</sub>	DC current gain	$I_C = -1 \text{ mA}; V_{CE} = -5 \text{ V}; \text{ see Fig.2}$				
	BCV28		4000	_	_	
	BCV48		2000	_	_	
	DC current gain	$I_C = -10 \text{ mA}; V_{CE} = -5 \text{ V}; \text{ see Fig.2}$				
	BCV28		10000	_	_	
	BCV48		4000	_	_	
	DC current gain	$I_C = -100 \text{ mA}$ ; $V_{CE} = -5 \text{ V}$ ; see Fig.2				
	BCV28		20000	_	_	
	BCV48		10000	_	_	
	DC current gain	$I_C = -500 \text{ mA}$ ; $V_{CE} = -5 \text{ V}$ ; see Fig.2				
	BCV28		4000	_	_	
	BCV48		2000	_	_	
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = -100 \text{ mA}; I_B = -0.1 \text{ mA}$	_	_	-1	V
V <sub>BEsat</sub>	base-emitter saturation voltage	$I_C = -100 \text{ mA}; I_B = -0.1 \text{ mA}$	_	_	-1.5	V
$V_{BEon}$	base-emitter on-state voltage	$I_C = -10 \text{ mA}; I_B = -5 \text{ mA}$	_	_	-1.4	V
f <sub>T</sub>	transition frequency	$I_C = -30 \text{ mA}; V_{CE} = -5 \text{ V};$ f = 100 MHz	_	220	_	MHz

## PNP Darlington transistors

BCV28; BCV48



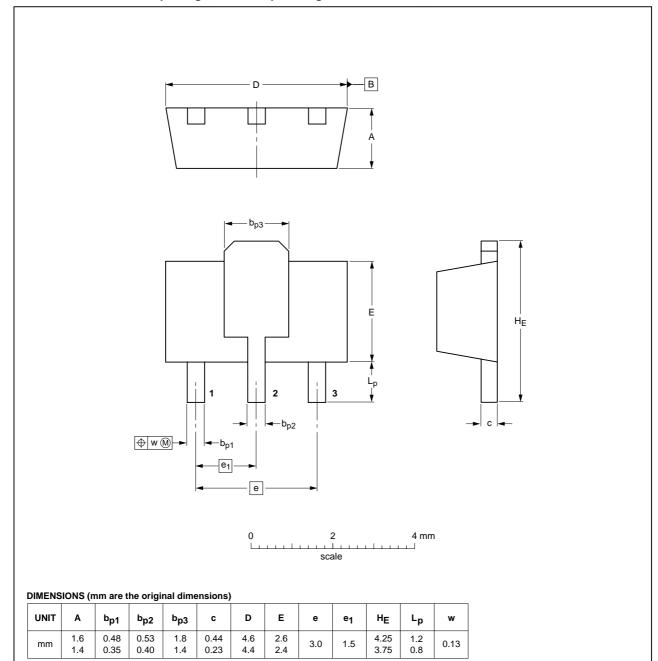
## PNP Darlington transistors

BCV28; BCV48

#### **PACKAGE OUTLINE**

#### Plastic surface mounted package; collector pad for good heat transfer; 3 leads

SOT89



	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT89		TO-243	SC-62			<del>99-09-13</del> 04-08-03

### PNP Darlington transistors

BCV28; BCV48

#### **DATA SHEET STATUS**

LEVEL	DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
II	Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
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- 3. For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

#### **DEFINITIONS**

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