DISCRETE SEMICONDUCTORS

DATA SHEET

2PD601A series NPN general purpose transistors; 50 V, 100 mA

Product specification Supersedes data of 2002 Jun 26 2004 Feb 12





NPN general purpose transistors; 50 V, 100 mA

2PD601A series

FEATURES

- Available in SOT323 (SC-70) and SOT346 (SC-59) packages
- Available in three different DC current gain versions (Q, R, S).

APPLICATIONS

• General purpose switching and amplification.

DESCRIPTION

NPN general purpose transistors (see "Simplified outline, symbol and pinning" for package details).

QUICK REFERENCE DATA

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
V _{CEO}	collector-emitter voltage	_	50	V
I _C	collector current (DC)	_	100	mA
h _{FE}	DC current gain			
	group Q	160	260	
	group R	210	340	
	group S	290	460	

PRODUCT OVERVIEW

TYPE NUMBER	PACI	KAGE	MADVING CODE	h CROUD
	PHILIPS	EIAJ	MARKING CODE	h _{FE} GROUP
2PD601AQ	SOT346	SC-59	ZQ	Q
2PD601AR	SOT346	SC-59	ZR	R
2PD601AS	SOT346	SC-59	ZS	S
2PD601AQW	SOT323	SC-70	*6D	Q
2PD601ARW	SOT323	SC-70	*6E	R
2PD601ASW	SOT323	SC-70	*6F	S

Note

- 1. * = p: Made in Hong Kong.
 - * = t: Made in Malaysia.
 - * = W: Made in China.

SIMPLIFIED OUTLINE, SYMBOL AND PINNING

TYPE NUMBER	SIMPLIFIED OUTLINE AND SYMBOL		PINNING
I TPE NUMBER	SIMPLIFIED OUTLINE AND STMBOL	PIN	DESCRIPTION
2PD601AQ		1	base
2PD601AR		2	emitter
2PD601AS	<u> 3 </u>	3	collector
2PD601AQW			
2PD601ARW	' 		
2PD601ASW	1		
	Top view MAM321		

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ORDERING INFORMATION

TYPE NUMBER		PACKAGE				
I TPE NUMBER	NAME	DESCRIPTION	VERSION			
2PD601AQ	_	plastic surface mounted package; 3 leads	SOT346			
2PD601AR						
2PD601AS						
2PD601AQW	_	plastic surface mounted package; 3 leads	SOT323			
2PD601ARW						
2PD601ASW						

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	60	V
V _{CEO}	collector-emitter voltage	open base	_	50	V
V _{EBO}	emitter-base voltage	open collector	_	6	V
I _C	collector current (DC)	_	100	mA	
I _{CM}	peak collector current	eak collector current		200	mA
P _{tot}	total power dissipation T _{amb} ≤ 25 °C; note 1				
	SOT346		_	250	mW
	SOT323		_	200	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1		
	SOT346		500	K/W
	SOT323		625	K/W

Note

1. Refer to SOT346 (SC-59) and SOT323 (SC-70) standard mounting conditions.

Soldering

Reflow soldering is the only recommended soldering method.

^{1.} Refer to SOT346 (SC-59) and SOT323 (SC-70) standard mounting conditions.

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CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	I _E = 0; V _{CB} = 60 V	_	10	nA
		$I_E = 0$; $V_{CB} = 60 \text{ V}$; $T_j = 150 ^{\circ}\text{C}$	_	5	μΑ
I _{EBO}	emitter-base cut-off current	I _C = 0; V _{EB} = 5 V	_	10	nA
h _{FE}	DC current gain	I _C = 100 mA; V _{CE} = 2 V; note 1	90	_	
h _{FE}	DC current gain	I _C = 2 mA; V _{CE} = 10 V			
	group Q		160	260	
	group R		210	340	
	group S		290	460	
V _{CEsat}	collector-emitter saturation voltage	I _C = 100 mA; I _B = 10 mA; note 1	_	250	mV
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = 10 \text{ V}$; $f = 1 \text{ MHz}$	_	3	pF
f _T	transition frequency	I _C = 2 mA; V _{CE} = 10 V; f = 100 MHz	100	_	MHz

Note

^{1.} Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

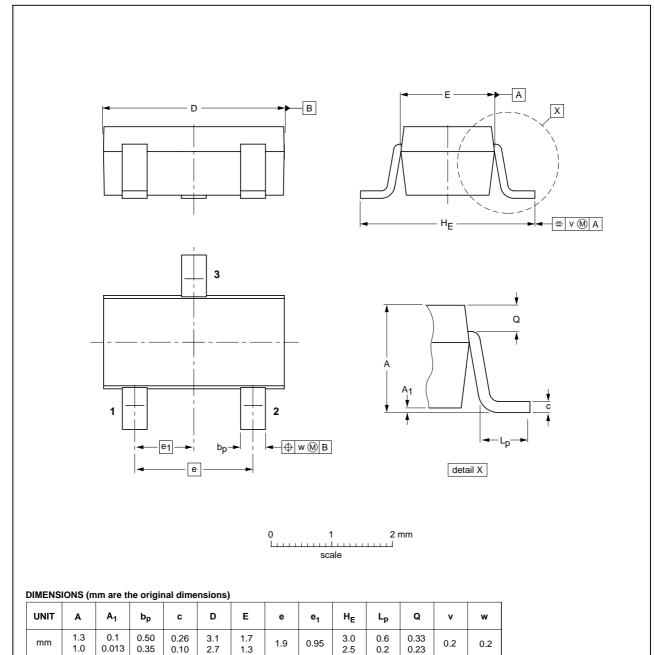
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PACKAGE OUTLINES

Plastic surface mounted package; 3 leads

SOT346



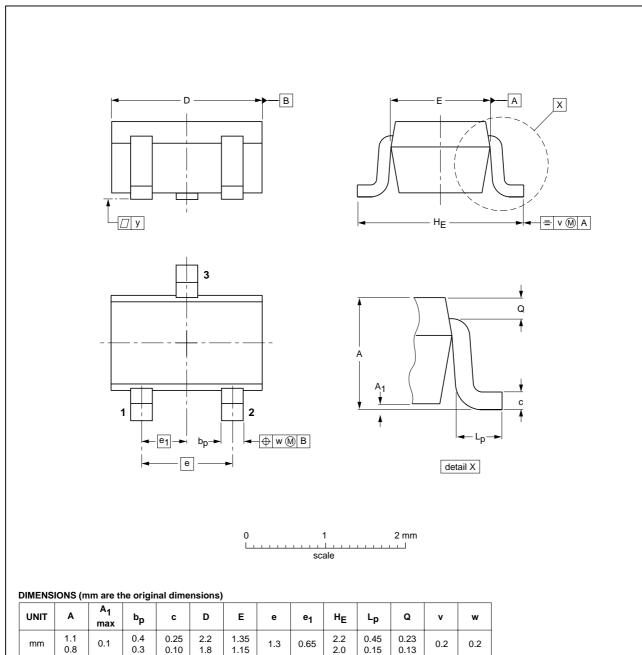
OUTLINE		REFERENCES			EUROPEAN LOQUE DATE	
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT346		TO-236	SC-59			98-07-17

NPN general purpose transistors; 50 V, 100 mA

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Plastic surface mounted package; 3 leads

SOT323



OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	EIAJ		PROJECTION	1330E DATE
SOT323			SC-70			97-02-28

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DATA SHEET STATUS

LEVEL	DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾⁽³⁾	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
II	Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
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Notes

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- 3. For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

DEFINITIONS

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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