

PBSS5230T

30 V, 2 A PNP low VCEsat (BISS) transistor Rev. 2 — 4 June 2012

Product data sheet

1. **Product profile**

1.1 General description

PNP low V_{CEsat} Breakthrough In Small Signal (BISS) transistor in a SOT23 small Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS4230T.

1.2 Features and benefits

- Low collector-emiter saturation voltage V_{CEsat}
- High collector current capability: I_C and I_{CM}

1.3 Applications

- DC-to-DC conversion
- Supply line switching
- Battery charger
- LCD backlighting

1.4 Quick reference data

- Higher efficiency leading to less heat generation
- AEC-Q101 qualified
- Driver in low supply voltage applications (e.g. lamps and LEDs)
- Inductive load driver (e.g. relays, buzzers and motors)

Table 1.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	-30	V
I _C	collector current		-	-	-2	А
I _{CM}	peak collector current	single pulse; $t_p \le 1 \text{ ms}$	-	-	-3	А
R _{CEsat}	collector-emitter saturation resistance	I_C = -500 mA; I_B = -50 mA; pulsed; $t_p \le 300$ μs; δ ≤ 0.02 ; T_{amb} = 25 °C	-	160	220	Ω



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2. Pinning information

Table 2.	Pinning	j information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		
2	Е	emitter		3
3	С	collector		
			SOT23 (TO-236AB)	sym013

3. Ordering information

Table 3. Ordering in	nformation		
Type number	Package		
	Name	Description	Version
PBSS5230T	TO-236AB	plastic surface-mounted package; 3 leads	SOT23

4. Marking

Table 4. Marking codes

Type number	Marking code ^[1]
PBSS5230T	%3G

[1] % = placeholder for manufacturing site code

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-30	V
V _{CEO}	collector-emitter voltage	open base		-	-30	V
V _{EBO}	emitter-base voltage	open collector		-	-5	V
I _C	collector current			-	-2	А
I _{CM}	peak collector current	single pulse; $t_p \le 1 \text{ ms}$		-	-3	А
I _B	base current			-	-300	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	<u>[1]</u>	-	300	mW
			[2]	-	480	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

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6. Thermal characteristics

Table 6.	Thermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance	in free air	<u>[1]</u>	-	-	417	K/W
	from junction to ambient		[2]	-	-	260	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

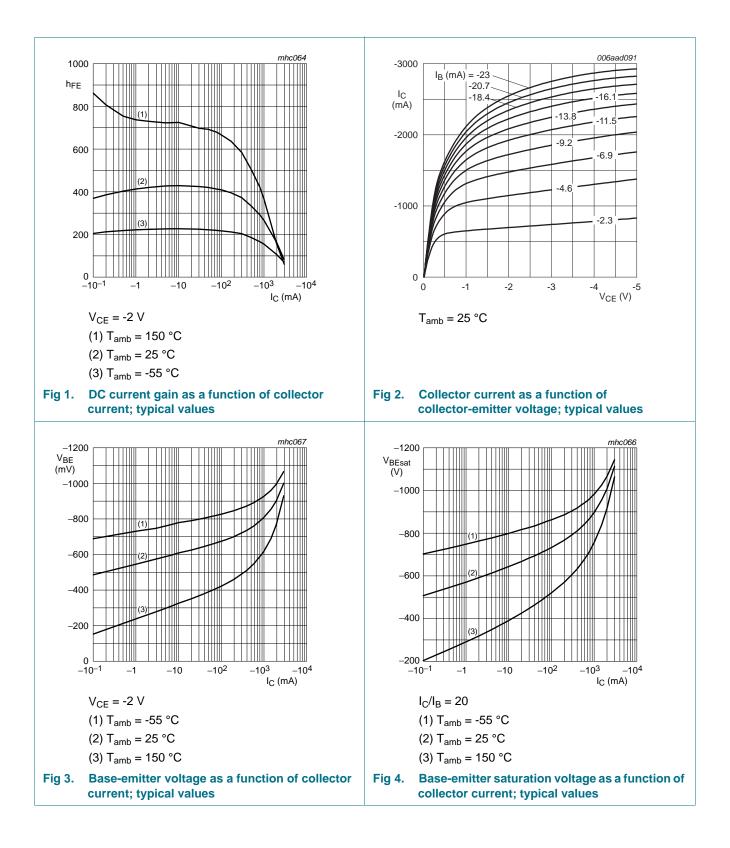
7. Characteristics

Table 7.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V_{CB} = -30 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-100	nA
	current	V_{CB} = -30 V; I _E = 0 A; T _j = 150 °C	-	-	-50	μA
I _{EBO}	emitter-base cut-off current	V_{EB} = -4 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-100	nA
h _{FE}	DC current gain	V_{CE} = -2 V; I _C = -100 mA; T _{amb} = 25 °C	300	450	-	
		$ V_{CE} = -2 \text{ V; } I_C = -1 \text{ A; pulsed;} $	200	290	-	
			100	180	-	
V _{CEsat}	collector-emitter saturation voltage	I_{C} = -500 mA; I_{B} = -50 mA; T_{amb} = 25 °C	-	-70	-110	mV
		I_{C} = -1 A; I_{B} = -50 mA; T_{amb} = 25 °C	-	-140	-225	mV
		I_{C} = -2 A; I_{B} = -200 mA; T_{amb} = 25 °C	-	-240	-350	mV
R _{CEsat}	collector-emitter saturation resistance	I_{C} = -500 mA; I_{B} = -50 mA; pulsed; $t_{p} \le 300$ μs; δ ≤ 0.02 ; T_{amb} = 25 °C	-	160	220	Ω
V _{BEsat}	base-emitter saturation voltage	I_{C} = -2 A; I_{B} = -50 mA; pulsed; $t_{p} \le 300 \ \mu$ s; $\delta \le 0.02$; T_{amb} = 25 °C	-	-	-1.1	V
V _{BEon}	base-emitter turn-on voltage	V_{CE} = -2 V; I _C = -100 mA; T _{amb} = 25 °C	-	-	-0.75	V
f⊤	transition frequency	V _{CE} = -10 V; I _C = -100 mA; f = 100 MHz; T _{amb} = 25 °C	100	200	-	MHz
C _c	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C	-	23	28	pF

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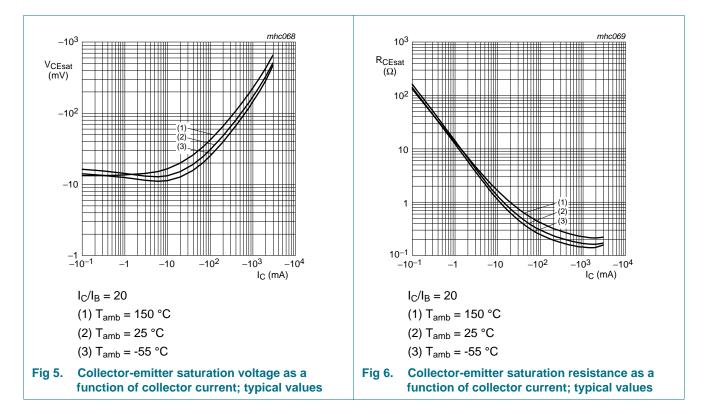


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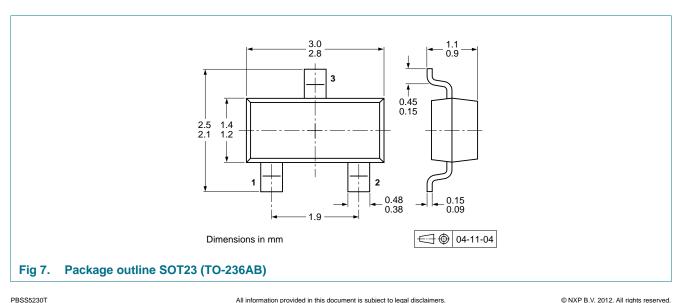


Test information 8.

8.1 **Quality information**

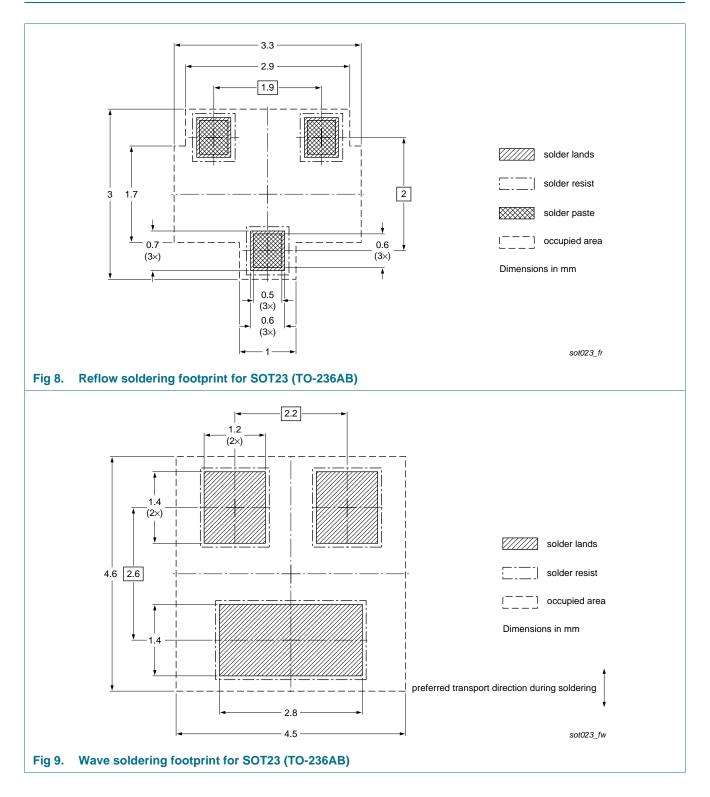
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

Package outline 9.



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10. Soldering



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11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
PBSS5230T v.2	20120604	Product data sheet	-	PBSS5230T v.1		
Modifications:	 The format of this document has been redesigned to comply with the new identity guidelines of NXP Semiconductors. 					
	 Legal texts have been adapted to the new company name where appropriate. 					
	 <u>1 "Product profile</u>": updated 					
	• <u>4 "Marking"</u> : corrected					
	• Table 5.: updated					
	• 7 "Characteristics": V _{CEsat} corrected, Fig 1. to Fig 6. added					
	8 "Test information": added					
	 9 "Package outline": replaced by minimized package outline drawing 					
	• 10 "Solderin	g": added				
PBSS5230T v.1	20031218	Product data sheet	-	-		

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12. Legal information

12.1 Data sheet status

Document status[1] [2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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