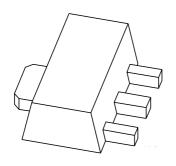
DISCRETE SEMICONDUCTORS

DATA SHEET



PXTA14NPN Darlington transistor

Product specification Supersedes data of 1999 Apr 14 2004 Dec 09





Philips Semiconductors

NPN Darlington transistor

PXTA14

FEATURES

- High current (max. 500 mA)
- Low voltage (max. 30 V).

APPLICATIONS

• High input impedance preamplifiers.

DESCRIPTION

NPN Darlington transistor in a SOT89 plastic package. PNP complement: PXTA64.

MARKING

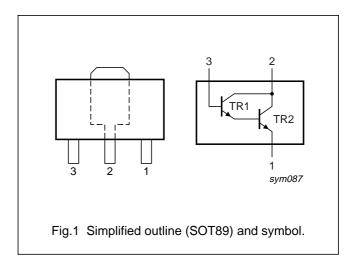
TYPE NUMBER	MARKING CODE(1)	
PXTA14	*1N	

Note

- 1. * = p: Made in Hong Kong.
 - * = t: Made in Malaysia.
 - * = W: Made in China.

PINNING

PIN	DESCRIPTION
1	emitter
2	collector
3	base



ORDERING INFORMATION

TYPE NUMBER	PACKAGE			
TIPE NOMBER	NAME DESCRIPTION			
PXTA14	SC-62	SC-62 plastic surface mounted package; collector pad for good heat transfer; 3 leads		

NPN Darlington transistor

PXTA14

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	30	V
V _{CES}	collector-emitter voltage	V _{BE} = 0 V	_	30	V
V _{EBO}	emitter-base voltage	open collector	_	10	V
I _C	collector current (DC)		_	500	mA
I _{CM}	peak collector current		_	1	А
I _B	base current (DC)		_	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	1.3	W
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	ambient temperature		-65	+150	°C

Note

1. Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 6 cm². For other mounting conditions, see "Thermal considerations for the SOT89 in the General Part of associated Handbook".

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	96	K/W
R _{th(j-s)}	thermal resistance from junction to solder point		16	K/W

Note

1. Device mounted on a printed-circuit board, single-sided copper, tin-plated, mounting pad for collector 6 cm². For other mounting conditions, see "Thermal considerations for the SOT89 in the General Part of associated Handbook".

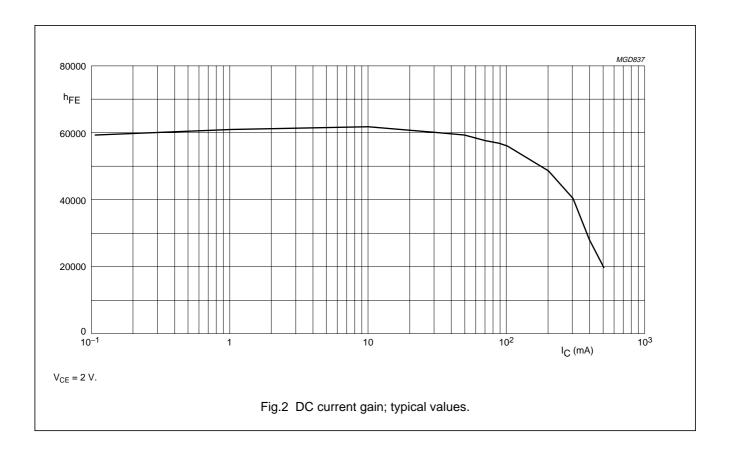
CHARACTERISTICS

 T_{amb} = 25 $^{\circ}C$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	I _E = 0 A; V _{CB} = 30 V	_	100	nA
I _{CES}	collector-emitter cut-off current	V _{BE} = 0 V; V _{CE} = 30 V	_	100	nA
I _{EBO}	emitter cut-off current	I _C = 0 A; V _{EB} = 10 V	_	100	nA
h _{FE}	DC current gain	$I_C = 10 \text{ mA}; V_{CE} = 5 \text{ V}; \text{ (see Fig.2)}$	10000	_	
		I _C = 100 mA; V _{CE} = 5 V; (see Fig.2)	20000	_	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 100 \text{ mA}; I_B = 0.1 \text{ mA}$	_	1.5	V
V_{BEsat}	base-emitter saturation voltage	$I_C = 100 \text{ mA}; I_B = 0.1 \text{ mA}$	_	1.5	V
V_{BEon}	base-emitter on-state voltage	$I_C = 100 \text{ mA}; V_{CE} = 5 \text{ V}$	_	2	V
f _T	transition frequency	$I_C = 30 \text{ mA}; V_{CE} = 5 \text{ V}; f = 100 \text{ MHz}$	125	_	MHz

NPN Darlington transistor

PXTA14



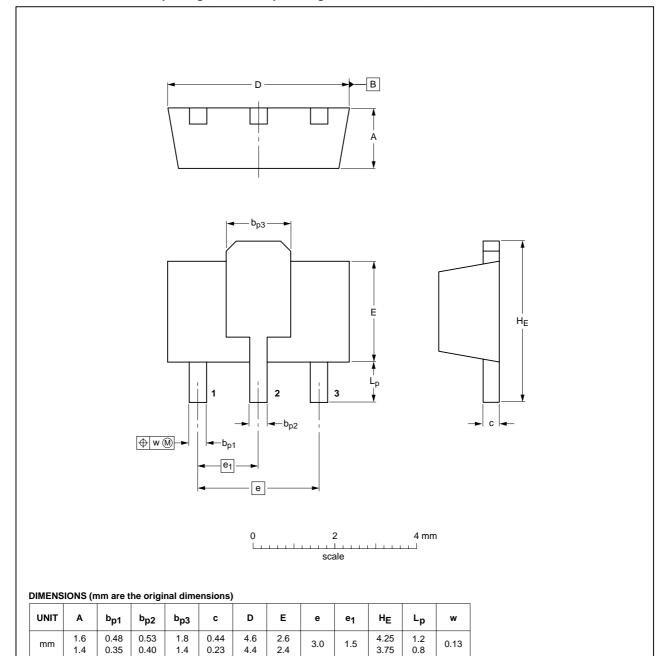
NPN Darlington transistor

PXTA14

PACKAGE OUTLINE

Plastic surface mounted package; collector pad for good heat transfer; 3 leads

SOT89



OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION ISSUE DAT	
SOT89		TO-243	SC-62			99-09-13 04-08-03

NPN Darlington transistor

PXTA14

DATA SHEET STATUS

LEVEL	DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
II	Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
III	Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Relevant changes will be communicated via a Customer Product/Process Change Notification (CPCN).

Notes

- 1. Please consult the most recently issued data sheet before initiating or completing a design.
- 2. The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.
- 3. For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

DEFINITIONS

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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