



ON Semiconductor®

ON Semiconductor
DATA SHEET**MCH6627** — N-Channel and P-Channel Silicon MOSFETs
General-Purpose Switching Device Applications**Features**

- The MCH6627 incorporates a N-channel MOSFET and a P-channel MOSFET that feature low ON-resistance and high-speed switching, thereby enabling high-density mounting.
- Excellent ON-resistance characteristic.
- 4V drive.

Specifications**Absolute Maximum Ratings** at Ta=25°C

Parameter	Symbol	Conditions	N-channel	P-channel	Unit
Drain-to-Source Voltage	V _{DSS}		30	-30	V
Gate-to-Source Voltage	V _{GSS}		±20	±20	V
Drain Current (DC)	I _D		1.4	-1.0	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	5.6	-4.0	A
Allowable Power Dissipation	P _D	Mounted on a ceramic board (900mm²×0.8mm)1unit	0.8		W
Channel Temperature	T _{ch}		150		°C
Storage Temperature	T _{stg}		-55 to +150		°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
[N-channel]						
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	I _D =1mA, V _{GS} =0	30			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0			1	μA
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} =±16V, V _{DS} =0			±10	μA
Cutoff Voltage	V _{GS(off)}	V _{DS} =10V, I _D =1mA	1.2		2.6	V
Forward Transfer Admittance	y _{fs}	V _{DS} =10V, I _D =0.7A	0.6	1.1		S
Static Drain-to-Source On-State Resistance	R _{DS(on)1}	I _D =0.7A, V _{GS} =10V		230	300	mΩ
	R _{DS(on)2}	I _D =0.4A, V _{GS} =4V		400	560	mΩ
Input Capacitance	C _{iss}	V _{DS} =10V, f=1MHz		65		pF
Output Capacitance	C _{oss}	V _{DS} =10V, f=1MHz		14		pF
Reverse Transfer Capacitance	C _{rss}	V _{DS} =10V, f=1MHz		8		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit.		5		ns
Rise Time	t _r	See specified Test Circuit.		4		ns
Turn-OFF Delay Time	t _{d(off)}	See specified Test Circuit.		11		ns
Fall Time	t _f	See specified Test Circuit.		3		ns

Marking : WB

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MCH6627

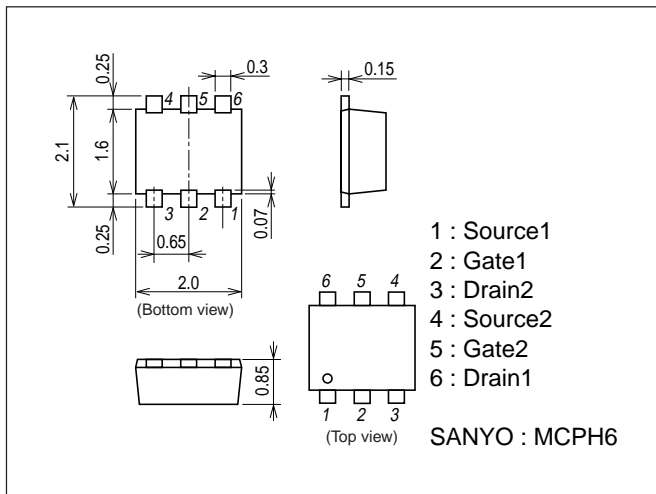
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =1.4A		2.5		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =1.4A		0.6		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =10V, I _D =1.4A		0.3		nC
Diode Forward Voltage	V _{SD}	I _S =1.4A, V _{GS} =0		0.87	1.2	V
[P-channel]						
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	I _D =-1mA, V _{GS} =0	-30			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0			-1	μA
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} =±16V, V _{DS} =0			±10	μA
Cutoff Voltage	V _{GS(off)}	V _{DS} =-10V, I _D =-1mA	-1.2		-2.6	V
Forward Transfer Admittance	y _{fs}	V _{DS} =-10V, I _D =-500mA	0.4	0.8		S
Static Drain-to-Source On-State Resistance	R _{DS(on)1}	I _D =-500mA, V _{GS} =-10V		420	550	mΩ
	R _{DS(on)2}	I _D =-300mA, V _{GS} =-4V		720	1000	mΩ
Input Capacitance	C _{iss}	V _{DS} =-10V, f=1MHz		75		pF
Output Capacitance	C _{oss}	V _{DS} =-10V, f=1MHz		16		pF
Reverse Transfer Capacitance	C _{rss}	V _{DS} =-10V, f=1MHz		9		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit.		6		ns
Rise Time	t _r	See specified Test Circuit.		4		ns
Turn-OFF Delay Time	t _{d(off)}	See specified Test Circuit.		12		ns
Fall Time	t _f	See specified Test Circuit.		4		ns
Total Gate Charge	Qg	V _{DS} =-10V, V _{GS} =-10V, I _D =-1A		2.6		nC
Gate-to-Source Charge	Qgs	V _{DS} =-10V, V _{GS} =-10V, I _D =-1A		0.5		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =-10V, V _{GS} =-10V, I _D =-1A		0.5		nC
Diode Forward Voltage	V _{SD}	I _S =-1A, V _{GS} =0		-0.89	-1.5	V

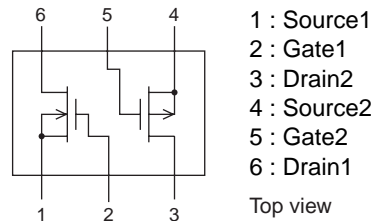
Package Dimensions

unit : mm

2173A

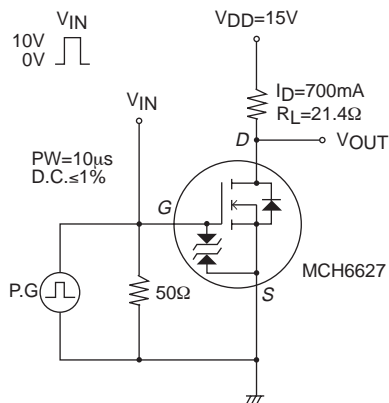


Electrical Connection

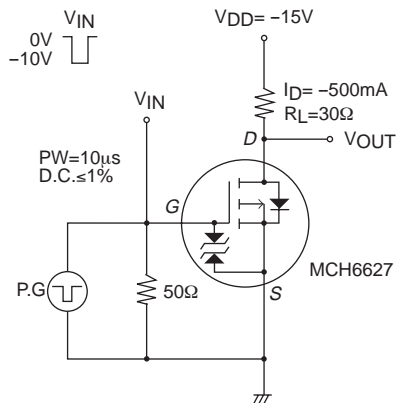


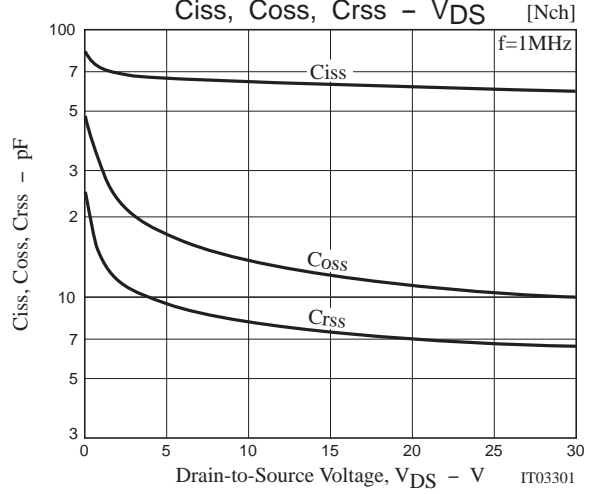
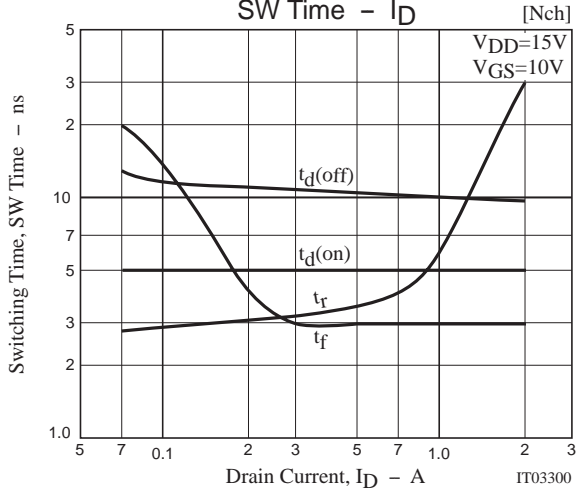
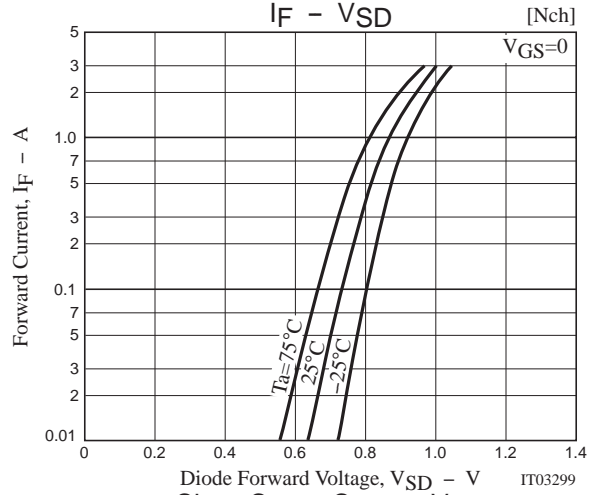
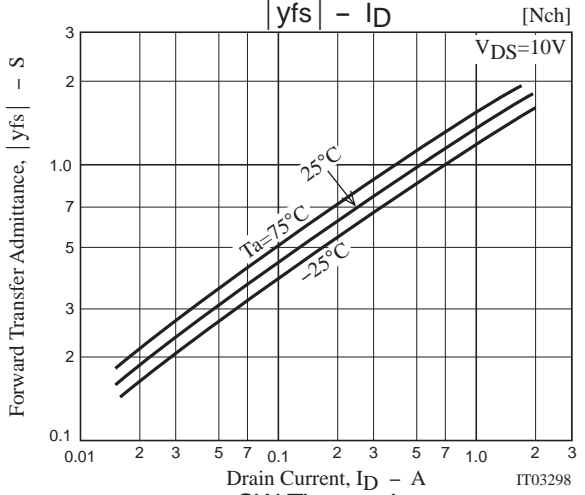
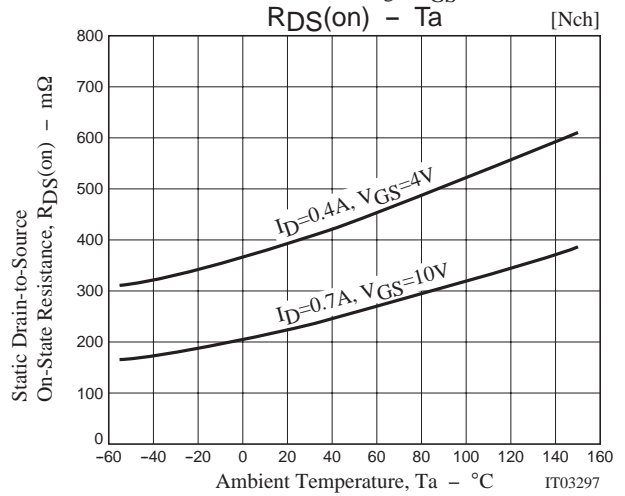
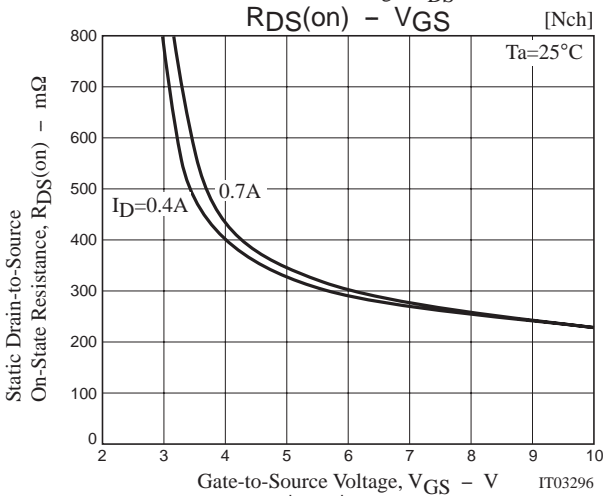
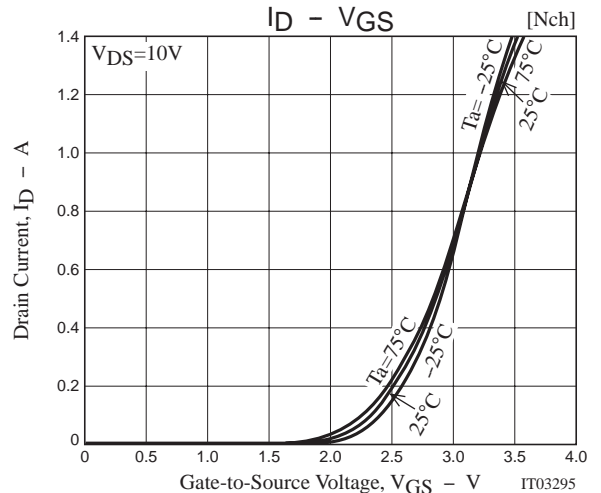
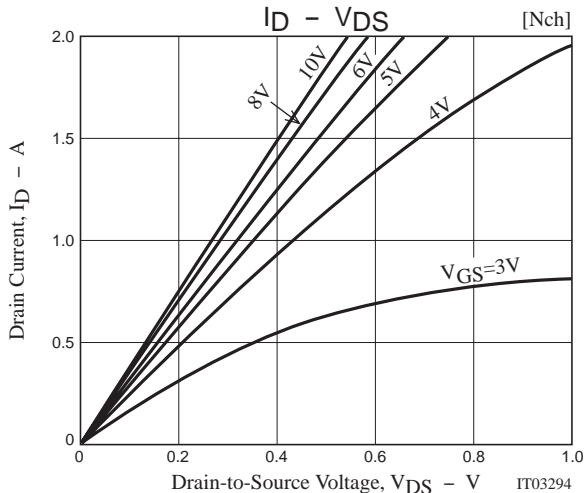
Switching Time Test Circuit

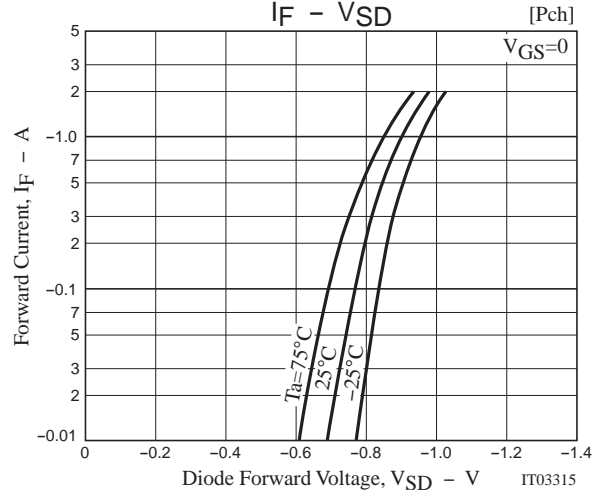
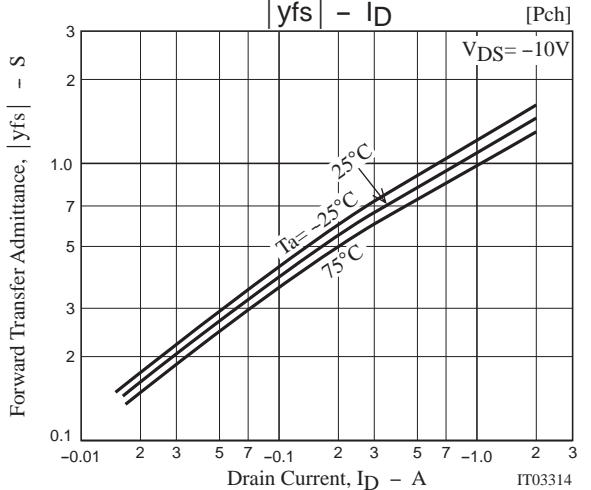
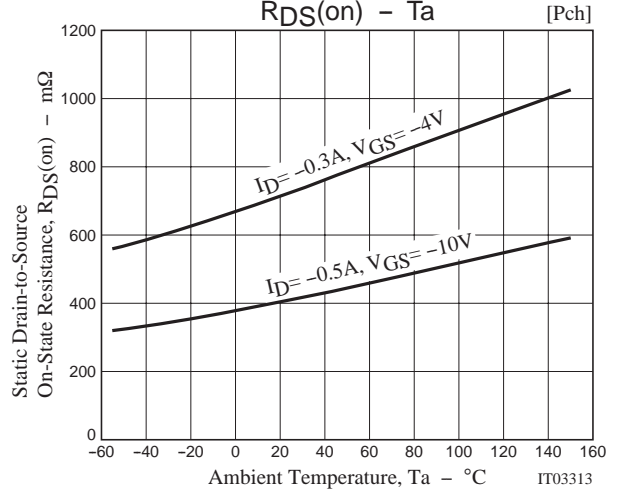
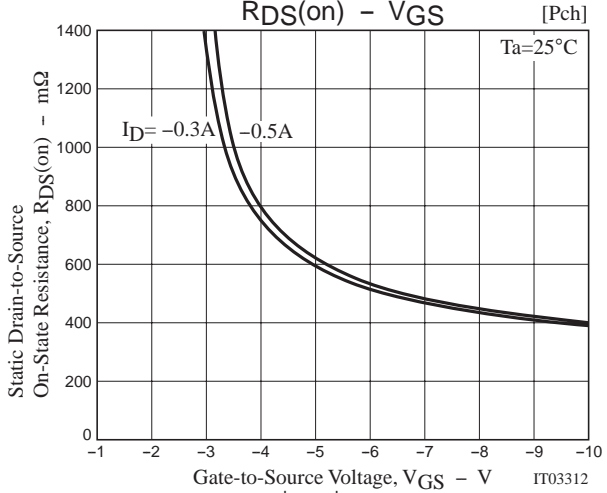
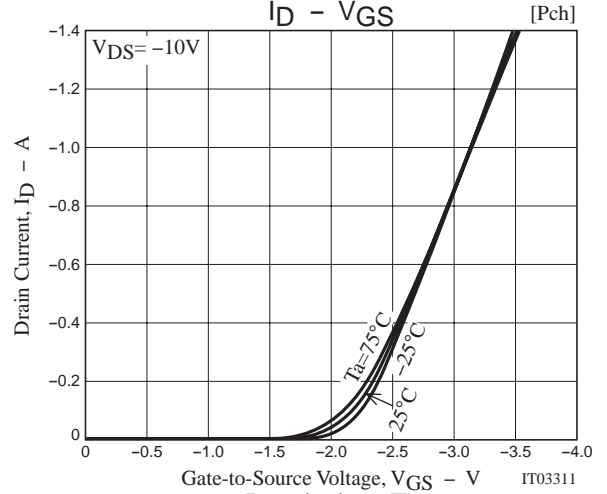
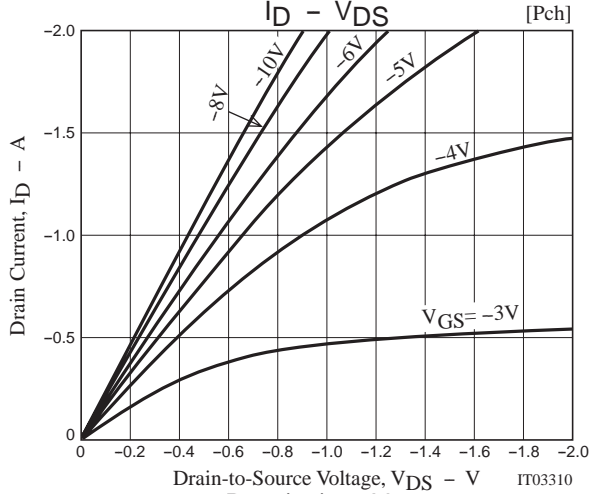
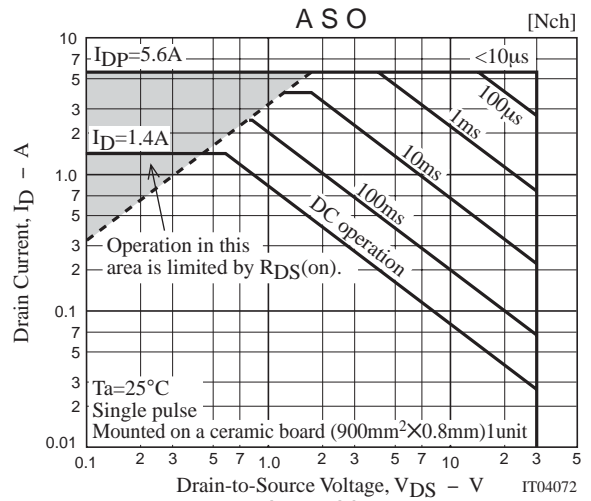
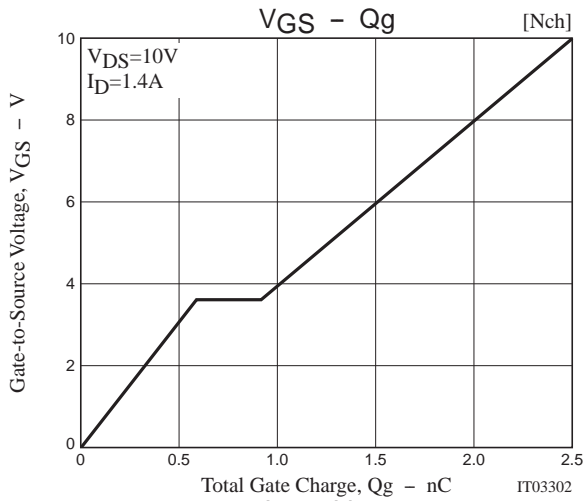
[N-channel]

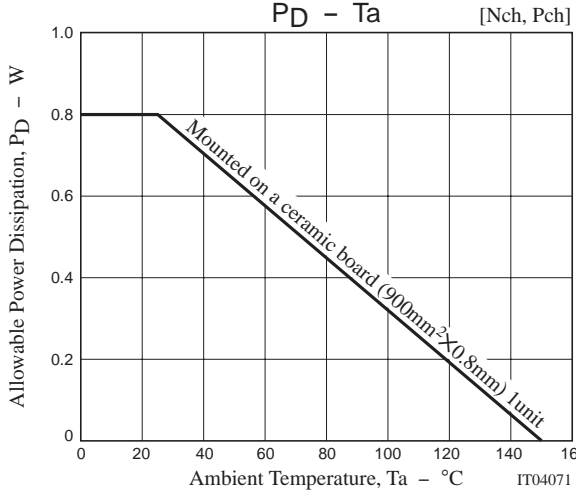
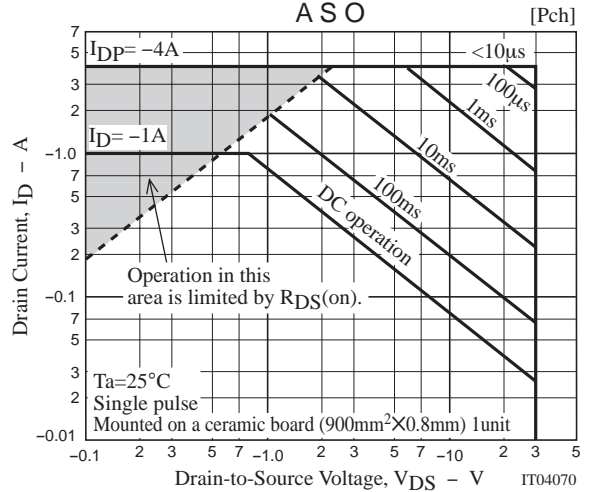
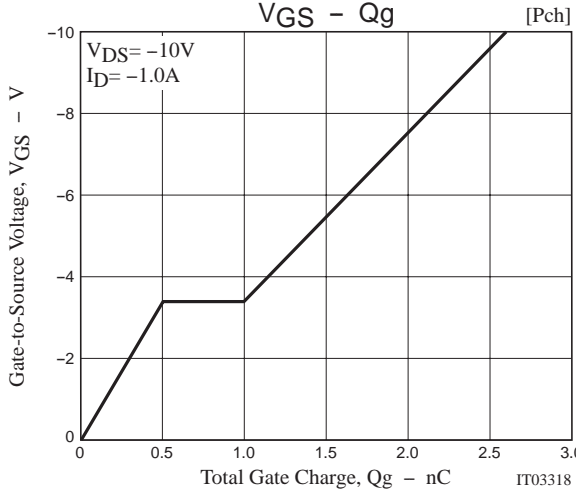
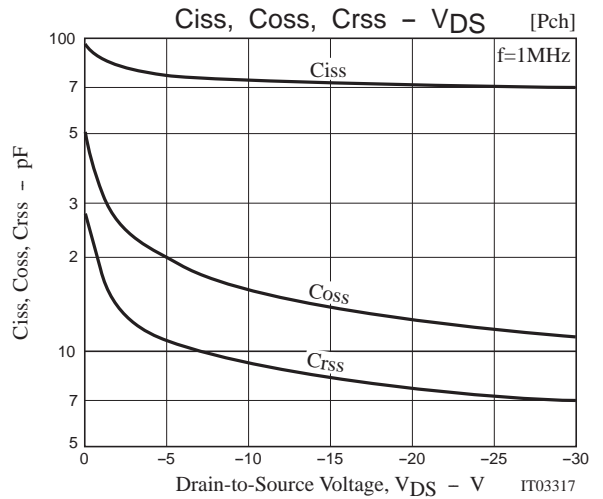
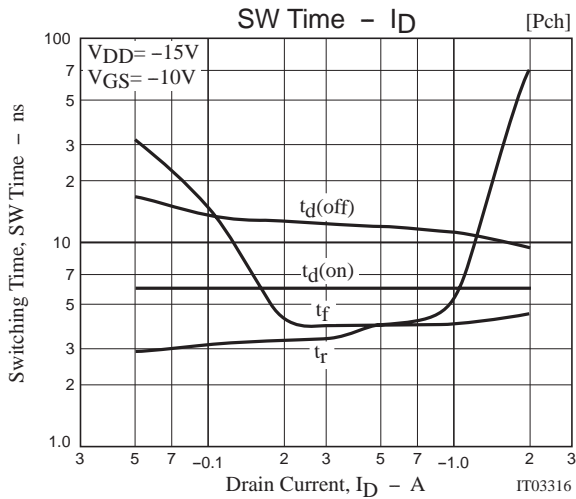


[P-channel]









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