## **Dual D-Type Positive Edge-Triggered Flip-Flop**

The SN74LS74A dual edge-triggered flip-flop utilizes Schottky TTL circuitry to produce high speed D-type flip-flops. Each flip-flop has individual clear and set inputs, and also complementary Q and  $\overline{Q}$ outputs.

Information at input D is transferred to the Q output on the positive-going edge of the clock pulse. Clock triggering occurs at a voltage level of the clock pulse and is not directly related to the transition time of the positive-going pulse. When the clock input is at either the HIGH or the LOW level, the D input signal has no effect.



## ON Semiconductor<sup>®</sup>

http://onsemi.com

LOW POWER SCHOTTKY

> PLASTIC **N SUFFIX** CASE 646

> > SOIC

**D SUFFIX** 

CASE 751A

## **MODE SELECT - TRUTH TABLE**

OPERATING MODE		INPUTS	OUTPUTS		
OPERATING MODE	SD	CD	D	Q	ā
Set	L	Н	Х	н	L
Reset (Clear)	н	L	Х	L	H
*Undetermined	L	L	X	Н	н
Load "1" (Set)	Н	Н	h	н	L
Load "0" (Reset)	Н	Н	1	L	Н

Both outputs will be HIGH while both  $\overline{S}_D$  and  $\overline{C}_D$  are LOW, but the output states are unpredictable if  $\overline{S}_D$  and  $\overline{C}_D$  go HIGH simultaneously. If the levels JEFOR at the set and clear are near  $V_{IL}$  maximum then we cannot guarantee to meet the minimum level for VOH

- H, h = HIGH Voltage Level
- L, I = LOW Voltage Level
- X = Don't Care
- I, h (q) = Lower case letters indicate the state of the referenced input (or output) one set-up time prior to the HIGH to LOW clock transition.



SOEIAJ **M SUFFIX CASE 965** 

### **GUARANTEED OPERATING RANGES**

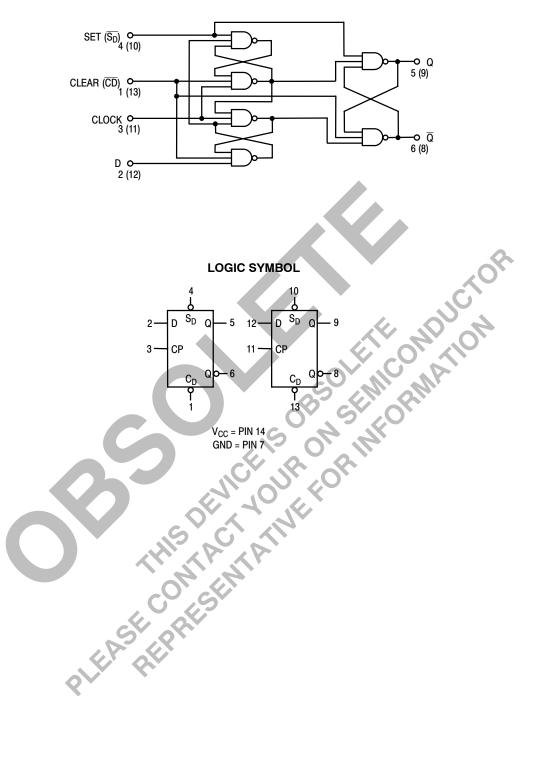
Symbol	Parameter	Min	Тур	Max	Unit
V <sub>CC</sub>	Supply Voltage	4.75	5.0	5.25	V
T <sub>A</sub>	Operating Ambient Temperature Range	0	25	70	°C
I <sub>OH</sub>	Output Current – High			-0.4	mA
I <sub>OL</sub>	Output Current – Low			8.0	mA

### **ORDERING INFORMATION**

Device	Package	Shipping
SN74LS74AN	14 Pin DIP	2000 Units/Box
SN74LS74AD	SOIC-14	55 Units/Rail
SN74LS74ADR2	SOIC-14	2500/Tape & Reel
SN74LS74AM	SOEIAJ-14	See Note 1
SN74LS74AMEL	SOEIAJ-14	See Note 1

1. For ordering information on the EIAJ version of the SOIC package, please contact your local ON Semiconductor representative.

## LOGIC DIAGRAM (Each Flip-Flop)



		Limits		Limits		Limits			
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions			
V <sub>IH</sub>	Input HIGH Voltage	2.0			V	Guaranteed Input HIGH Voltage for All Inputs			
V <sub>IL</sub>	Input LOW Voltage			0.8	V	Guaranteed Input LOW Voltage for All Inputs			
V <sub>IK</sub>	Input Clamp Diode Voltage		-0.65	-1.5	V	$V_{CC}$ = MIN, $I_{IN}$ = – 18 mA			
V <sub>OH</sub>	Output HIGH Voltage	2.7	3.5		V	$\label{eq:VCC} \begin{array}{l} V_{CC} = MIN, \ I_{OH} = MAX, \ V_{IN} = V_{IH} \\ \text{or } V_{IL} \ \text{per Truth Table} \end{array}$			
			0.25	0.4	V	I <sub>OL</sub> = 4.0 mA	$V_{CC} = V_{CC} MIN,$		
V <sub>OL</sub>	Output LOW Voltage		0.35	0.5	V	I <sub>OL</sub> = 8.0 mA	V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> per Truth Table		
Iн	Input High Current Data, Clock Set, Clear			20 40	μΑ	V <sub>CC</sub> = MAX, V <sub>IN</sub> = 2.7 V			
	Data, Clock Set, Clear			0.1 0.2	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub> =	7.0 V		
I <sub>IL</sub>	Input LOW Current Data, Clock Set, Clear			-0.4 -0.8	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub> =	= 0.4 V		
I <sub>OS</sub>	Output Short Circuit Current (Note 2)	-20		-100	mA	V <sub>CC</sub> = MAX			
I <sub>CC</sub>	Power Supply Current			8.0	mA	V <sub>CC</sub> = MAX			

## DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

2. Not more than one output should be shorted at a time, nor for more than 1 second. **AC CHARACTERISTICS** ( $T_{\Delta} = 25^{\circ}C$ ,  $V_{CC} = 5.0$  V)

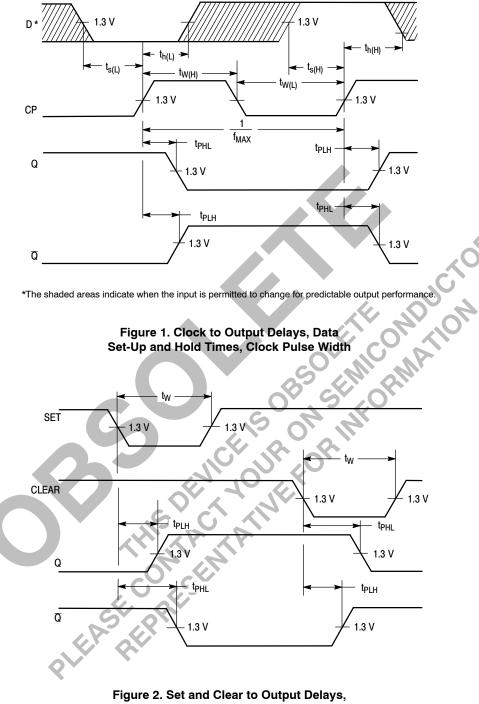
# AC CHARACTERISTICS ( $T_A = 25^{\circ}C$ , $V_{CC} = 5.0$ V).

			Limits				
Symbol	Parameter	Min	Тур	Max	Unit	Test Co	onditions
f <sub>MAX</sub>	Maximum Clock Frequency	25	33		MHz	Figure 1	
t <sub>PLH</sub>			13	25	ns	Eisen d	V <sub>CC</sub> = 5.0 V C <sub>L</sub> = 15 pF
t <sub>PHL</sub>	Clock, Clear, Set to Output		25	40	ns	Figure 1	

# AC SETUP REQUIREMENTS (T<sub>A</sub> = 25°C)

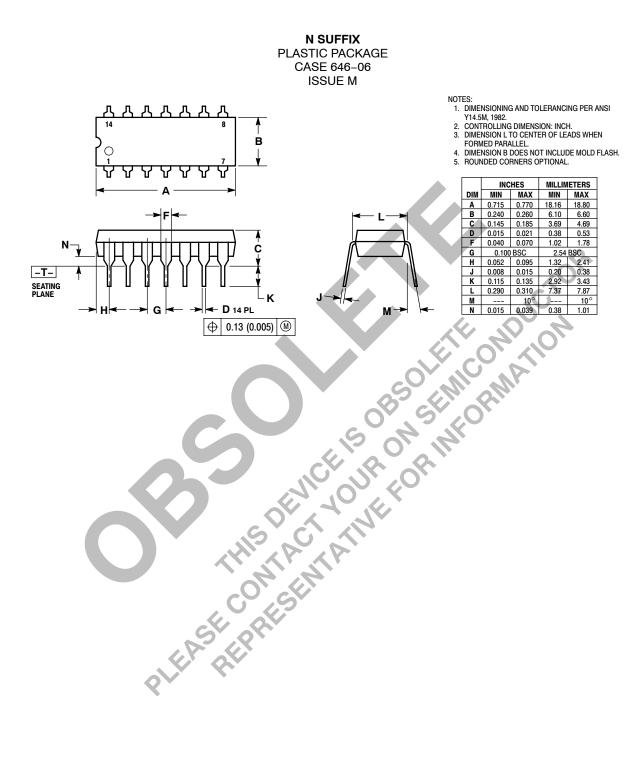
		Limits					
Symbol	Parameter	Min Typ Max		Unit	Test Conditions		
t <sub>W (H)</sub>	Clock	25			ns	Figure 1	
t <sub>W (L)</sub>	Clear, Set	25			ns	Figure 2	
÷	Data Setup Time — HIGH	20			ns	Figure 1	V <sub>CC</sub> = 5.0 V
ls	LOW	20			ns		
t <sub>h</sub>	Hold Time	5.0			ns	Figure 1	]

#### AC WAVEFORMS

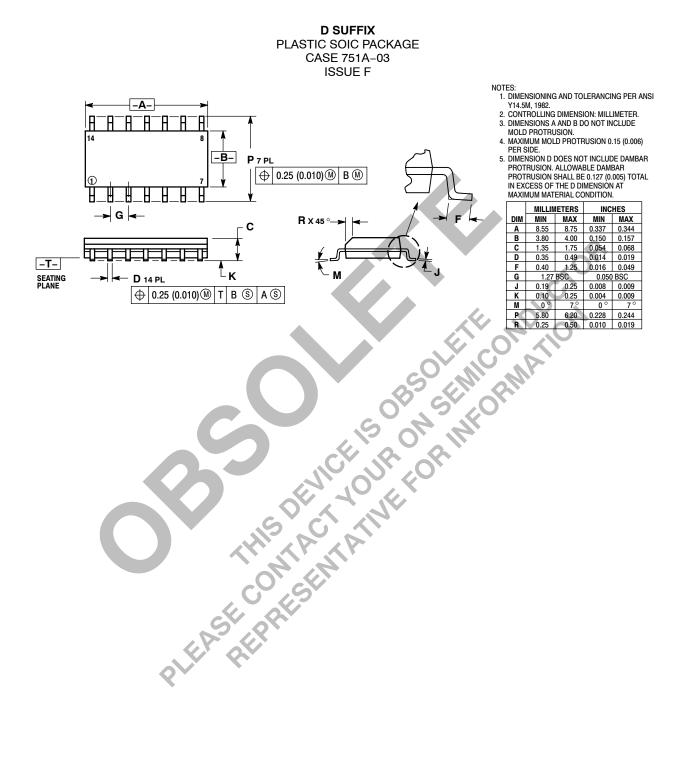


Set and Clear Pulse Widths

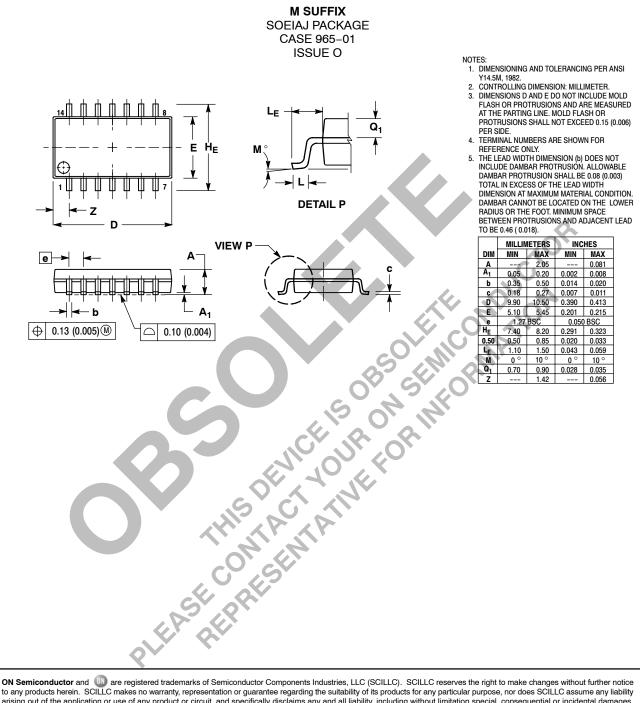
### PACKAGE DIMENSIONS



### PACKAGE DIMENSIONS



#### PACKAGE DIMENSIONS



ON Semiconductor and I are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use personse, and reasonable attorney fees andising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized to application engine to the gain of the regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada Fax: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81–3–5773–3850 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative