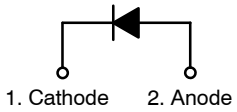
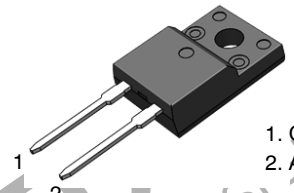
Silicon Carbide (SiC) Schottky Diode – EliteSiC, 10 A, 650 V, D1, TO-220F-2L

FFSPF1065A



Schottky Diode



TO−220 FP / TO−220F−2FS
CASE 221AS

Description

Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size and cost.

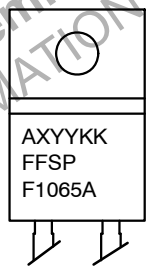
Features

- Max Junction Temperature 175°C
- Avalanche Rated 64 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery / No Forward Recovery
- This Device is Pb−Free and is RoHS Compliant

Applications

- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits

MARKING DIAGRAM



A	= Assembly Plant Code
XYY	= Date Code (Year & Week)
KK	= Lot Traceability Code
FFSPF1065A	= Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

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ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, Unless otherwise specified)

Symbol	Parameter		FFSPF1065A	Unit
V_{RRM}	Peak Repetitive Reverse Voltage		650	V
E_{AS}	Single Pulse Avalanche Energy (Note 1)		64	mJ
I_F	Continuous Rectified Forward Current	@T_C < 110°C	10	A
		@ T_C < 135°C	7.2	
$I_{F, Max}$	Non−Repetitive Peak Forward Surge Current	T_C = 25°C, 10 μs	720	A
		T_C = 150°C, 10 μs	680	A
$I_{F, SM}$	Non−Repetitive Forward Surge Current	Half−Sine Pulse, t_P = 8.3 ms	56	A
$I_{F, RM}$	Repetitive Forward Surge Current	Half−Sine Pulse, t_P = 8.3 ms	28	A
P_{tot}	Power Dissipation	T_C = 25°C	38	W
		T_C = 150°C	6.4	W
T_J, T_{STG}	Operating and Storage Temperature Range		−55 to +175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
1. E_{AS} of 64 mJ is based on starting T_J = 25°C, L = 0.5 mH, I_{AS} = 16 A, V = 50 V.

THERMAL CHARACTERISTICS

Symbol	Parameter	Ratings	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	3.9	°C/W

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Quantity
FFSPF1065A	FFSPF1065A	TO−220 FP / TO−220F−2FS	Tube	N/A	N/A	50 Units

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_F	Forward Voltage	I_F = 10 A, T_C = 25°C	–	1.50	1.75	V
		I_F = 10 A, T_C = 125°C	–	1.60	2.0	
		I_F = 10 A, T_C = 175°C	–	1.72	2.4	
I_R	Reverse Current	V_R = 650 V, T_C = 25°C	–	–	200	μA
		V_R = 650 V, T_C = 125°C	–	–	400	
		V_R = 650 V, T_C = 175°C	–	–	600	
Q_C	Total Capacitance Charge	V = 400 V	–	34	–	nC
C	Total Capacitance	V_R = 1 V, f = 100 kHz	–	575	–	pF
		V_R = 200 V, f = 100 kHz	–	62	–	
		V_R = 400 V, f = 100 kHz	–	47	–	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

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TYPICAL CHARACTERISTICS

(T_J = 25°C UNLESS OTHERWISE NOTED)

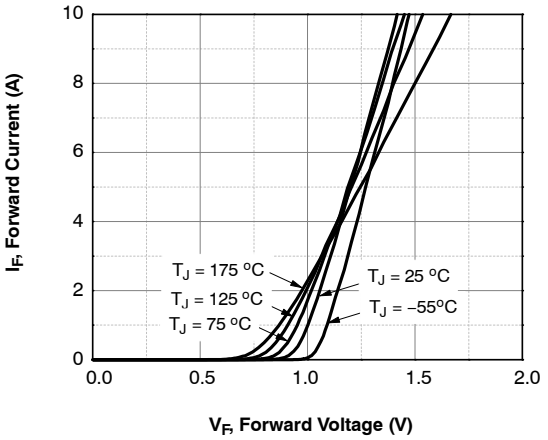


Figure 1. Forward Characteristics

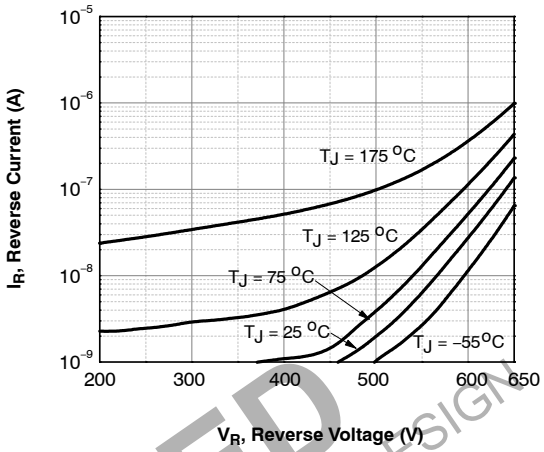


Figure 2. Reverse Characteristics

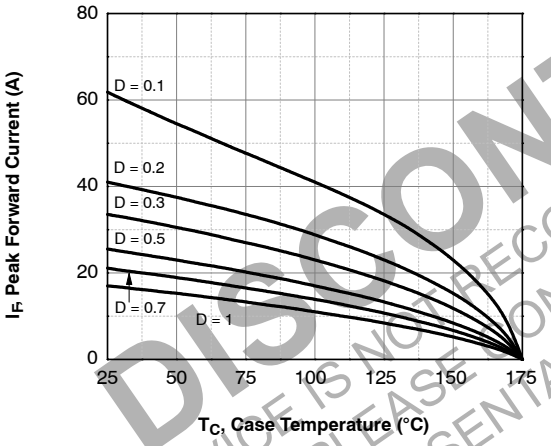


Figure 3. Current Derating

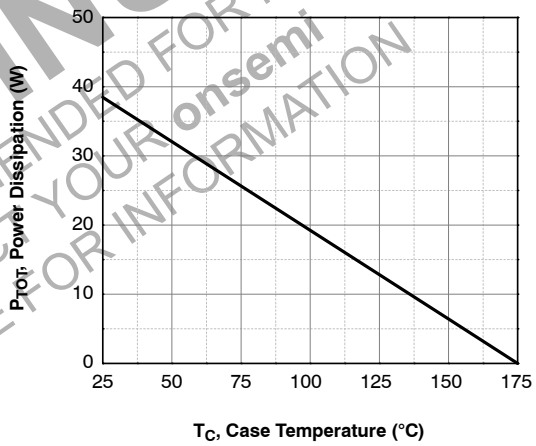


Figure 4. Power Derating

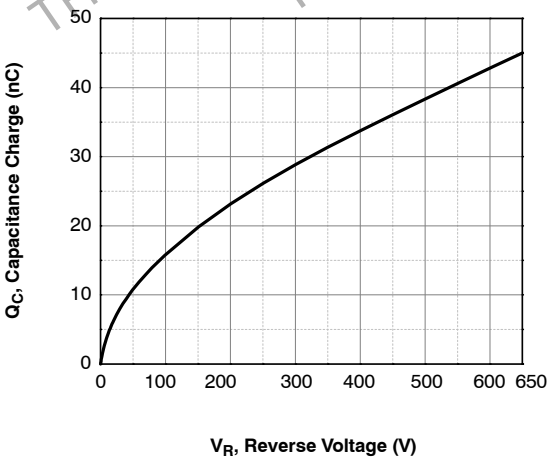


Figure 5. Capacitive Charge vs. Reverse Voltage

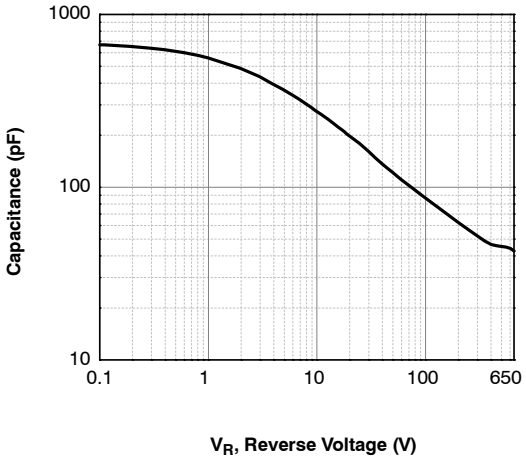


Figure 6. Capacitance vs. Reverse Voltage

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TYPICAL CHARACTERISTICS (CONTINUED)

(T_J = 25°C UNLESS OTHERWISE NOTED)

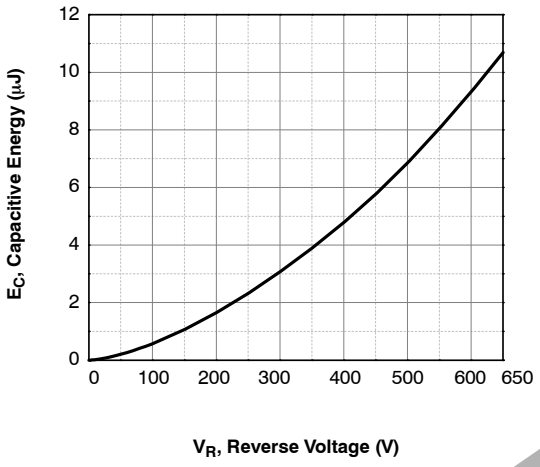


Figure 7. Capacitance Stored Energy

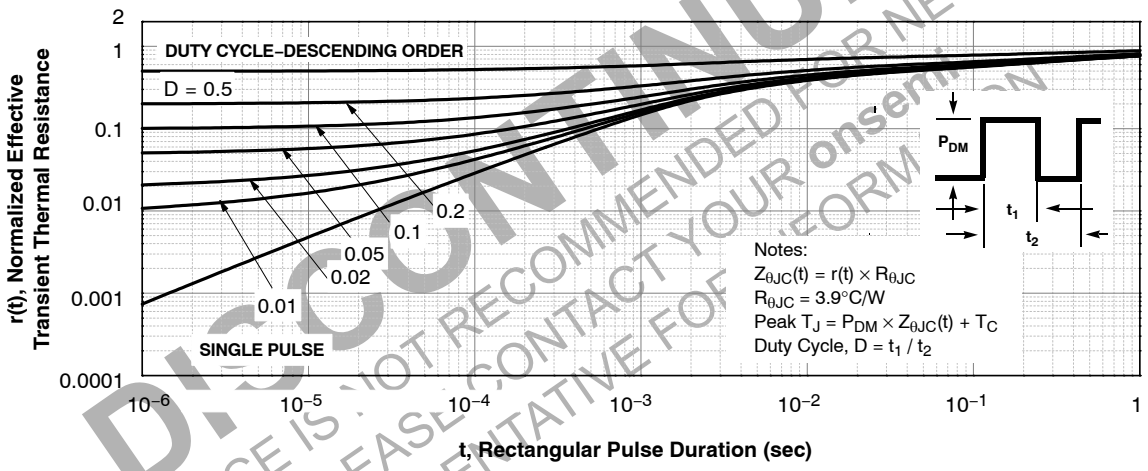


Figure 8. Junction−to−Case Transient Thermal Response Curve

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TEST CIRCUIT AND WAVEFORMS

L = 0.5 mH
R < 0.1 Ω
V_{DD} = 50 V
EAVL = 1/2LI2 [$V_{R(AVL)}$/($V_{R(AVL)}$ − V_{DD})]
Q1 = IGBT (BV_{CES} > DUT $V_{R(AVL)}$)

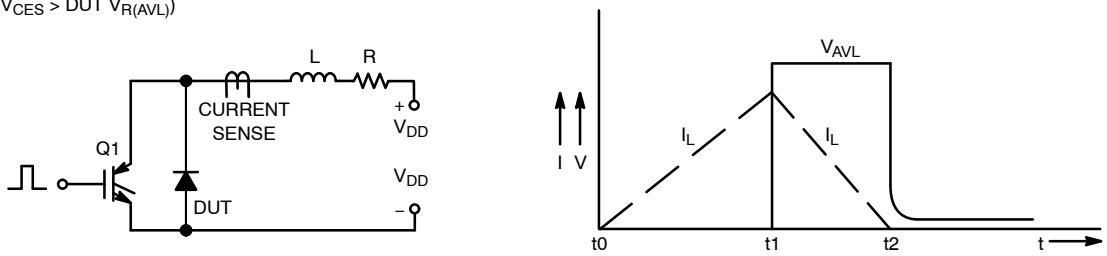
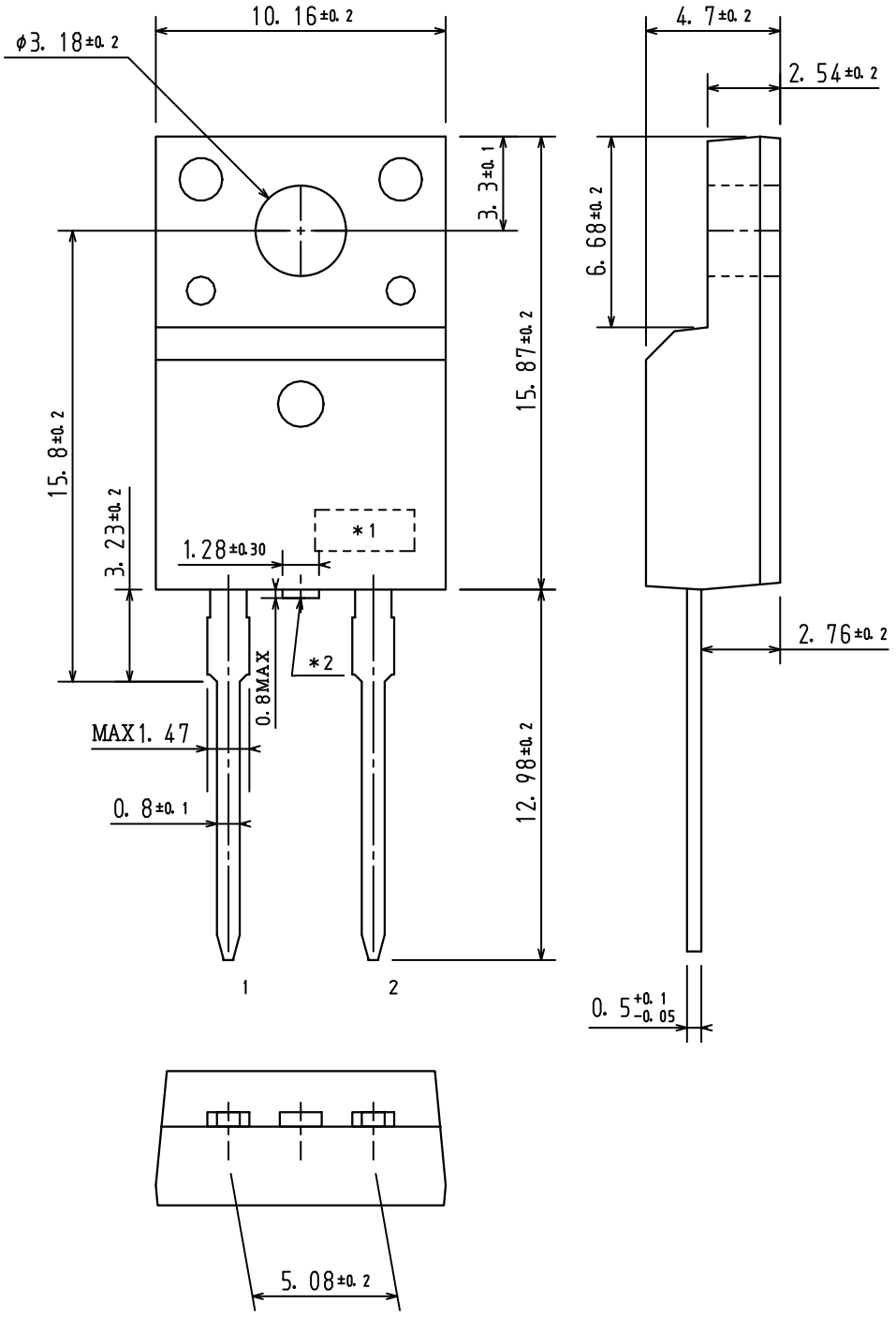


Figure 9. Unclamped Inductive Switching Test Circuit & Waveform

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TO−220 Fullpack, 2−Lead / TO−220F−2FS
CASE 221AS
ISSUE O

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