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<u>Silicon Carbide (SiC)</u> <u>Schottky Diode</u> – EliteSiC, 8 A, 650 V, D1, DPAK

FFSD0865A

Description

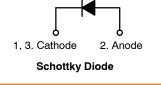
Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size and cost.

Features

- Max Junction Temperature 175°C
- Avalanche Rated 49 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery/No Forward Recovery
- This Device is Pb–Free, Halogen Free/BFR Free and RoHS Compliant

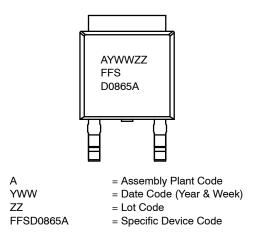
Applications

- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits





MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

FFSD0865A

Symbol	Parameter	FFSD0865A	Unit	
V _{RRM}	Peak Repetitive Reverse Voltage	650	V	
E _{AS}	Single Pulse Avalanche Energy (Note 1)	49	mJ	
١ _F	Continuous Rectified Forward Current @ $T_C < 1$	8	Α	
	Continuous Rectified Forward Current @ $T_C < 13$	15		
I _{F,Max}	Non-Repetitive Peak Forward Surge Current	T _C = 25°C, 10 μs	750	А
		T _C = 150°C, 10 μs	730	А
I _{F,SM}	Non-Repetitive Forward Surge Current	Half-Sine Pulse, t _p = 8.3 ms	49	А
I _{F,RM}	Repetitive Forward Surge CurrentHalf-Sine Pulse, tp = 8.3 ms		28	А
Ptot	Power Dissipation	$T_{\rm C} = 25^{\circ}{\rm C}$	125	W
		T _C = 150°C	21	W
T _J , T _{STG}	Operating and Storage Temperature Range		–55 to +175	°C

Table 1. ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1. E_{AS} of 49 mJ is based on starting $T_J = 25^{\circ}$ C, L = 0.5 mH, $I_{AS} = 14$ A, V = 50 V.

Table 2. THERMAL CHARACTERISTICS

Symbol	Parameter	Rating	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	1.2	°C/W

Table 3. OPERATING CHARACTERISTICS (T_C = 25° C, unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
V _F	Forward Voltage	I _F = 8 A, T _C = 25°C	-	1.50	1.75	V
		I _F = 8 A, T _C = 125°C	-	1.6	2.0	
		I _F = 8 A, T _C = 175°C	-	1.72	2.4	
I _R	Reverse Current	$V_{\rm R}$ = 650 V, $T_{\rm C}$ = 25°C	-	-	200	μΑ
		$V_{\rm R}$ = 650 V, $T_{\rm C}$ = 125°C	-	-	400	
		$V_{\rm R}$ = 650 V, $T_{\rm C}$ = 175°C	-	-	600	
Q _C	Total Capacitive Charge	V = 400 V	-	27	-	nC
С	Total Capacitance	V _R = 1 V, f = 100 kHz	-	463	-	pF
		V _R = 200 V, f = 100 kHz	-	48	-	
		V _R = 400 V, f = 100 kHz	-	38	_	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

PART MARKING AND ORDERING INFORMATION

Part Number	Top Mark	Package	Packing Method	Reel Size [†]	Tape Width	Quantity
FFSD0865A	FFSD0865A	DPAK	N/A	13″	N/A	2500 units

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D

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TYPICAL CHARACTERISTICS

 $(T_J = 25^{\circ}C \text{ UNLESS OTHERWISE NOTED})$

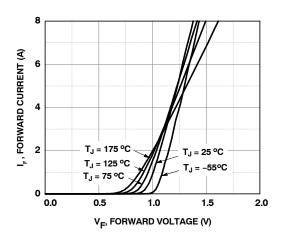


Figure 1. Forward Characteristics

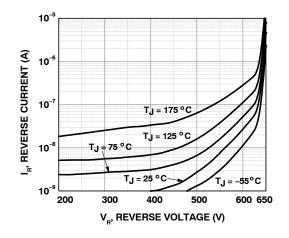
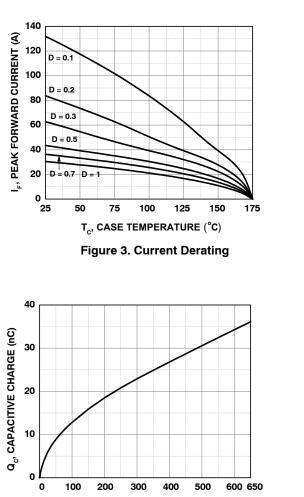


Figure 2. Reverse Characteristics



V_R, REVERSE VOLTAGE (V) Figure 5. Capacitive Charge vs. Reverse Voltage

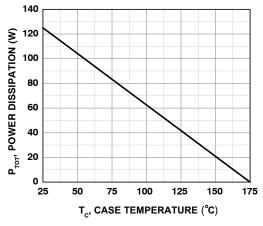


Figure 4. Power Derating

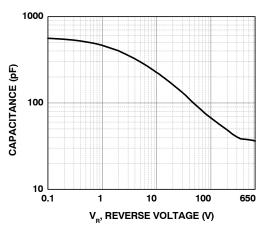


Figure 6. Capacitance vs. Reverse Voltage

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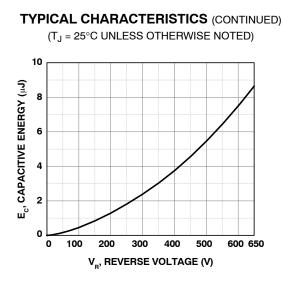


Figure 7. Capacitance Stored Energy

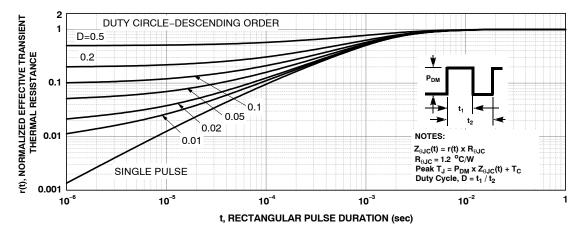


Figure 8. Junction-to-Case Transient Thermal Response Curve

TEST CIRCUIT AND WAVEFORMS

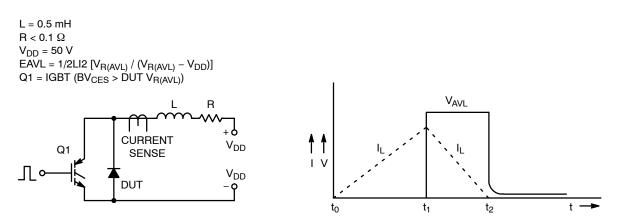
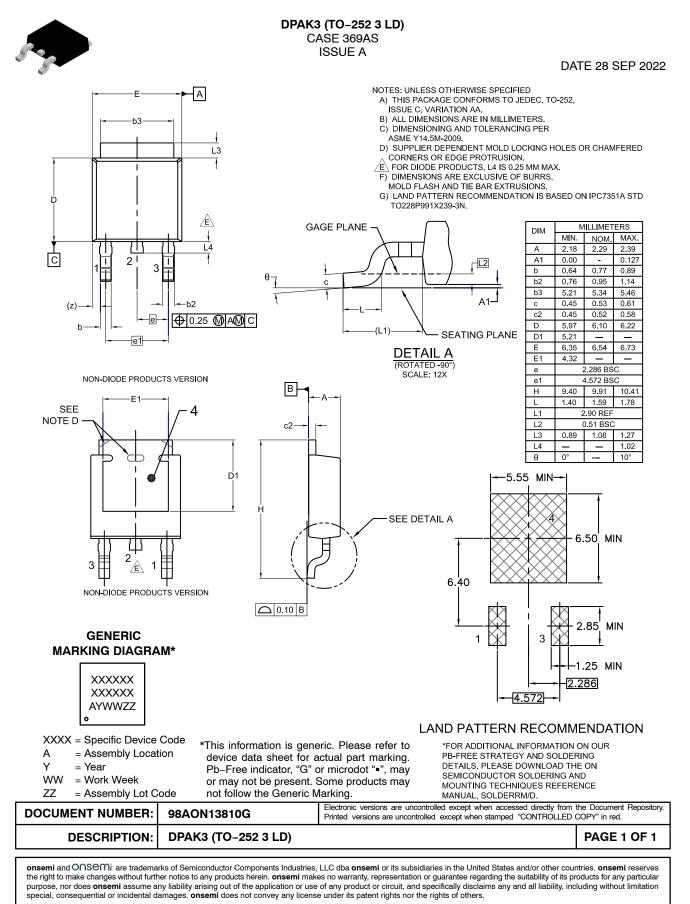


Figure 9. Unclamped Inductive Switching Test Circuit & Waveform

MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

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