

IGBT - Field Stop, Trench

650 V, 75 A

FGH75T65UPD, FGH75T65UPD-F155

Description

Using innovative field stop trench IGBT technology, onsemi's new series of field-stop trench IGBTs offer optimum performance for solar inverter, UPS, welder, and digital power genera-tor where low conduction and switching losses are essential.

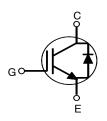
Features

- Maximum Junction Temperature: T_J = 175°C
- Positive Temperature Co-efficient for Easy Parallel Operating
- High Current Capability
- /5 A

 Applications
 Solar Inverter, UPS, Digital Power Generator

 8.2
 8.3
 8.4
 FGH75T65L

 ORi
 See detailed orde dimensions section





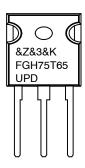
TO-247-3LD CASE 340CK

FGH75T65UPD

TO-247-3LD CASE 340CH

FGH75T65UPD-F155

MARKING DIAGRAMS



= Assembly Plant Code = Numeric Date Code

= Lot Code

FGH75T65UPD = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

ABSOLUTE MAXIMUM RATINGS

Descriptio	Symbol	Ratings	Unit	
Collector to Emitter Voltage		V _{CES}	650	V
Gate to Emitter Voltage		V _{GES}	±20	V
Transient Gate to Emitter Voltage		1	±25	V
Collector Current	T _C = 25°C	I _C	150	А
Collector Current	T _C = 100°C	1 [75	А
Pulsed Collector Current (Note 1)		I _{CM}	225	А
Clamped Inductive Load Current (Note 2)	T _C = 25°C	I _{LM}	225	А
Diode Forward Current	T _C = 25°C	I _F	75	А
Diode Forward Current	T _C = 100°C	1 [50	Α
Pulsed Diode Maximum Forward Current (No	te 1)	I _{FM}	225	Α
Maximum Power Dissipation	T _C = 25°C	P _D	375	W
Maximum Power Dissipation	T _C = 100°C	1 [187	W
Short Circuit Withstand Time	T _C = 25°C	SCWT	5	μs
Operating Junction Temperature		TJ	-55 to +175	°C
Storage Temperature Range		T _{stg}	-55 to +175	°C
Maximum Lead Temp. for Soldering Purposes	s, 1/8" from Case for 5 Seconds	T_L	300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. Repetitive rating: Pulse width limited by max. junction temperature.
- 2. Ic = 225 A, Vce = 400 V, Rg = 10 Ω

THERMAL CHARACTERISTICS

Parameter	Symbol	Тур	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$ (IGBT)	-	0.40	°C/W
Thermal Resistance, Junction to Case	$R_{\theta JC}$ (Diode)	-	0.86	°C/W
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	-	40	°C/W

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FGH75T65UPD	FGH75T65UPD	TO-247-3	Tube	N/A	N/A	30
FGH75T65UPD-F155	FGH75T65UPD	TO-247-3	Tube	N/A	N/A	30

ELECTRICAL CHARACTERISTICS OF THE IGBT ($T_C = 25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Off Characteristics		•				
Collector to Emitter Breakdown Voltage	BV _{CES}	V _{GE} = 0 V, I _C = 1 mA	650	-	-	V
Temperature Coefficient of Breakdown Voltage	$\Delta BV_{CES}/\Delta T_{J}$	V_{GE} = 0 V, I_{C} = 250 μA		0.65		V/°C
Collector Cut-Off Current	I _{CES}	V _{CE} = V _{CES} , V _{GE} = 0 V	-	-	250	μΑ
G-E Leakage Current	I _{GES}	V _{GE} = V _{GES} , V _{CE} = 0 V	-	-	±400	nA
On Characteristics						
G-E Threshold Voltage	V _{GE(th)}	$I_C = 75 \text{ mA}, V_{CE} = V_{GE}$	4.0	6.0	7.5	٧
Collector to Emitter Saturation Voltage	V _{CE(sat)}	I _C = 75 A, V _{GE} = 15 V	-	1.65	2.3	V
		I _C = 75 A, V _{GE} = 15 V, T _C = 175°C	-	2.05	-	V
Dynamic Characteristics		_				
Input Capacitance	C _{ies}	$V_{CE} = 30 \text{ V}, V_{GE} = 0 \text{ V}, f = 1 \text{ MHz}$	-	5665	-	pF
Output Capacitance	C _{oes}		-	205	-	pF
Reverse Transfer Capacitance	C _{res}		-	100	-	pF
Switching Characteristics						
Turn-On Delay Time	t _{d(on)}	$\begin{array}{c} V_{CC} = 400 \text{ V, } I_{C} = 75 \text{ A,} \\ R_{G} = 3 \Omega, V_{GE} = 15 \text{ V,} \\ \text{Inductive Load, } T_{C} = 25^{\circ}\text{C} \end{array}$	-	32	42	ns
Rise Time	t _r		-	43	56	ns
Turn-Off Delay Time	t _{d(off)}		_	166	216	ns
Fall Time	t _f		_	24	33	ns
Turn-On Switching Loss	E _{on}		_	2.85	3.68	mJ
Turn-Off Switching Loss	E _{off}		-	1.20	1.60	mJ
Total Switching Loss	E _{ts}		-	4.05	5.3	mJ
Turn-On Delay Time	t _{d(on)}	V _{CC} = 400 V, I _C = 75 A,	-	30	-	ns
Rise Time	t _r	$R_G = 3 \Omega$, $V_{GE} = 15 V$, Inductive Load, $T_C = 175^{\circ}C$	-	57	-	ns
Turn-Off Delay Time	t _{d(off)}		-	176	-	ns
Fall Time	t _f		-	21	-	ns
Turn-On Switching Loss	E _{on}		-	4.45	-	mJ
Turn-Off Switching Loss	E _{off}		_	1.60	-	mJ
Total Switching Loss	E _{ts}		-	6.05	-	mJ
Short Circuit Withstand Time	Tsc	$V_{GE} = 15 \text{ V}, V_{CC} \le 400 \text{ V}, Rg = 10 \Omega$	5	_	-	μS
Total Gate Charge	Qg	V _{CE} = 400 V, I _C = 75 A, V _{GE} = 15 V	-	385	578	nC
Gate to Emitter Charge	Q _{ge}		_	45	68	nC
Gate to Collector Charge	Q _{gc}		-	210	315	nC

ELECTRICAL CHARACTERISTICS OF THE DIODE (T_C = 25°C unless otherwise noted)

Parameter	Symbol	Test Conditions		Min	Тур	Max	Unit
Diode Forward Voltage	V _{FM}	I _F = 50 A	T _C = 25°C	_	2.1	2.6	V
			T _C = 175°C	_	1.7	-	
Reverse Recovery Energy	E _{rec}	I _F = 50 A, di _F /dt = 200 A/μs	T _C = 175°C	_	40	-	μJ
Diode Reverse Recovery Time	t _{rr}		T _C = 25°C	_	65	85	ns
			T _C = 175°C	_	127	-	
Diode Reverse Recovery Charge	Q _{rr}		T _C = 25°C	_	120	170	nC
			T _C = 175°C	-	550	_	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL PERFORMANCE CHARACTERISTICS

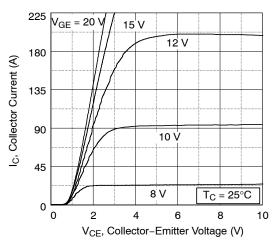


Figure 1. Typical Output Characteristics

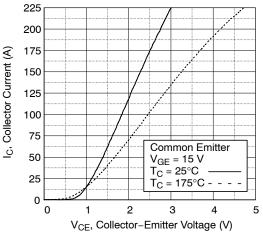


Figure 3. Typical Saturation Voltage Characteristics

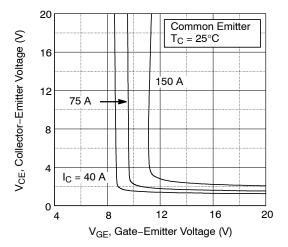


Figure 5. Saturation Voltage vs. V_{GE}

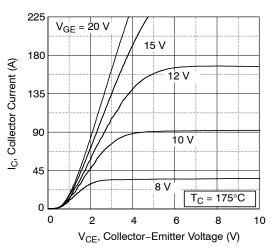


Figure 2. Typical Output Characteristics

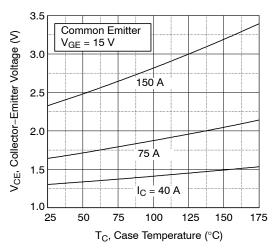


Figure 4. Saturation Voltage vs. Case Temperature at Variant Current Level

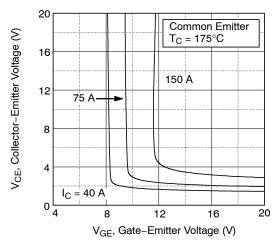


Figure 6. Saturation Voltage vs. V_{GE}

TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUED)

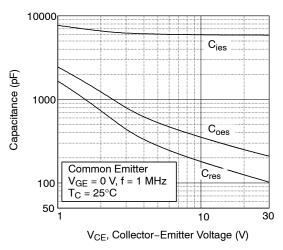


Figure 7. Capacitance Characteristics

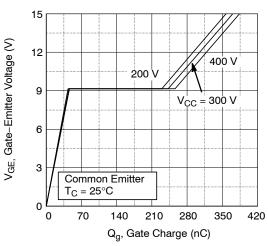


Figure 8. Gate Charge Characteristics

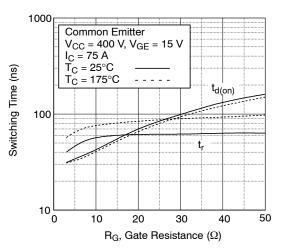


Figure 9. Turn-On Characteristics vs. Gate Resistance

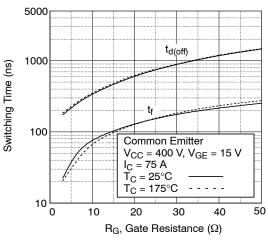


Figure 10. Turn-Off Characteristics vs. Gate Resistance

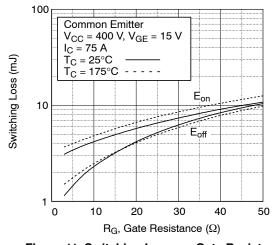


Figure 11. Switching Loss vs. Gate Resistance

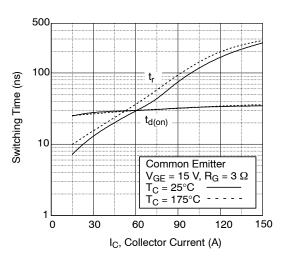


Figure 12. Turn-On Characteristics vs. Collector Current

TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUED)

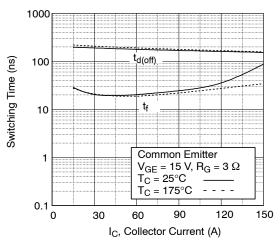


Figure 13. Turn-Off Characteristics vs. Collector Current

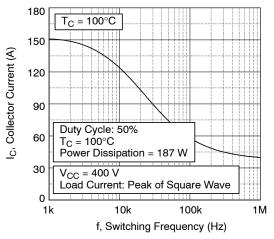


Figure 15. Load Current vs. Frequency

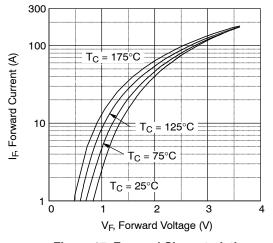


Figure 17. Forward Characteristics

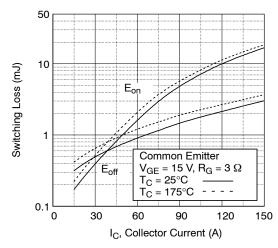


Figure 14. Switching Loss vs. Collector Current

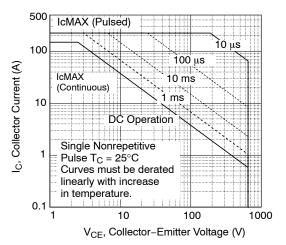


Figure 16. SOA Characteristics

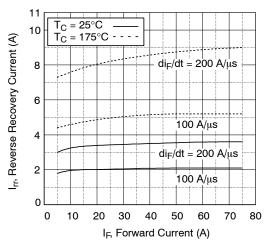


Figure 18. Reverse Recovery Current

TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUED)

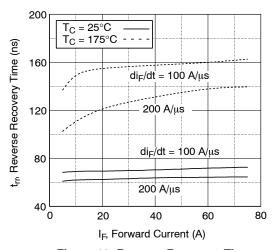


Figure 19. Reverse Recovery Time

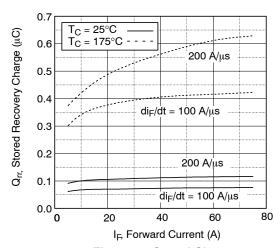


Figure 20. Stored Charge

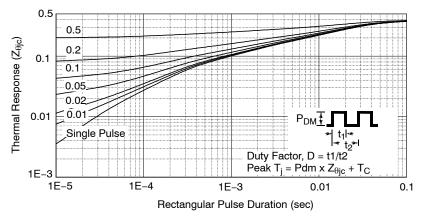


Figure 21. Transient Thermal Impedance of IGBT

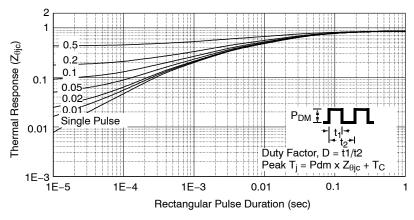
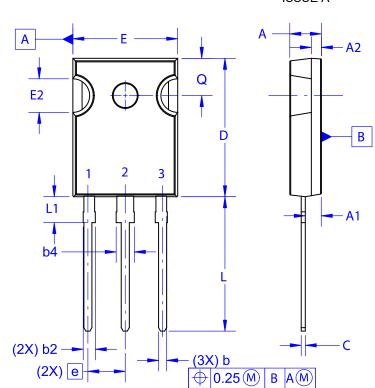


Figure 22. Transient Thermal Impedance of Diode

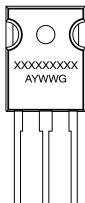
TO-247-3LD CASE 340CH **ISSUE A**





- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
 D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC **MARKING DIAGRAM***



XXXX = Specific Device Code

= Assembly Location

WW = Work Week

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

	DATE 0	9 OCT 2019
Ø P —		Ø P1 D2
S E1 —		D1
	2	
		y

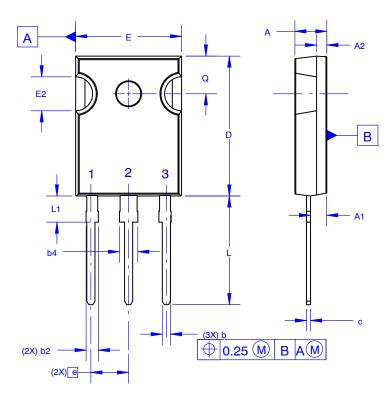
DIM	MILLIMETERS			
DIM	MIN	NOM	MAX	
Α	4.58	4.70	4.82	
A 1	2.29	2.475	2.66	
A2	1.40	1.50	1.60	
D	20.32	20.57	20.82	
Е	15.37	15.62	15.87	
E2	4.96	5.08	5.20	
e	~	5.56	~	
L	19.75	20.00	20.25	
L1	3.69	3.81	3.93	
ØΡ	3.51	3.58	3.65	
Q	5.34	5.46	5.58	
S	5.34	5.46	5.58	
b	1.17	1.26	1.35	
b2	1.53	1.65	1.77	
b4	2.42	2.54	2.66	
С	0.51	0.61	0.71	
D1	13.08	~	~	
D2	0.51	0.93	1.35	
E1	12.81	~	~	
ØP1	6.61	6.73	6.85	

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TO-247-3LD SHORT LEAD

CASE 340CK ISSUE A



NOTES: UNLESS OTHERWISE SPECIFIED.

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- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code

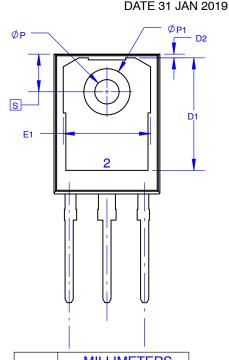
A = Assembly Location

Y = Year

WW = Work Week

ZZ = Assembly Lot Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



DIM	MIL	LIMET	ERS
DIIVI	MIN	NOM	MAX
Α	4.58	4.70	4.82
A1	2.20	2.40	2.60
A2	1.40	1.50	1.60
b	1.17	1.26	1.35
b2	1.53	1.65	1.77
b4	2.42	2.54	2.66
С	0.51	0.61	0.71
D	20.32	20.57	20.82
D1	13.08	~	~
D2	0.51	0.93	1.35
E	15.37	15.62	15.87
E1	12.81	~	~
E2	4.96	5.08	5.20
е	~	5.56	~
L	15.75	16.00	16.25
L1	3.69	3.81	3.93
ØΡ	3.51	3.58	3.65
Ø P1	6.60	6.80	7.00
Q	5.34	5.46	5.58
S	5.34	5.46	5.58

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