IGBT

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop (FS) Trench construction, and provides superior performance in demanding switching applications, offering both low on-state voltage and minimal switching loss. The IGBT is well suited for resonant or soft switching applications. Incorporated into the device is a rugged co-packaged free wheeling diode with a low forward voltage.

Features

- Low Saturation Voltage using Trench with Field Stop Technology
- Low Switching Loss Reduces System Power Dissipation
- Optimized for Low Case Temperature in IH Cooker Application
- Low Gate Charge
- These are Pb–Free Devices

Typical Applications

- Inductive Heating
- Consumer Appliances
- Soft Switching

ABSOLUTE MAXIMUM RATINGS

Symbol	Value	Unit
V _{CES}	1200	V
Ι _c	60 30	A
I _{CM}	200	A
I _F	60 30	A
I _{FM}	200	A
V_{GE}	±20	V
P _D	192 77	W
ТJ	–55 to +150	°C
T _{stg}	–55 to +150	°C
T _{SLD}	260	°C
	V _{CES} I _C I _{CM} I _F I _{FM} V _{GE} P _D T _J T _{stg}	$\begin{tabular}{ c c c c c } \hline V_{CES} & 1200 \\ \hline I_C & 60 \\ \hline 30 & 200 \\ \hline I_{CM} & 200 \\ \hline I_F & 60 \\ \hline 30 & 30 \\ \hline I_F & 200 \\ \hline V_{GE} & \pm 20 \\ \hline V_{GE} & \pm 20 \\ \hline P_D & 192 \\ \hline 77 & 7J & -55 \ to +150 \\ \hline T_{stg} & -55 \ to +150 \\ \hline \end{tabular}$

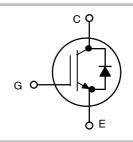
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

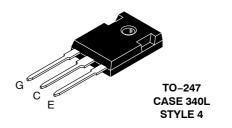


ON Semiconductor®

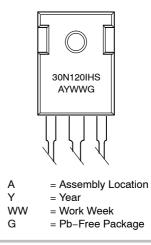
http://onsemi.com

30 A, 1200 V V_{CEsat} = 2.00 V E_{off} = 1.0 mJ





MARKING DIAGRAM



ORDERING INFORMATION

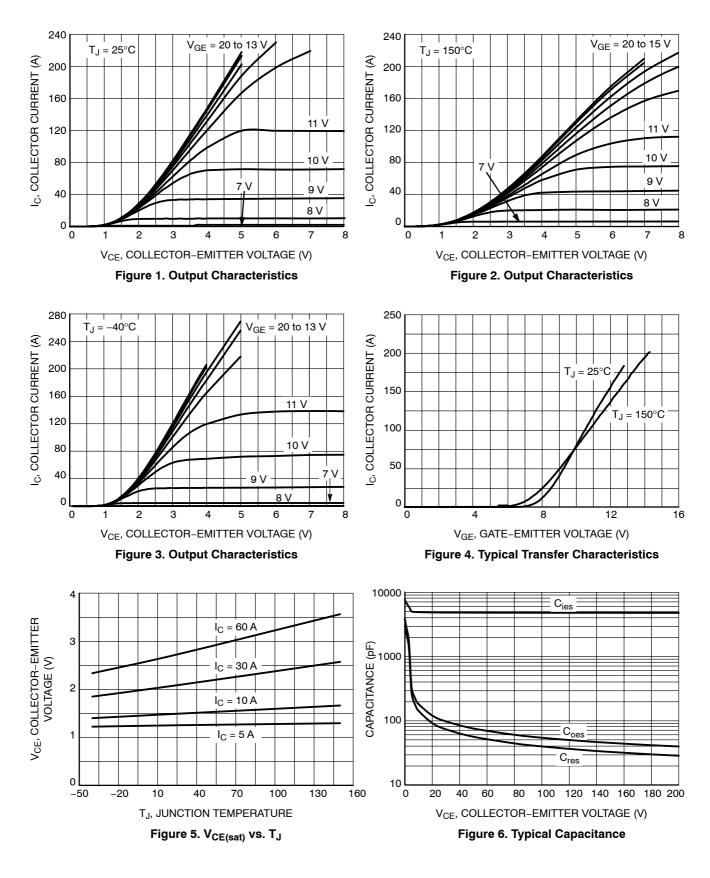
Device	Package	Shipping
NGTB30N120IHSWG	TO-247 (Pb-Free)	30 Units / Rail

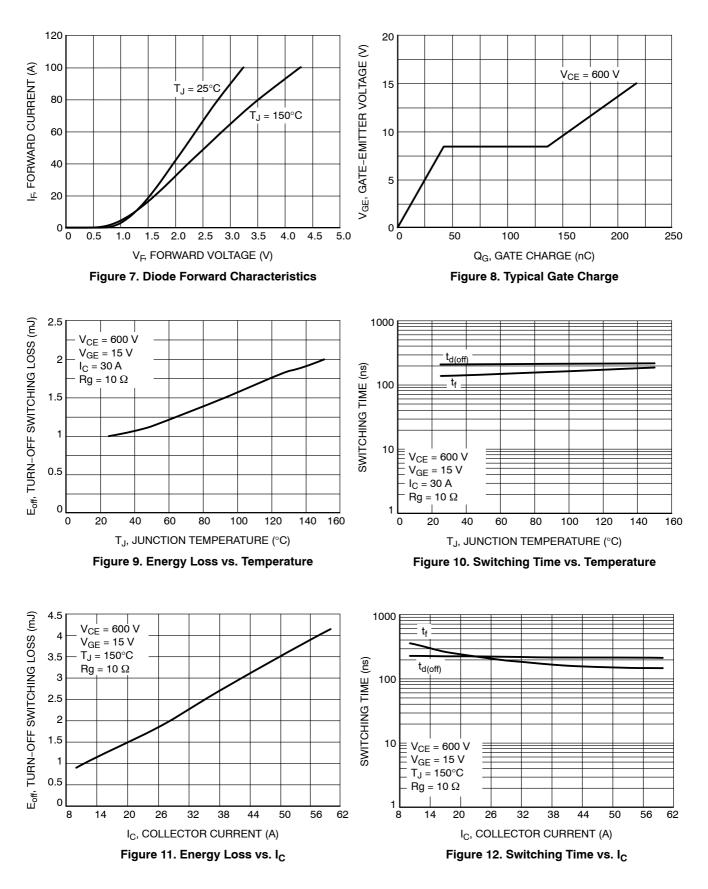
THERMAL CHARACTERISTICS

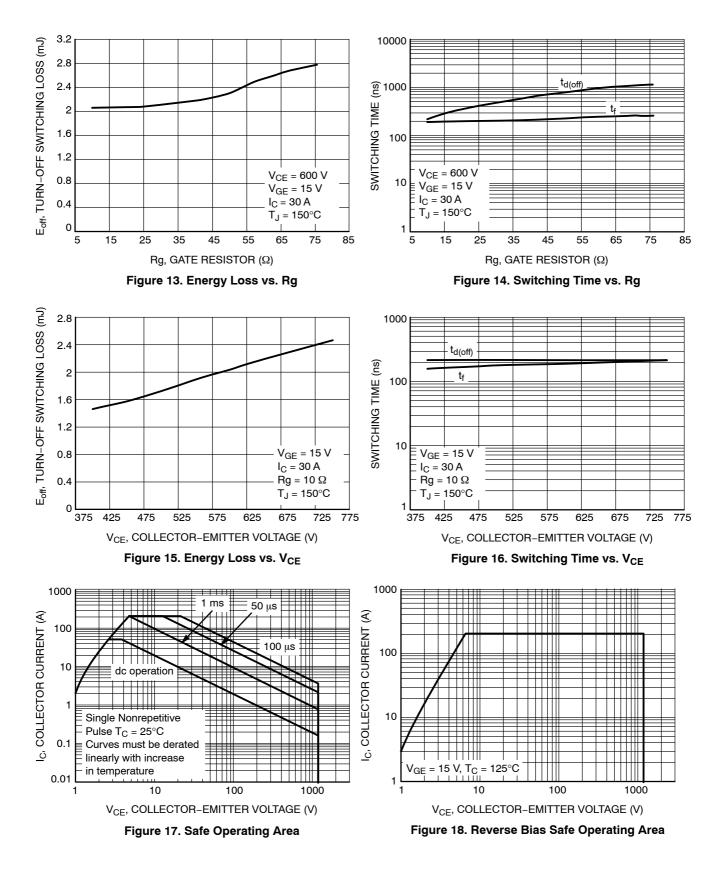
Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{ ext{ heta}JC}$	0.65	°C/W
Thermal resistance junction-to-case, for Diode	$R_{ ext{ heta}JC}$	2.0	°C/W
Thermal resistance junction-to-ambient	$R_{ hetaJA}$	40	°C/W

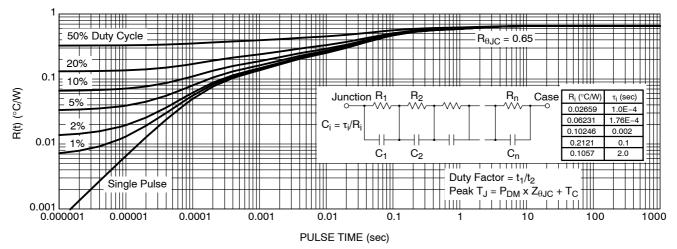
ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC	·					
Collector-emitter breakdown voltage, gate-emitter short-circuited	V_{GE} = 0 V, I _C = 500 μ A	V _{(BR)CES}	1200	_	-	V
Collector-emitter saturation voltage	V_{GE} = 15 V, I _C = 30 A V_{GE} = 15 V, I _C = 30 A, T _J = 150°C	V _{CEsat}		2.0 2.6	2.4 _	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}, I_C = 250 \ \mu A$	V _{GE(th)}	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate- emitter short-circuited	$V_{GE} = 0 V, V_{CE} = 1200 V$ $V_{GE} = 0 V, V_{CE} = 1200 V, T_{J=} 150^{\circ}C$	I _{CES}		_ _	0.5 2.0	mA
Gate leakage current, collector-emitter short-circuited	V_{GE} = 20 V, V_{CE} = 0 V	I _{GES}	-	-	100	nA
DYNAMIC CHARACTERISTIC	·					
Input capacitance		C _{ies}	-	5300	-	pF
Output capacitance	V_{CE} = 20 V, V_{GE} = 0 V, f = 1 MHz	C _{oes}	-	125	_	
Reverse transfer capacitance		C _{res}	-	95	-	
Gate charge total		Qg	-	220	-	nC
Gate to emitter charge	V_{CE} = 600 V, I _C = 30 A, V _{GE} = 15 V	Q _{ge}	-	42	-	
Gate to collector charge		Q _{gc}	-	95	-	
SWITCHING CHARACTERISTIC, INDUCT	TIVE LOAD					
Turn-off delay time	$T_{J} = 25^{\circ}C$	t _{d(off)}	-	210	-	ns
Fall time	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 30 \text{ A}$ $\text{R}_{g} = 10 \Omega$	t _f	-	140	-	
Turn-off switching loss	V _{GE} = 0 V/ 15V	E _{off}	-	1.0	-	mJ
Turn-off delay time	T _J = 125°C	t _{d(off)}	-	215	-	ns
Fall time	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 30 \text{ A}$ $R_{a} = 10 \Omega$	t _f	-	175	-	
Turn-off switching loss	V _{GE} = 0 V/ 15V	E _{off}	-	1.8	-	mJ
DIODE CHARACTERISTIC						
Forward voltage	V_{GE} = 0 V, I _F = 30 A V _{GE} = 0 V, I _F = 30 A, T _J = 150°C	V _F	_	1.8 2.0	2.0 _	V











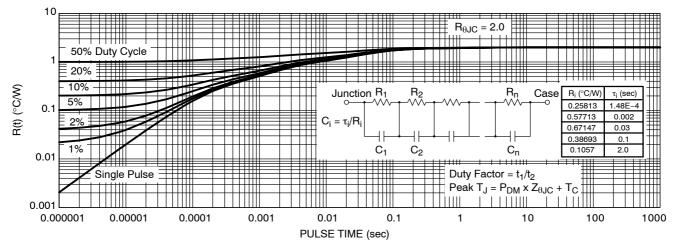


Figure 20. Diode Transient Thermal Impedance

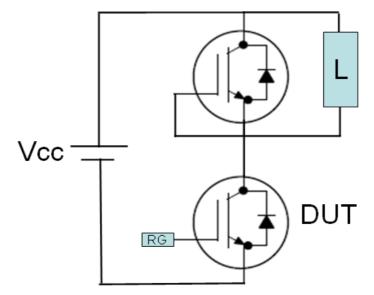
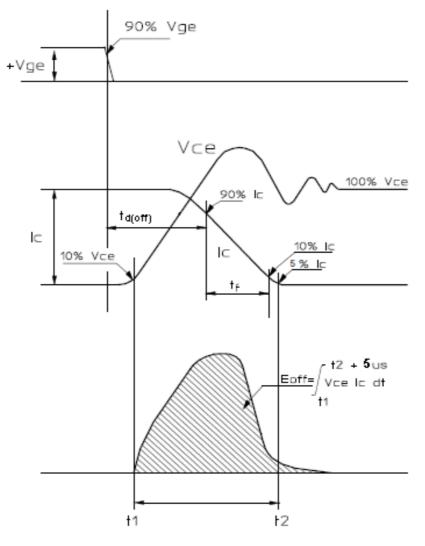
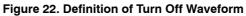


Figure 21. Test Circuit for Switching Characteristics





MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

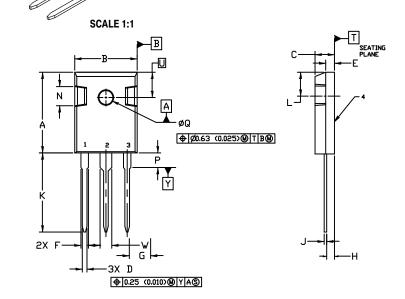
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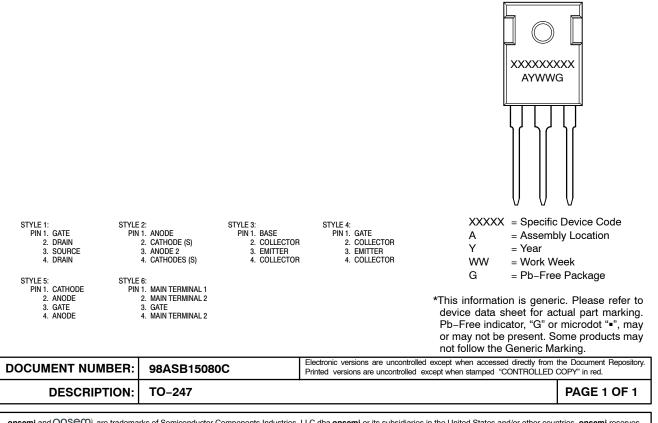


- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: MILLIMETER



	MILLIMETERS		INC	HES
DIM	MIN.	MAX.	MIN.	MAX.
Α	20.32	21.08	0.800	0.830
В	15.75	16.26	0.620	0.640
С	4.70	5.30	0.185	0.209
D	1.00	1.40	0.040	0.055
E	1.90	2.60	0.075	0.102
F	1.65	2.13	0.065	0.084
G	5.45	BSC	0.215	BSC
Н	1.50	2.49	0.059	0.098
J	0.40	0.80	0.016	0.031
к	19.81	20.83	0.780	0.820
L	5.40	6.20	0.212	0.244
N	4.32	5.49	0.170	0.216
Р		4.50		0.177
Q	3.55	3.65	0.140	0.144
U	6.15	6.15 BSC		BSC
V	2.87	3.12	0.113	0.123

GENERIC **MARKING DIAGRAM***



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