NUP5150MU

ESD Protection Diode Array, 5-Line

This 5-line surge protection array is designed for applications requiring surge protection capability. It is intended for use in over-transient voltage and ESD sensitive equipment such as cell phones, portables, computers, printers and other applications. This device features a monolithic common anode design which protects five independent lines in a single UDFN package. This device is ideal for situations where board space is at a premium.

Features

- Protects up to 5 Lines in a Single UDFN Package
- ESD Rating of Class 3B (Exceeding 8 kV) per Human Body Model
- Compliance with IEC 61000-4-2
- This is a Pb–Free Device

Applications

- Hand Held Portable Applications
- Serial and Parallel Ports
- Notebooks, Desktops, Servers

MAXIMUM RATINGS (T_J = 25°C, unless otherwise specified)

Symbol	Rating	Value	Unit
TJ	Operating Junction Temperature Range	-40 to 125	°C
T _{STG}	Storage Temperature Range	–55 to 150	°C
ΤL	Lead Solder Temperature – Maximum (10 seconds)	260	°C
ESD	Human Body Model (HBM) IEC 61000-4-2 Contact (ESD)	16000 8000	V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

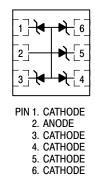


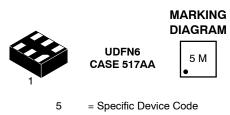
ON Semiconductor®

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UDFN6 5-LINE SURGE PROTECTION

PIN ASSIGNMENT





M = Month Code

*Specific Device Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

	Device	Package	Shipping [†]
١	NUP5150MUTBG	UDFN6 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

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Parameter	Conditions	Symbol	Min	Тур	Max	Unit
Reverse Working Voltage	(Note 1)	V _{RWM}		-	5.0	V
Breakdown Voltage	I _T = 1 mA, (Note 2)	V _{BR}	6.2	6.8	7.2	V
Reverse Leakage Current	V _{RWM} = 3 V	I _R	-		0.1	μΑ
Capacitance	$V_R = 0 V$, f = 1 MHz (Line to GND)	CJ	-	12	15	pF

Surge protection devices are normally selected according to the working peak reverse voltage (V_{RWM}), which should be equal or greater than the DC or continuous peak operating voltage level.
V_{BR} is measured at pulse test current I_T.

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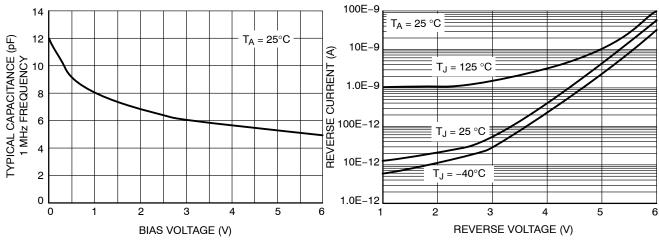


Figure 1. Capacitance

Figure 2. Typical Reverse Current vs. Reverse Voltage

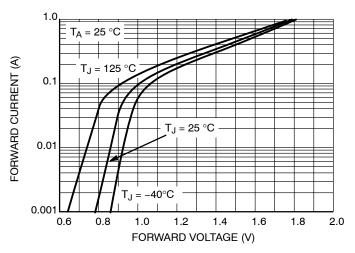
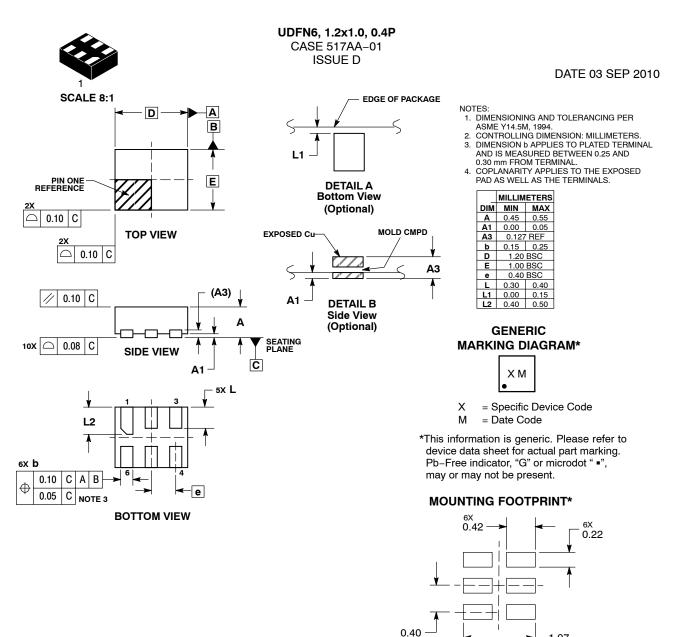


Figure 3. Typical Forward Current vs. Forward Voltage





DIMENSIONS: MILLIMETERS

1.07

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PITCH

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DESCRIPTION:	6 PIN UDFN, 1.2X1.0, 0.4P		PAGE 1 OF 1		
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