Power MOSFET

30 V, 7.8 A, Single N-Channel, 2x2 mm WDFN Package

Features

- WDFN Package Provides Exposed Drain Pad for Excellent Thermal Conduction
- 2x2 mm Footprint Same as SC-88
- Lowest R_{DS(on)} in 2x2 mm Package
- 1.8 V R_{DS(on)} Rating for Operation at Low Voltage Logic Level Gate Drive
- Low Profile (< 0.8 mm) for Easy Fit in Thin Environments
- This is a Pb-Free Device

Applications

- DC-DC Conversion
- Boost Circuits for LED Backlights
- Optimized for Battery and Load Management Applications in Portable Equipment such as, Cell Phones, PDA's, Media Players, etc.
- Low Side Load Switch

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter		Symbol	Value	Unit												
Drain-to-Source Voltage			V_{DSS}	30	V											
Gate-to-Source Voltage	je		V_{GS}	±8.0	V											
Continuous Drain	Steady	T _A = 25°C	I _D	6.0	Α											
Current (Note 1)	State	T _A = 85°C		4.4												
	t ≤ 5 s	T _A = 25°C		7.8												
Power Dissipation (Note 1)	Steady State	T _A = 25°C	P _D	1.92	W											
	t ≤ 5 s			3.3												
Continuous Drain		T _A = 25°C	I _D	3.6	Α											
Current (Note 2)	Steady	,	,	,	,	,	,	,	,	,	,	,	T _A = 85°C		2.6	
Power Dissipation (Note 2)	State	T _A = 25°C	P _D	0.70	W											
Pulsed Drain Current	t _p = 10 μs		I _{DM}	28	Α											
Operating Junction and Storage Temperature			T _J , T _{STG}	–55 to 150	°C											
Source Current (Body Diode) (Note 2)			I _S	3.0	Α											
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			TL	260	°C											

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

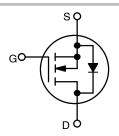
- Surface Mounted on FR4 Board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces).
- Surface Mounted on FR4 Board using the minimum recommended pad size of 30 mm2, 2 oz Cu.



ON Semiconductor®

www.onsemi.com

V _{(BR)DSS}	R _{DS(on)} MAX	I _D MAX (Note 1)
	35 m Ω @ 4.5 V	
30 V	45 mΩ @ 2.5 V	7.8 A
	55 mΩ @ 1.8 V	



N-CHANNEL MOSFET





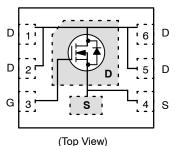
JB = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

PIN CONNECTIONS



ORDERING INFORMATION

Device	Package	Shipping [†]
NTLJS4159NT1G	WDFN6 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Ambient - Steady State (Note 3)	$R_{ hetaJA}$	65	
Junction-to-Ambient – $t \le 5$ s (Note 3)	$R_{ heta JA}$	38	°C/W
Junction-to-Ambient - Steady State Min Pad (Note 4)	$R_{ heta JA}$	180	

- Surface Mounted on FR4 Board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces).
 Surface Mounted on FR4 Board using the minimum recommended pad size (30 mm², 2 oz Cu).

MOSFET ELECTRICAL CHARACTERISTICS (T_{.1} = 25°C unless otherwise noted)

Parameter	Symbol	Test Conditions		Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		30			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I_D = 250 μ A, Ref to 25°C			20		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}		$T_J = 25^{\circ}C$			1.0	μΑ
		$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$	$T_J = 65^{\circ}C$			1.0	
			$T_J = 85^{\circ}C$			5.0	
Gate-to-Source Leakage Current	I _{GSS}	$V_{DS} = 0 V$, $V_{GS} = \pm$	8.0 V			±100	nA
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D = 25$	50 μΑ	0.4	0.7	1.0	V
Negative Gate Threshold Temperature Coefficient	V _{GS(TH)} /T _J				3.18		mV/°C
Drain-to-Source On-Resistance	R _{DS(on)}	$V_{GS} = 4.5, I_D = 2.5$.0 A		20.3	35	mΩ
		$V_{GS} = 2.5, I_D = 2.5$.0 A		25.8	45	1
		V _{GS} = 1.8, I _D = 1.8 A			35.2	55	
Forward Transconductance	9 _{FS}	V _{DS} = 16 V, I _D = 2.0 A			5.3		S
CHARGES, CAPACITANCES AND GA	TE RESISTANO	CE					
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1.0 MHz, V _{DS} = 15 V			1045		pF
Output Capacitance	C _{OSS}				115.5		
Reverse Transfer Capacitance	C _{RSS}				45.3		
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = 4.5 \text{ V}, V_{DS} = 15 \text{ V},$ $I_D = 2.0 \text{ A}$			12.1	13	nC
Threshold Gate Charge	Q _{G(TH)}				1.2		1
Gate-to-Source Charge	Q_{GS}				1.9		
Gate-to-Drain Charge	Q_{GD}				2.7		
Gate Resistance	R_{G}				3.65		Ω
SWITCHING CHARACTERISTICS (No	ote 6)						
Turn-On Delay Time	t _{d(ON)}				6.8		ns
Rise Time	t _r	V _{GS} = 4.5 V, V _{DD} =	15 V,		12.4		
Turn-Off Delay Time	t _{d(OFF)}	$I_D = 2.0 \text{ A}, R_G = 3.0 \Omega$			26		
Fall Time	t _f				5.1		
DRAIN-SOURCE DIODE CHARACTE	RISTICS						- -
Forward Recovery Voltage	V_{SD}	V 0V/10 00:	T _J = 25°C		0.71	1.2	T ,,
		$V_{GS} = 0 \text{ V, IS} = 2.0 \text{ A}$	T _J = 125°C		0.58	3 V	\ \
Reverse Recovery Time	t _{RR}				15	35	
Charge Time	ta	V_{GS} = 0 V, d_{ISD}/d_t = 100 A/ μ s, I_S = 1.0 A			9.0		ns
Discharge Time	t _b				6.0		
Reverse Recovery Time	Q _{RR}				7.0		nC

- 5. Pulse Test: Pulse Width \leq 300 $\mu s,$ Duty Cycle \leq 2%.
- 6. Switching characteristics are independent of operating junction temperatures.

TYPICAL PERFORMANCE CURVES (T $_{J}$ = 25°C unless otherwise noted)

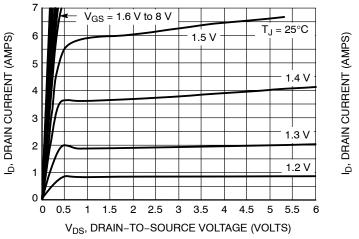
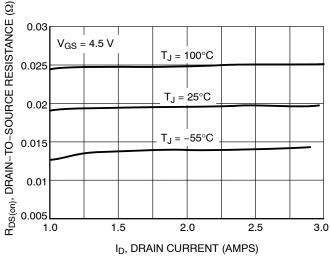


Figure 1. On-Region Characteristics

Figure 2. Transfer Characteristics



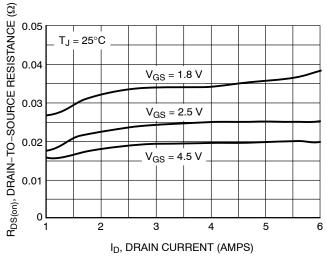
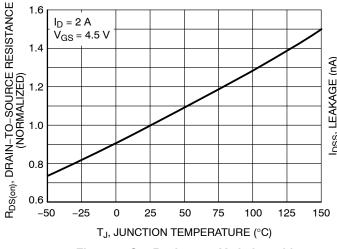


Figure 3. On-Resistance versus Drain Current

Figure 4. On-Resistance versus Drain Current and Gate Voltage



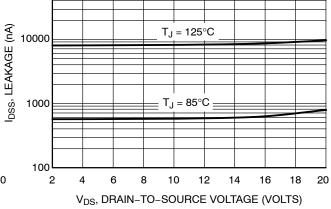


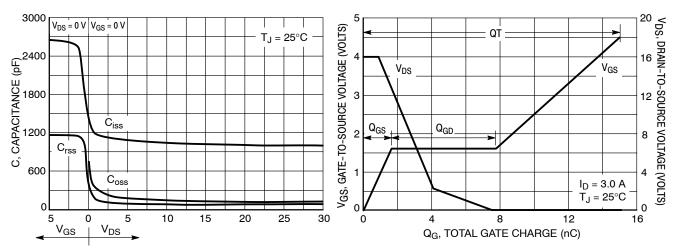
Figure 5. On–Resistance Variation with Temperature

Figure 6. Drain-to-Source Leakage Current versus Voltage

100000

 $V_{GS} = 0 V$

TYPICAL PERFORMANCE CURVES ($T_J = 25^{\circ}C$ unless otherwise noted)



GATE-TO-SOURCE OR DRAIN-TO-SOURCE VOLTAGE (VOLTS)

Figure 7. Capacitance Variation

Figure 8. Gate-To-Source and Drain-To-Source Voltage versus Total Charge

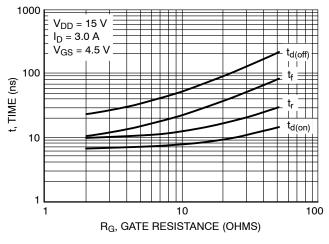


Figure 9. Resistive Switching Time Variation versus Gate Resistance

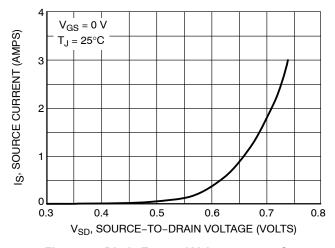


Figure 10. Diode Forward Voltage versus Current

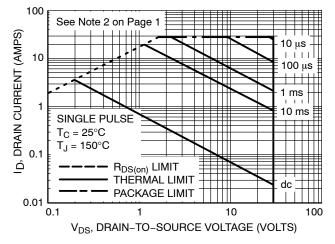


Figure 11. Maximum Rated Forward Biased Safe Operating Area

TYPICAL PERFORMANCE CURVES ($T_J = 25^{\circ}C$ unless otherwise noted)

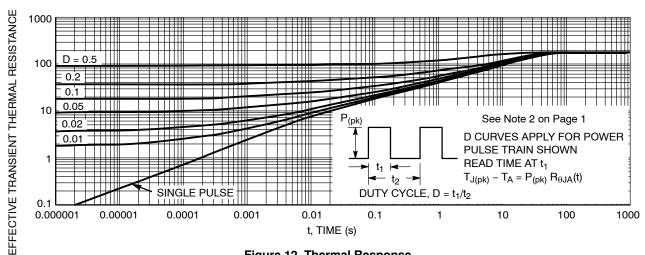


Figure 12. Thermal Response

Α

4X е

b1 6X

Ф

NOTE 5

0.10

b 6X

Ф

3 Ш

 \Box

PIN 1. COLLECTOR 2. COLLECTOR

EMITTER COLLECTOR

COLLECTOR

3. BASE

山口

BOTTOM VIEW

STYLE 2:

5.

В

Ε

C SEATING

0.10 C A

CAB

NOTE 3

С 0.05

0.05

С

В





SCALE 4:1

PIN ONE REFERENCE

2X 🗀 0.10

0.10 C

С

D2

7X \alpha 0.08

6X L

E2

STYLE 1:

5. DRAIN

PIN 1. DRAIN DRAIN 2. GATE

SOURCE

DRAIN 6.

0.10 C

WDFN6 2x2 CASE 506AP-01 **ISSUE B**

DATE 26 APR 2006

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20mm FROM TERMINAL.
- COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.
- CENTER TERMINAL LEAD IS OPTIONAL. TERMINAL LEAD IS CONNECTED TO TERMINAL LEAD # 4.
- 2. PINS 1, 2, 5 AND 6 ARE TIED TO THE FLAG.

	MILLIMETERS			
DIM	MIN	MAX		
Α	0.70	0.80		
A1	0.00	0.05		
A3	0.20	REF		
b	0.25	0.35		
b1	0.51	0.61		
D	2.00) BSC		
D2	1.00	1.20		
E	2.00 BSC			
E2	1.10	1.30		
е	0.65	BSC		
K	0.15 REF			
L	0.20	0.30		
L2	0.20	0.30		
J	0.27 REF			
J1	0.65 REF			

GENERIC MARKING DIAGRAM*

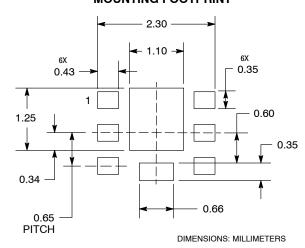


XX = Specific Device Code

= Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present.

SOLDERMASK DEFINED MOUNTING FOOTPRINT



1	DOCUMENT NUMBER		Electronic versions are uncontrolled except when accessed directly from	the Document Repository.	
	DOCUMENT NUMBER:	98AON20860D	Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
	DESCRIPTION:	6 PIN WDFN 2X2, 0.65P		PAGE 1 OF 1	

ON Semiconductor and (III) are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.



onsemi, ONSEMI, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT: Email Requests to: orderlit@onsemi.com

onsemi Website: www.onsemi.com

TECHNICAL SUPPORT North American Technical Support: Voice Mail: 1 800–282–9855 Toll Free USA/Canada

Phone: 011 421 33 790 2910

Europe, Middle East and Africa Technical Support:

Phone: 00421 33 790 2910

For additional information, please contact your local Sales Representative