## BCX17LT1G, PNP BCX18LT1G, PNP BCX19LT1G, NPN SBCX19LT1G, NPN



## **Voltage and Current are Negative for PNP Transistors**

## **Features**

- S and NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector – Emitter Voltage BCX17, BCX19 BCX18	V <sub>CEO</sub>	45 25	Vdc
Collector – Base Voltage BCX17, BCX19 BCX18	V <sub>CBO</sub>	50 30	Vdc
Emitter – Base Voltage	V <sub>EBO</sub>	5.0	Vdc
Collector Current – Continuous	Ic	500	mAdc

## THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1), T <sub>A</sub> = 25°C	P <sub>D</sub>	225	mW
Derate above 25°C		1.8	mW/°C
Thermal Resistance, Junction–to–Ambient	$R_{ heta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate, (Note 2) T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	300 2.4	mW mW/°C
Thermal Resistance, Junction–to–Ambient	$R_{ heta JA}$	417	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1

- 1.  $FR-5 = 1.0 \times 0.75 \times 0.062$  in.
- 2. Alumina = 0.4  $\times$  0.3  $\times$  0.024 in 99.5% alumina.

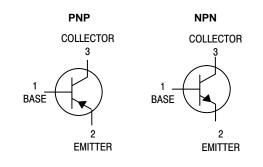


## ON Semiconductor®

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SOT-23 (TO-236) CASE 318-08 STYLE 6



## **MARKING DIAGRAM**



XX = T1, T2 or U1

M = Date Code\*

= Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or overbar may vary depending upon manufacturing location.

## **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

## BCX17LT1G, PNP BCX18LT1G, PNP BCX19LT1G, NPN SBCX19LT1G, NPN

## **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	1	•		1	
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 10 mAdc, I <sub>B</sub> = 0) BCX17, BCX19, SBCX19 BCX18	V <sub>(BR)</sub> CEO	45 25	- -	- -	Vdc
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 10 μAdc, I <sub>C</sub> = 0) BCX17, BCX19, SBCX19 BCX18	V <sub>(BR)</sub> CES	50 30	- -	- -	Vdc
Collector Cutoff Current $(V_{CB} = 20 \text{ Vdc}, I_E = 0)$ $(V_{CB} = 20 \text{ Vdc}, I_E = 0, T_A = 150^{\circ}\text{C})$	I <sub>CBO</sub>	- -	- -	100 5.0	nAdc μAdc
Emitter Cutoff Current (V <sub>EB</sub> = 5.0 Vdc, I <sub>C</sub> = 0)	I <sub>EBO</sub>	-	-	10	μAdc
ON CHARACTERISTICS					
DC Current Gain $ \begin{aligned} &(I_C = 100 \text{ mAdc, } V_{CE} = 1.0 \text{ Vdc}) \\ &(I_C = 300 \text{ mAdc, } V_{CE} = 1.0 \text{ Vdc}) \\ &(I_C = 500 \text{ mAdc, } V_{CE} = 1.0 \text{ Vdc}) \end{aligned} $	h <sub>FE</sub>	100 70 40	- - -	600 - -	-
Collector–Emitter Saturation Voltage (I <sub>C</sub> = 500 mAdc, I <sub>B</sub> = 50 mAdc)	V <sub>CE(sat)</sub>	-	_	0.62	Vdc
Base–Emitter On Voltage (I <sub>C</sub> = 500 mAdc, V <sub>CE</sub> = 1.0 Vdc)	V <sub>BE(on)</sub>	_	-	1.2	Vdc

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## **ORDERING INFORMATION**

Device	Specific Marking	Package	Shipping <sup>†</sup>
BCX17LT1G	T1	SOT-23 (Pb-Free)	3,000 / Tape & Reel
NSVBCX17LT1G*	T1	SOT-23 (Pb-Free)	3,000 / Tape & Reel
BCX18LT1G	T2	SOT-23 (Pb-Free)	3,000 / Tape & Reel
BCX19LT1G	U1	SOT-23 (Pb-Free)	3,000 / Tape & Reel
SBCX19LT1G*	U1	SOT-23 (Pb-Free)	3,000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

<sup>\*</sup>S and NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.

## BCX17LT1G, PNP BCX18LT1G, PNP BCX19LT1G, NPN SBCX19LT1G, NPN

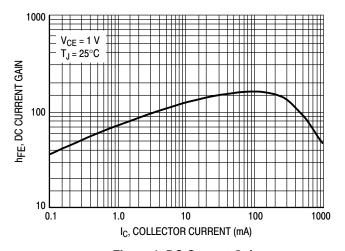


Figure 1. DC Current Gain

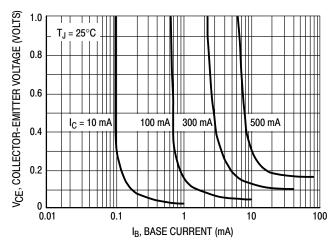


Figure 2. Saturation Region

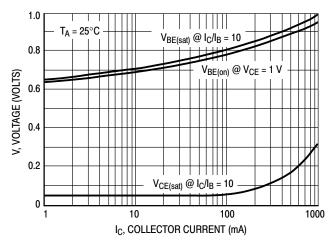


Figure 3. "On" Voltages

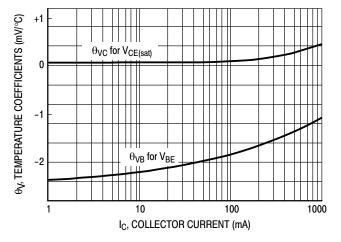


Figure 4. Temperature Coefficients

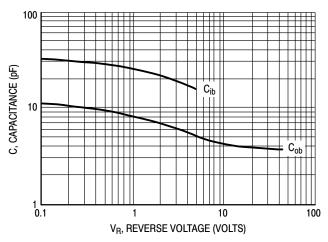


Figure 5. Capacitances



SOT-23 (TO-236) CASE 318-08 **ISSUE AS** 

**DATE 30 JAN 2018** 

# SCALE 4:1 D - 3X b

**TOP VIEW** 







## **RECOMMENDED SOLDERING FOOTPRINT**



DIMENSIONS: MILLIMETERS

#### NOTES:

- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.
  3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH.
  MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,

	PROT	RUSIONS, OR GATE BURRS.	
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	MILLIMETERS				INCHES	
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
С	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
Т	0°		10°	0°		10°

## **GENERIC MARKING DIAGRAM\***



XXX = Specific Device Code

= Date Code

= Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 7: PIN 1. EMITTER 2. BASE 3. COLLECTOR	STYLE 8: PIN 1. ANODE 2. NO CONNECTION 3. CATHODE
OT (1 F O			

SOT-23 (TO-236)

STYLE 9:	STYLE 10:	STYLE 11:	STYLE 12:	STYLE 13:	STYLE 14:
PIN 1. ANODE	PIN 1. DRAIN	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. SOURCE	PIN 1. CATHODE
<ol><li>ANODE</li></ol>	<ol><li>SOURCE</li></ol>	<ol><li>CATHODE</li></ol>	<ol><li>CATHODE</li></ol>	2. DRAIN	2. GATE
<ol><li>CATHODE</li></ol>	3. GATE	<ol><li>CATHODE-ANODE</li></ol>	<ol><li>ANODE</li></ol>	3. GATE	<ol><li>ANODE</li></ol>

STYLE 15:	STYLE 16:	STYLE 17:	STYLE 18:	STYLE 19:	STYLE 20:
PIN 1. GATE	PIN 1. ANODE	PIN 1. NO CONNECTION	PIN 1. NO CONNECTION	PIN 1. CATHODE	PIN 1. CATHODE
<ol><li>CATHODE</li></ol>	<ol><li>CATHODE</li></ol>	2. ANODE	<ol><li>CATHODE</li></ol>	2. ANODE	<ol><li>ANODE</li></ol>
<ol><li>ANODE</li></ol>	<ol><li>CATHODE</li></ol>	<ol><li>CATHODE</li></ol>	<ol><li>ANODE</li></ol>	<ol><li>CATHODE-ANOD</li></ol>	E 3. GATE

STYLE 21:	STYLE 22:	STYLE 23:	STYLE 24:	STYLE 25:	STYLE 26:
PIN 1. GATE	PIN 1. RETURN	PIN 1. ANODE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE
<ol><li>SOURCE</li></ol>	<ol><li>OUTPUT</li></ol>	2. ANODE	2. DRAIN	2. CATHODE	2. ANODE
3 DRAIN	3 INPLIT	3 CATHODE	3. SOURCE	3. GATE	<ol><li>NO CONNECTION</li></ol>

STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE	
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