ON Semiconductor

Is Now

Onsemi

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Power MOSFET 3 Amps, 30 Volts

P-Channel SO-8, FETKY™

The FETKY product family incorporates low R_{DS(on)}, MOSFETs packaged with industry leading, low forward drop, low leakage Schottky Barrier rectifiers to offer high efficiency components in a space saving configuration. Independent pinouts for MOSFET and Schottky die allow the flexibility to use a single component for switching and rectification functions in a wide variety of applications such as Buck Converter, Buck-Boost, Synchronous Rectification, Low Voltage Motor Control, and Load Management in Battery Packs, Chargers, Cell Phones and other Portable Products.

- Power MOSFET with Low V_F, Low I_R Schottky Rectifier
- Lower Component Placement and Inventory Costs along with, **Board Space Savings**
- R2 Suffix for Tape and Reel (2500 units/13" reel)
- Mounting Information for SO-8 Package Provided
- I_{DSS} Specified at Elevated Temperature
- Applications Information Provided
- Marking: 3P303

MOSFET MAXIMUM RATINGS (TJ = 25°C unless otherwise noted) (Notes 1. & 2.)

| Rating | Symbol | Value | Unit |
|--|------------------|-------------------|------------|
| Drain-to-Source Voltage | V _{DSS} | 30 | Vdc |
| Drain-to-Gate Voltage (R_{GS} = 1.0 M Ω) | V _{DGR} | 30 | Vdc |
| Gate-to-Source Voltage - Continuous | V _{GS} | ±20 | Vdc |
| $\label{eq:transformation} \begin{array}{l} \text{Drain Current} \\ - \text{ Continuous @ } T_{A} = 25^{\circ}\text{C} \\ - \text{ Continuous @ } T_{A} = 100^{\circ}\text{C} \\ - \text{ Single Pulse (tp $\leq $10 μs)} \end{array}$ | ID ID IDM | 3.5 2.25 12 | Adc Apk |
| Total Power Dissipation @ T _A = 25°C (Note 3.) | PD | 2.0 | Watts |
| $ Single Pulse Drain-to-Source Avalanche \\ Energy - STARTING T_J = 25^\circ C \\ V_{DD} = 30 \ Vdc, \ V_{GS} = 10 \ Vdc, \ V_{DS} = 20 \ Vdc, \\ I_L = 9.0 \ Apk, \ L = 10 \ mH, \ R_G = 25 \ \Omega $ | E _{AS} | 375 | mJ |

1. Negative sign for P-channel device omitted for clarity.

2. Pulse Test: Pulse Width ≤[250 μs, Duty Cycle ≤ 2.0%.

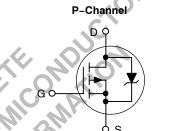
3. Mounted on 2" square FR4 board (1" sq. 2 oz. Cu 0.06" thick single sided), 10 sec. max.



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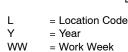
3 AMPERES 30 VOLTS $R_{DS(on)} = 100 \text{ m}\Omega$ V_F = 0.42 Volts



MARKING DIAGRAM

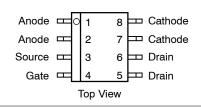
Н

H A A 6N303 CASE 751 LYWW STYLE 18



SO-8

PIN ASSIGNMENT



ORDERING INFORMATION

| Device | Package | Shipping |
|--------------|---------|------------------|
| MMDFS3P303R2 | SO-8 | 2500 Tape & Reel |

SCHOTTKY RECTIFIER MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

| Peak Repetitive Reverse Voltage DC Blocking Voltage | V _{RRM} V _R | 30 | Volts |
|--|------------------------------------|-----|-------|
| Average Forward Current (Note 3.) (Rated V_R) $T_A = 100^{\circ}C$ | Io | 3.0 | Amps |
| Peak Repetitive Forward Current (Note 3.) (Rated V _R , Square Wave, 20 kHz) T _A = 105°C | I _{frm} | 6.0 | Amps |
| Non-Repetitive Peak Surge Current (Surge applied at rated load conditions, halfwave, single phase, 60 Hz) | I _{fsm} | 30 | Amps |

THERMAL CHARACTERISTICS - SCHOTTKY AND MOSFET

| Thermal Resistance – Junction-to-Ambient (Note 1) – MOSFET | R _{θJA} | 201 | °C/W |
|---|-----------------------------------|------------|------|
| Thermal Resistance – Junction-to-Ambient (Note 2) – MOSFET | R _{0JA} | 105 | |
| Thermal Resistance – Junction-to-Ambient (Note 3.) – MOSFET | R _{θJA} | 62.5 | |
| Thermal Resistance – Junction-to-Ambient (Note 1) – Schottky | R _{θJA} | 197 | |
| Thermal Resistance – Junction-to-Ambient (Note 2) – Schottky | R _{θJA} | 97 | 2 |
| Thermal Resistance – Junction-to-Ambient (Note 3.) – Schottky | R _{θJA} | 62.5 | |
| Operating and Storage Temperature Range | T _j , T _{stg} | -55 to 150 | °C |

3. Mounted on 2" square FR4 board (1" sq. 2 oz. Cu 0.06" thick single sided), 10 sec. max.

1. Mounted with minimum recommended pad size, PC Board FR4.

2. Mounted on 2" square FR4 board (1" sq. 2 oz. Cu 0.06" thick single sided), Steady State.

NDLON **MOSFET ELECTRICAL CHARACTERISTICS** ($T_J = 25^{\circ}C$ unless otherwise noted) (Notes 1. & 3)

| Characteristics | Symbol | Min | Тур | Max | Unit |
|--|----------------------|----------|----------------|----------------|--------------|
| OFF CHARACTERISTICS | ~ ~ | | | • | |
| Drain–Source Voltage (V _{GS} = 0 Vdc, I _D = 0.25 mA) Temperature Coefficient (Positive) | V _{(BR)DSS} | 30 - | 27 | | Vdc mV/°C |
| Zero Gate Drain Current $(V_{DS} = 30 \text{ Vdc}, V_{GS} = 0 \text{ Vdc})$ $(V_{DS} = 30 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, T_J = 125^{\circ}\text{C})$ | IDSS | - | - | 1.0 10 | μAdc |
| Gate Body Leakage Current (V _{GS} = ± 20 Vdc, V _{DS} = 0) | I _{GSS} | - | _ | 100 | nAdc |
| ON CHARACTERISTICS (Note 3) | SV I | | | • | |
| | V _{GS(th)} | 1.0 _ | 1.7 3.5 | | Vdc mV/°C |
| Static Drain–Source Resistance (V _{GS} = 10 Vdc, I _D = 3.5 Adc) (V _{GS} = 4.5 Vdc, I _D = 2.0 Adc) | R _{DS(on)} | - | 0.085 0.130 | 0.100 0.160 | Ω |
| Forward Transconductance (V _{DS} = 15 Vdc, I _D = 3.5 Adc) | 9FS | - | 5.0 | _ | mhos |
| DYNAMIC CHARACTERISTICS | | | | | |
| Input Capacitance | C _{iss} | - | 405 | - | pF |
| Output Capacitance $(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, f = 1.0 \text{ MHz})$ | C _{oss} | - | 200 | - | |
| Reverse Transfer Capacitance | C _{rss} | - | 55 | - | |
| SWITCHING CHARACTERISTICS (Note 4) | • | | | • | |
| Turn-On Delay Time | t _{d(on)} | - | 12.5 | 25 | ns |
| Rise Time $(V_{DD} = 20 \text{ Vdc}, I_D = 2.0 \text{ Adc}, I_D = 2.0 \text{ Adc}$ | t _r | - | 16 | 30 | |
| VGS = 10 Vdc,Turn-Off Delay Time $R_G = 6.0 \Omega$ | t _{d(off)} | - | 50 | 90 | |
| Fall Time | t _f | - | 35 | 65 | |

1. Negative signs for P-Channel device omitted for clarity.

3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

4. Switching characteristics are independent of operating junction temperature.

| MOSFET ELECTRICAL CHARACTERISTICS - c | ontinued (T _J = 25°C unless otherwise noted) (Notes 1. & 3) |
|---------------------------------------|---|
|---------------------------------------|---|

| Cha | racteristics | Symbol | Min | Тур | Max | Unit |
|--------------------------|--|---------------------|-----|------|-----|------|
| SWITCHING CHARACTERISTIC | S - continued (Note 4) | | | | | |
| Turn-On Delay Time | | t _{d(on)} | - | 19 | - | ns |
| Rise Time | $(V_{DD} = 20 \text{ Vdc}, I_D = 2.0 \text{ Adc},$ | t _r | - | 36 | - | |
| Turn-Off Delay Time | | t _{d(off)} | - | 27 | - | |
| Fall Time | | t _f | - | 31 | - | |
| Gate Charge | | QT | - | 14 | 25 | nC |
| | (V _{DS} = 20 Vdc, I _D = 3.5 Adc, | Q ₁ | - | 1.8 | - | |
| | V _{GS} = 10 Vdc) | Q ₂ | - | 4.5 | - | |
| | | Q ₃ | - | 2.85 | - | |

DRAIN SOURCE DIODE CHARACTERISTICS

| Forward On-Voltage (Note 3) | $(I_S = 1.7 \text{ Adc}, V_{GS} = 0 \text{ Vdc})$ | V _{SD} | - | 0.9 | 1.2 | V |
|-----------------------------------|---|-----------------|---|------|-----------------------------|----|
| Reverse Recovery Time | | t _{rr} | - | 26.6 | | ns |
| | | t _a | - | 18.8 | ^x O ^x | |
| | (V _{GS} = 0 V, I _S = 3.5 A, dIS/dt = 100 A/μs) | t _b | - | 7.8 | - | |
| Reverse Recovery Stored Charge | | Q _{RR} | | 0.03 | 1 | μC |

SCHOTTKY RECTIFIER ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

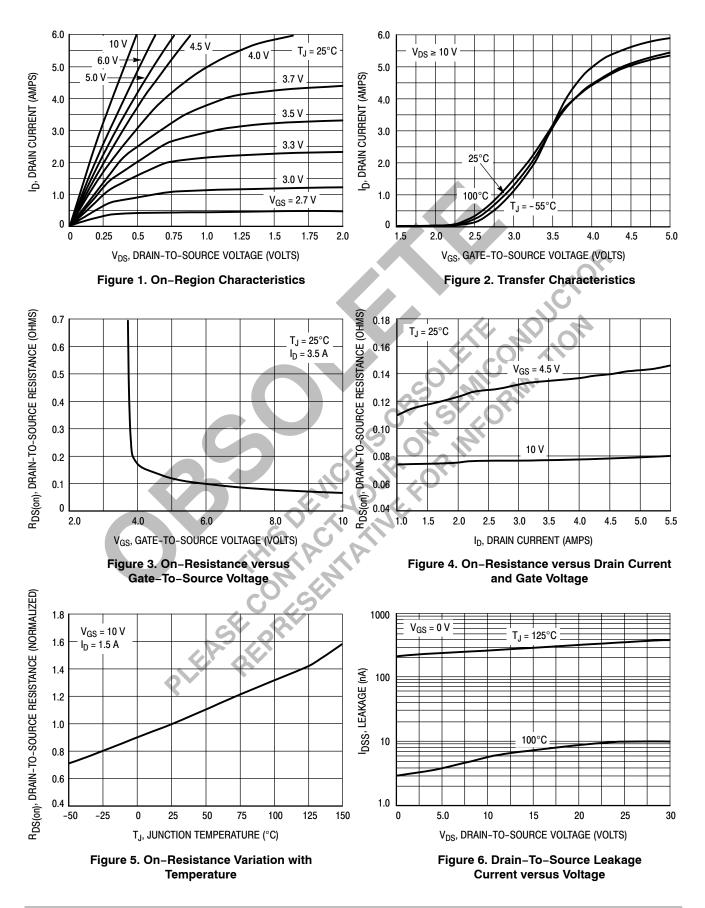
| Maximum Instantaneous Forward Voltage (Note 3) VF | T _J = 25°C | T _J = 125°C | Volts |
|--|-----------------------|------------------------|-------|
| $I_F = 100 \text{ mAdc}$ $I_F = 3.0 \text{ Adc}$ | 0.28 | 0.13 | |
| I _F = 6.0 Adc | 0.42 0.50 | 0.33 0.45 | |
| Maximum Instantaneous Reverse Current (Note 3) | T _J = 25°C | T _J = 125°C | μA |
| V _R = 30 V | 250 | - | |
| | _ | 25 | mA |
| Maximum Voltage Rate of Change V _R = 30 V dV/dt | 10,0 | 000 | V/µs |

1. Negative signs for P-Channel device omitted for clarity.

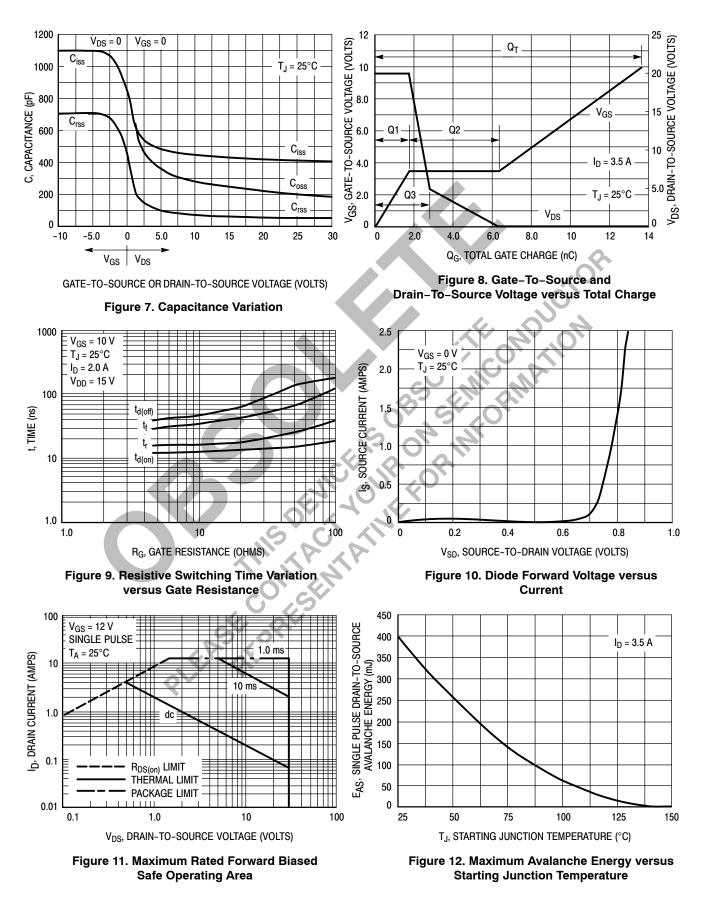
6. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

6. Pulse Test: Pulse Width ≤[300 µs, Duty Cycle ≤ 2%.
7. Switching characteristics are independent of operating junction temperature.

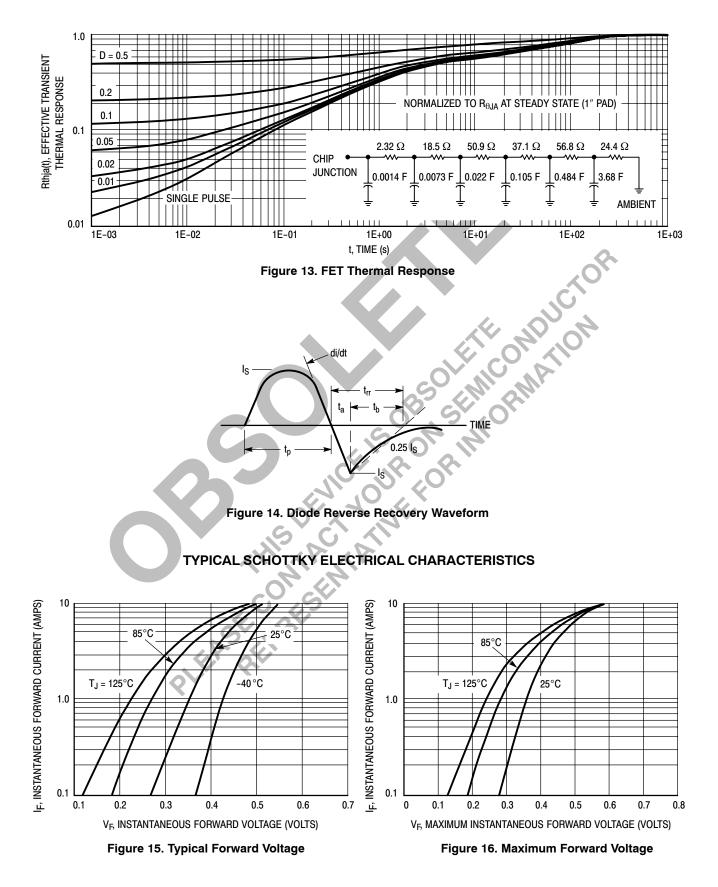
TYPICAL FET ELECTRICAL CHARACTERISTICS



TYPICAL FET ELECTRICAL CHARACTERISTICS



TYPICAL FET ELECTRICAL CHARACTERISTICS



TYPICAL SCHOTTKY ELECTRICAL CHARACTERISTICS

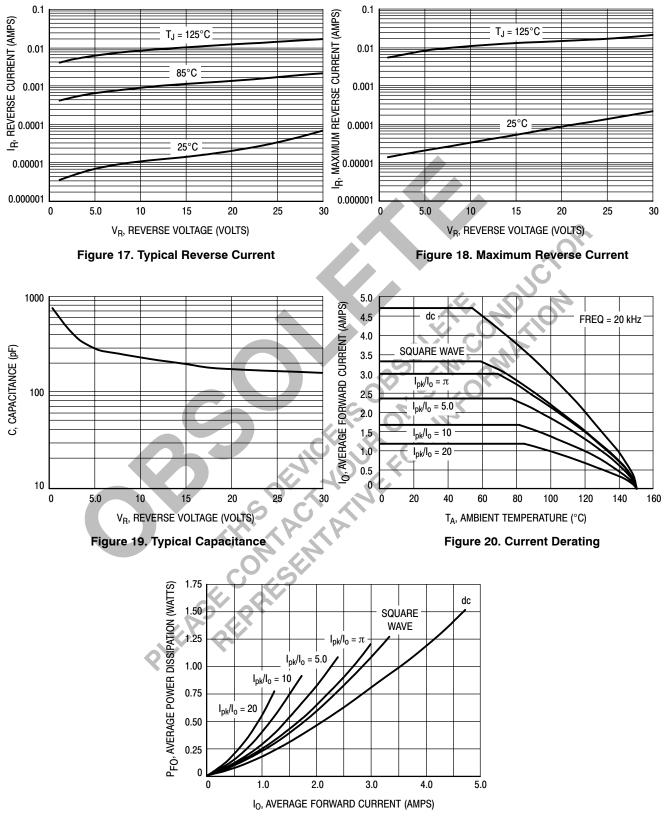
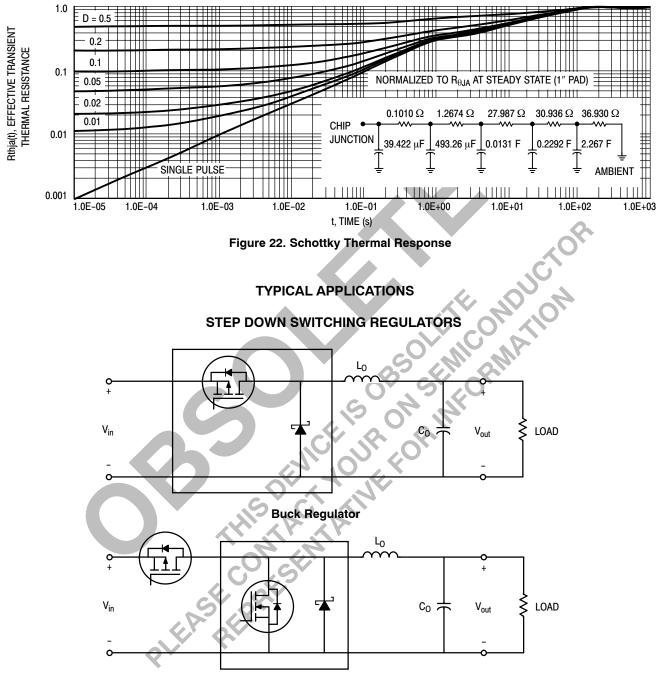


Figure 21. Forward Power Dissipation

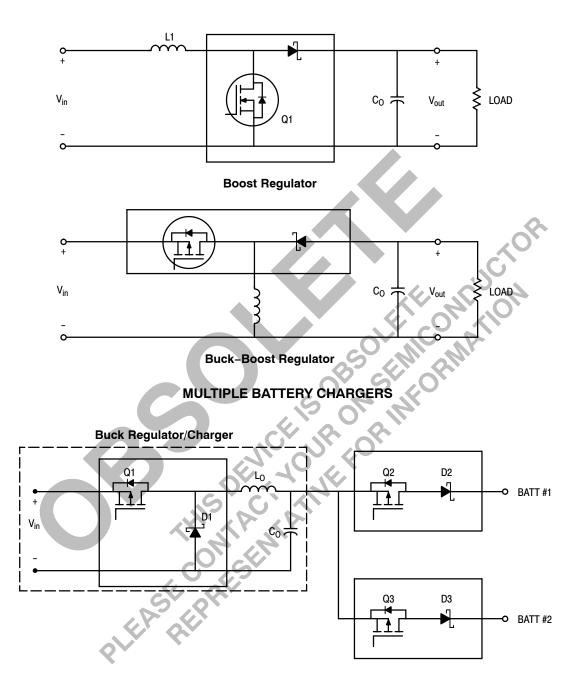
TYPICAL SCHOTTKY ELECTRICAL CHARACTERISTICS





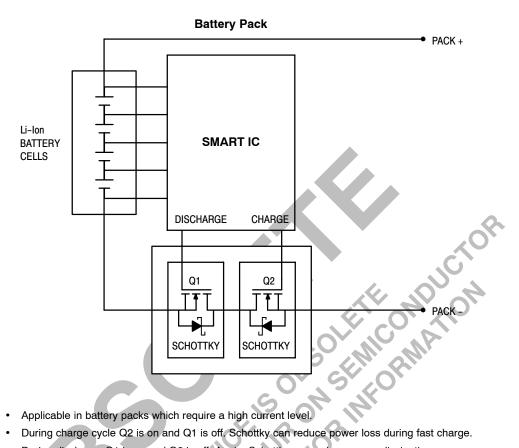
TYPICAL APPLICATIONS

STEP UP SWITCHING REGULATORS

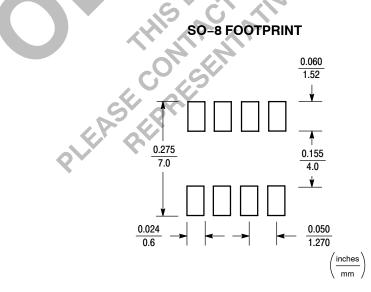


TYPICAL APPLICATIONS

Li-Ion BATTERY PACK APPLICATIONS

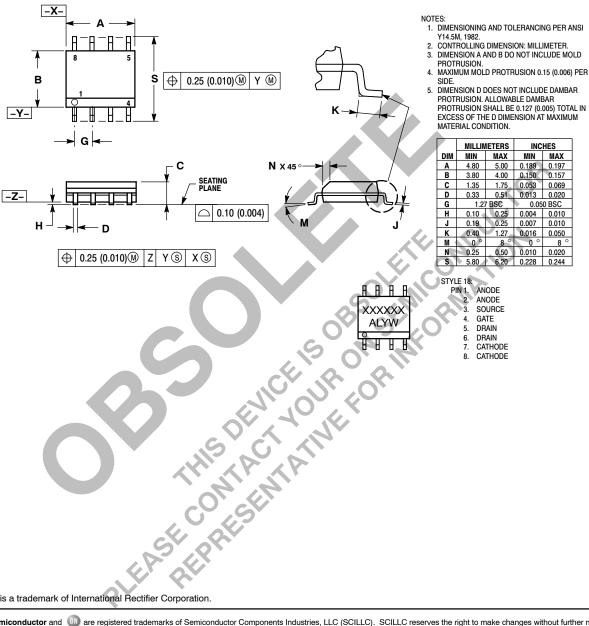


- ٠
- During charge cycle Q2 is on and Q1 is off. Schottky can reduce power loss during fast charge. ٠
- During discharge Q1 is on and Q2 is off. Again, Schottky can reduce power dissipation. ٠
- Under normal operation, both transistors are on. •



PACKAGE DIMENSIONS





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