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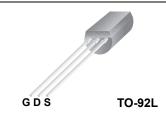
IRFNL210B N-Channel B-FET 200 V, 1.0 A, 1.5 Ω

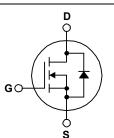
Description

These N-Channel enhancement mode power field effect transistors are produced using ON Semiconductor's proprietary, planar, DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switching DC/DC converters, switch mode power supplies, DC-AC converters for uninterrupted power supply and motor control.

Features

- + 1.0 A, 200 V, $R_{DS(on)}$ = 1.5 Ω @ V_{GS} = 10 V + Low Gate Charge (typical 7.2 nC)
- Low Crss (typical 6.8 pF)
- Fast Switching
- 100% Avalanche Tested
- Improved dv/dt Capability





Absolute Maximum Ratings T_c = 25°C unless otherwise noted.

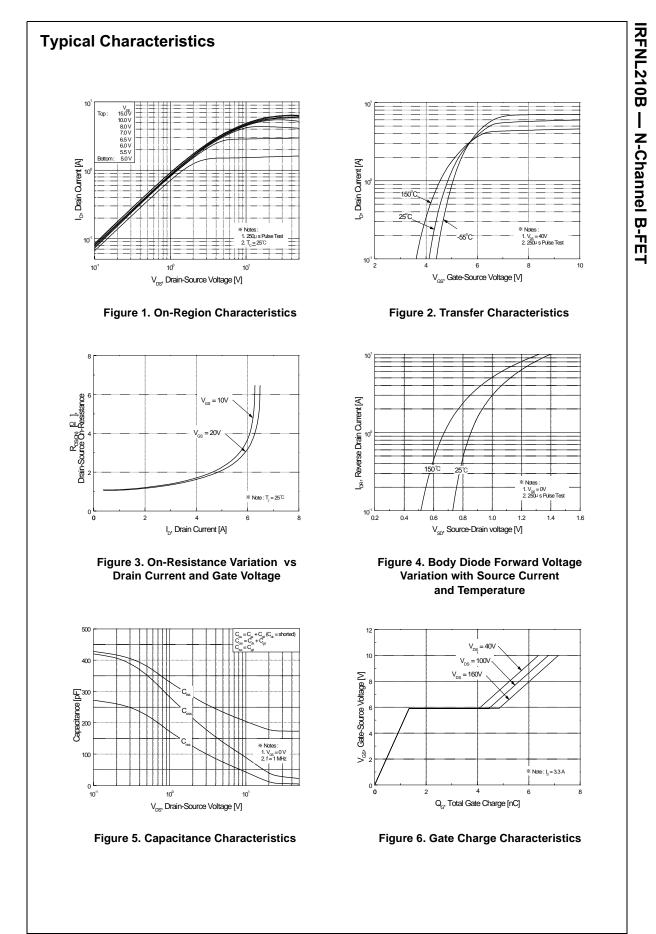
Symbol	Parameter	IRFNL210BTA-FP001	Unit	
V _{DSS}	Drain-Source Voltage	200	V	
I _D	Drain Current - Continuous ($T_C = 25^\circ$	1.0	А	
	- Continuous (T _C = 100	0.93	А	
I _{DM}	Drain Current - Pulsed	(Note 1)	10	A
V _{GSS}	Gate-Source Voltage	± 30	V	
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	40	mJ
I _{AR}	Avalanche Current	(Note 1)	3.3	A mJ
E _{AR}	Repetitive Avalanche Energy	(Note 1)	0.031	
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	5.0	V/ns
PD	Power Dissipation ($T_C = 25^{\circ}C$)		3.1	W
	- Derate above 25°C	0.025	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Rar	-55 to +150	°C	
TL	Maximum lead temperature for soldering 1/8" from case for 5 seconds	300	°C	

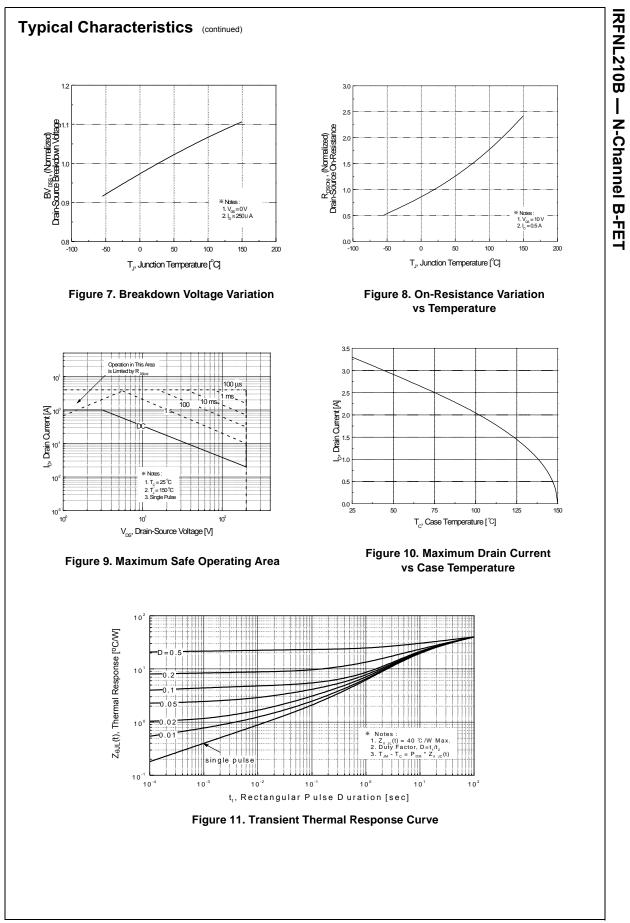
Thermal Characteristics

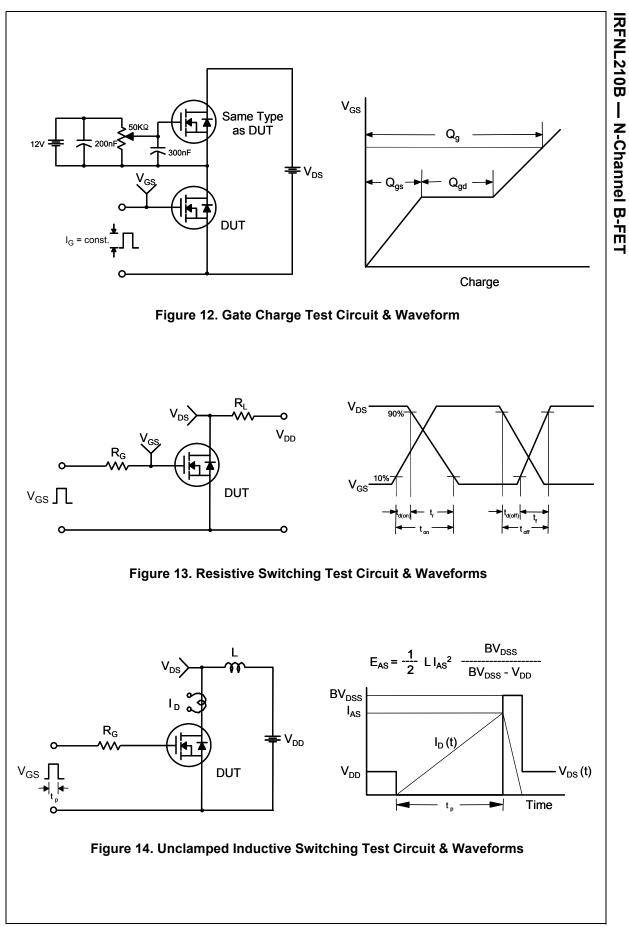
Symbol	Parameter	IRFNL210BTA-FP001	Unit
$R_{\theta JL}$	Thermal Resistance, Junction-to-Lead, Max.	40	°C/W

Part Number IRFNL210BTA-FP001		Top Mark	Pa	Package Packing Method		Reel	Size	Tape Width		Quantity
		210B	T	D-92L	AMMO	N/.	A	N/A		2000 units
lectri	cal Chara	cteristics _{Tc} =	= 25°C unl	less otherwi	se noted.					
Symbol		Parameter			Test Conditions		Min	Тур	Max	Units
Off Cha	racteristics	5								
BV _{DSS}	Drain-Source Breakdown Voltage		$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$			200			V	
ΔBV _{DSS} ΔT _J	Breakdown Voltage Temperature Coefficient Zero Gate Voltage Drain Current		$I_D = 250 \ \mu$ A, Referenced to 25°C				0.2		V/°C	
DSS			$V_{DS} = 200 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 160 \text{ V}, T_C = 125^{\circ}\text{C}$					10 100	μA μA	
GSSF	Gate-Body Le	Gate-Body Leakage Current, Forward		$V_{GS} = 30 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$					100	nA
GSSR	-	Gate-Body Leakage Current, Polward		$V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$					-100	nA
	,	0 /	10100	- 63					100	10.0
GS(th)	Gate Thresho			$V_{DS} = V$	V _{GS} , I _D = 250 μA		2.0		4.0	V
R _{DS(on)}	Static Drain-S On-Resistanc	atic Drain-Source		V _{GS} =10V, I _D =0.5 A				1.16	1.5	Ω
Ĵfs	Forward Trans	rd Transconductance		V _{DS} = 40 V, I _D = 1.0 A				2.4		S
Dynami	ic Characte	ristics								
viss	Input Capacita	ance		V _{DS} = 25 V, V _{GS} = 0 V,				175	225	pF
Soss	Output Capac	citance			f = 1.0 MHz			30	40	pF
rss		sfer Capacitance		1 - 1.0				6.8	9.0	pF
Switchi	ng Charact	eristics								
d(on)	Turn-On Dela	y Time		V	100 V, I _D = 3.3 A,			5.2	20	ns
	Turn-On Rise	Time		$R_G = 2$	-			35	80	ns
d(off)	Turn-Off Dela	y Time		NG - 20	0 12			20	50	ns
	Turn-Off Fall	Time				(Note 4)		25	60	ns
٥ ^d	Total Gate Ch	arge		Vne = 1	160 V, I _D = 3.3 A,			7.2	9.3	nC
2 _{gs}	Gate-Source	Charge		$V_{GS} = 7$				1.3		nC
2 _{gd}	Gate-Drain Cl	harge		00		(Note 4)		3.5		nC
)rain-S	ource Diod	e Characterist	ics ar	nd Max	imum Ratings					
3	Source Diode Characteristics and Maximum Ratings Maximum Continuous Drain-Source Diode Forward Current							3.3	А	
SM	Maximum Pul	sed Drain-Source	Diode F	orward C	Current				10	Α
/ _{SD}	Drain-Source	Diode Forward Vo	ltage	$V_{GS} = 0$	0 V, I _S = 3.3 A				1.5	V
rr	Reverse Reco	overy Time			0 V, I _S = 3.3 A,			106		ns
2 ^{rr}	Reverse Reco	overy Charge		dl _F / dt	= 100 A/μs			0.37		μC
L = 5.5 mH, I _{SD} ≤ 3.3 A,	I _{AS} = 3.3 A, V _{DD} = 5	mited by maximum junction V , $R_G = 25 \Omega$, Starting $_{DD} \leq BV_{DSS}$, Starting T_J ating temperature.	$T_{J} = 25^{\circ}$							

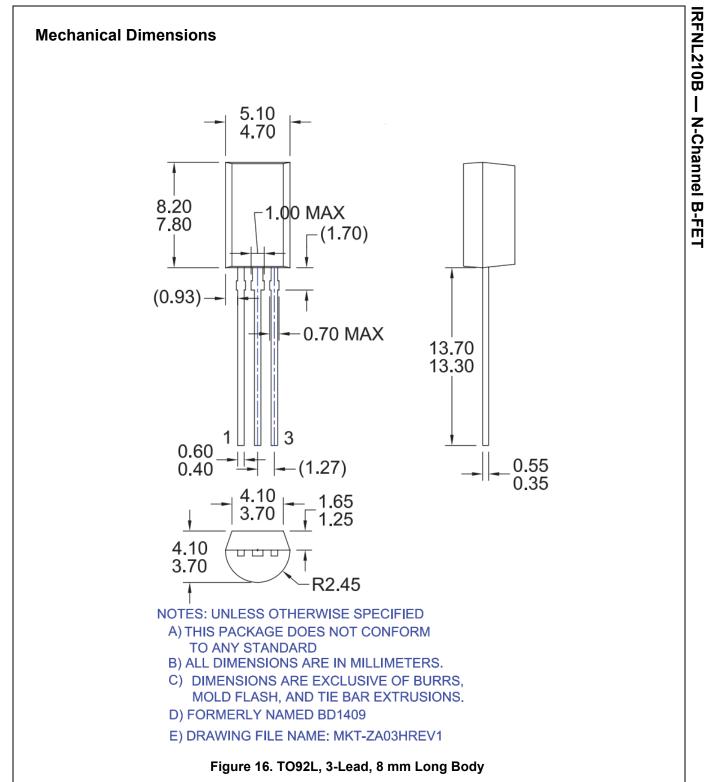
IRFNL210B — N-Channel B-FET







DUT + V_{DS} 0 ۱_{sd} م L Driver R_G, Same Type as DUT ŧν_{DD} ∏∏ v_{gs} • dv/dt controlled by R_{G} • I_{SD} controlled by pulse period C 1 Gate Pulse Width V_{GS} D = Gate Pulse Period 10V (Driver) \mathbf{I}_{FM} , Body Diode Forward Current I _{SD} di/dt (DUT) I_{RM} Body Diode Reverse Current V_{DS} (DUT) Body Diode Recovery dv/dt $V_{\underline{SD}}$ V_{DD} Body Diode Forward Voltage Drop Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms



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