NSDEMN11XV6T1, NSDEMN11XV6T5

Common Cathode Quad Array Switching Diode

This Common Cathode Epitaxial Planar Quad Diode is designed for use in ultra high speed switching applications. This device is housed in the SOT–563 package which is designed for low power surface mount applications, where board space is at a premium.

Features

- Fast t_{rr}
- Low C_D
- Pb-Free Packages are Available

MAXIMUM RATINGS $(T_A = 25^{\circ}C)$

Rating	Symbol	Value	Unit
Reverse Voltage	V _R	80	Vdc
Peak Reverse Voltage	V _{RM}	80	Vdc
Forward Current	lF	100	mAdc
Peak Forward Current	I _{FM}	300	mAdc
Peak Forward Surge Current	I _{FSM} (Note 1)	2.0	Adc

THERMAL CHARACTERISTICS

Characteristic (One Junction Heated)	Symbol	Max	Unit
Total Device Dissipation @T _A = 25°C Derate above 25°C	P _D	357 (Note 2) 2.9 (Note 2)	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	350 (Note 2)	°C/W
Characteristic (Both Junctions Heated)	Symbol	Max	Unit
Total Device Dissipation @T _A = 25°C Derate above 25°C	P _D	500 (Note 2) 4.0 (Note 2)	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	250 (Note 2)	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

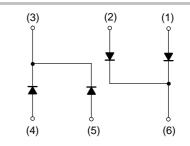
1. $t = 1 \mu S$

2. FR-4 @ Minimum Pad



ON Semiconductor®

http://onsemi.com





SOT-563 CASE 463A PLASTIC

MARKING DIAGRAM



N9 = Specific Device Code

M = Date Code

■ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
NSDEMN11XV6T1	SOT-563	4000/Tape & Reel
NSDEMN11XV6T1G	SOT-563 (Pb-Free)	4000/Tape & Reel
NSDEMN11XV6T5	SOT-563	8000/Tape & Reel
NSDEMN11XV6T5G	SOT-563 (Pb-Free)	8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

NSDEMN11XV6T1, NSDEMN11XV6T5

ELECTRICAL CHARACTERISTICS (T_A = 25°C)

Characteristic	Symbol	Condition	Min	Max	Unit
Reverse Voltage Leakage Current	I _R	V _R = 70 V	_	0.1	μAdc
Forward Voltage	V _F	I _F = 100 mA	-	1.2	Vdc
Reverse Breakdown Voltage	V _R	I _R = 100 μA	80	-	Vdc
Diode Capacitance	C _D	V _R = 6.0 V, f = 1.0 MHz	-	3.5	pF
Reverse Recovery Time	t _{rr} (Note 3)	I_F = 5.0 mA, V_R = 6.0 V, R_L = 100 Ω , I_{rr} = 0.1 I_R	-	4.0	ns

^{3.} t_{rr} Test Circuit on following page.

TYPICAL ELECTRICAL CHARACTERISTICS

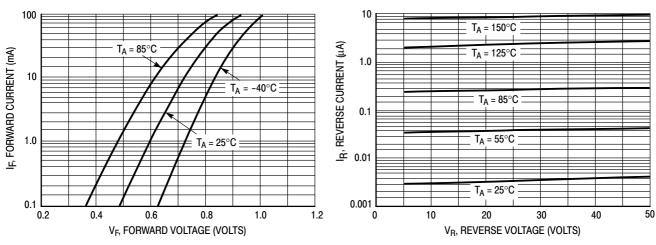


Figure 1. Forward Voltage

Figure 2. Reverse Current

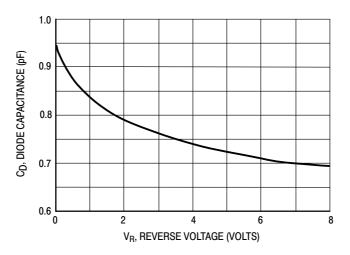


Figure 3. Diode Capacitance

NSDEMN11XV6T1, NSDEMN11XV6T5

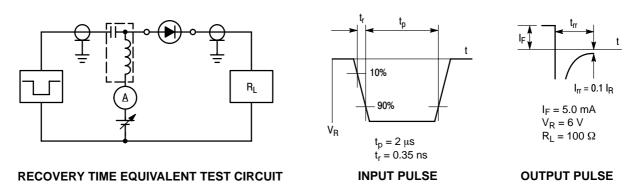


Figure 4. Reverse Recovery Time Test Circuit for the NSDEMN11XV6T1

MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS



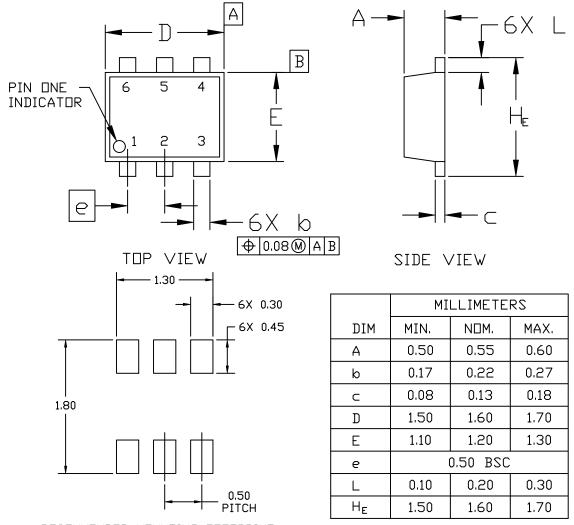


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NOTES:

- I. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.



RECOMMENDED MOUNTING FOOTPRINT*

For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

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DESCRIPTION:	SOT-563, 6 LEAD		PAGE 1 OF 2

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STYLE 1: PIN 1. EMITTER 1 2. BASE 1 3. COLLECTOR 2 4. EMITTER 2 5. BASE 2 6. COLLECTOR 1	STYLE 2: PIN 1. EMITTER 1 2. EMITTER 2 3. BASE 2 4. COLLECTOR 2 5. BASE 1 6. COLLECTOR 1	STYLE 3: PIN 1. CATHODE 1 2. CATHODE 1 3. ANODE/ANODE 4. CATHODE 2 5. CATHODE 2 6. ANODE/ANODE
STYLE 4: PIN 1. COLLECTOR 2. COLLECTOR 3. BASE 4. EMITTER 5. COLLECTOR 6. COLLECTOR	STYLE 5: PIN 1. CATHODE 2. CATHODE 3. ANODE 4. ANODE 5. CATHODE 6. CATHODE	STYLE 6: PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE 5. CATHODE 6. CATHODE
STYLE 7: PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE 5. ANODE 6. CATHODE	STYLE 8: PIN 1. DRAIN 2. DRAIN 3. GATE 4. SDURCE 5. DRAIN 6. DRAIN	STYLE 9: PIN 1. SDURCE 1 2. GATE 1 3. DRAIN 2 4. SDURCE 2 5. GATE 2 6. DRAIN 1
STYLE 10: PIN 1. CATHODE 1 2. N/C 3. CATHODE 2 4. ANODE 2 5. N/C 6. ANODE 1	STYLE 11: PIN 1. EMITTER 2 2. BASE 2 3. COLLECTOR 1 4. EMITTER 1 5. BASE 1 6. COLLECTOR 2	

GENERIC MARKING DIAGRAM*



XX = Specific Device CodeM = Month Code= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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