1SMF16BT1, 1SMF16BT3, 1SMF16BT3G

Zener Transient Voltage Suppressor SOD-123 Flat Lead Package

The 1SMF16B is designed to protect voltage sensitive components from high voltage, high energy transients. Excellent clamping capability, high surge capability, low Zener impedance and fast response time. Because of its small size, it is ideal for use in cellular phones, portable devices, business machines, power supplies and many other industrial/consumer applications.

Specification Features:

- Stand-off Voltage: 16 Volt
- Peak Power 175 Watts @ 1 ms
- Maximum Clamp Voltage @ Peak Pulse Current
- Low Leakage
- Response Time is Typically < 1 ns
- IEC61000-4-2 Level 4 ESD Protection
- Low Profile Maximum Height of 1.0 mm
- Small Footprint
- Cathode Indicated by Polarity Band
- Pb-Free Package is Available

Mechanical Characteristics:

CASE: Void-free, transfer-molded, thermosetting plastic

LEAD FINISH: 100% Matte Sn (Tin)

MOUNTING POSITION: Anv

QUALIFIED MAX REFLOW TEMPERATURE: 260°C

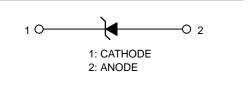
Device Meets MSL 1 Requirements

Epoxy Meets UL 94, V-0



http://onsemi.com

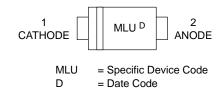
PLASTIC SURFACE MOUNT ZENER OVERVOLTAGE TRANSIENT SUPPRESSOR 175 WATT PEAK POWER





SOD-123FL CASE 498 PLASTIC

MARKING DIAGRAM



ORDERING INFORMATION

Device	Package	Shipping [†]
1SMF16BT1	SOD-123FL	3,000/Tape & Reel
1SMF16BT3	SOD-123FL	10,000/Tape & Reel
1SMF16BT3G	SOD-123FL (Pb-Free)	10,000/Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

1SMF16BT1, 1SMF16BT3, 1SMF16BT3G

MAXIMUM RATINGS

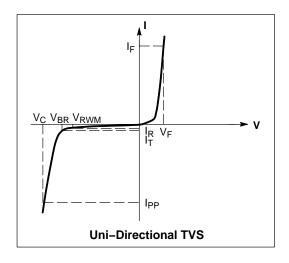
Rating	Symbol	Value	Unit
Maximum P _{pk} Dissipation @ T _A = 25°C, (PW–10/1000 μs) (Note 1)	P _{pk}	175	W
Maximum P _{pk} Dissipation @ T _A = 25°C, (PW-8/20 μs) (Note 2)	P_{pk}	1000	W
Operating and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

- 1. Non–repetitive current pulse at $T_A=25^{\circ}C$, per waveform of Figure 3. 2. Non–repetitive current pulse at $T_A=25^{\circ}C$, per waveform of Figure 4.

ELECTRICAL CHARACTERISTICS

(T_A = 25°C unless otherwise noted)

Symbol	Parameter				
I _{PP}	Maximum Reverse Peak Pulse Current				
V _C	Clamping Voltage @ I _{PP}				
V_{RWM}	Working Peak Reverse Voltage				
I _R	Maximum Reverse Leakage Current @ V _{RWM}				
V_{BR}	Breakdown Voltage @ I _T				
I _T	Test Current				
lF	Forward Current				
V _F	Forward Voltage @ I _F				



ELECTRICAL CHARACTERISTICS ($T_L = 30$ °C unless otherwise noted, $V_F = 1.3$ Volts @ 850 mA)

		V _{RWM} (Note 3)	V _{BR} @ I_T (V) (Note 4)			I _T	I _R @ V _{RWM}	Max V _C @ I _{PP} = 1 Amp	Max V _C @ I _{PP} = 7 Amp
Device	Marking	(V)	Min	Nom	Max	(mA)	(μΑ)	(V)	(V)
1SMF16B	MLU	16	16.7	17.6	18.5	1.0	1.0	20	26

- 3. A transient suppressor is normally selected according to the Working Peak Reverse Voltage (V_{RWM}) which should be equal to or greater than the DC or continuous peak operating voltage level.
- 4. V_{BR} measured at pulse test current I_T at ambient temperature of 25°C.

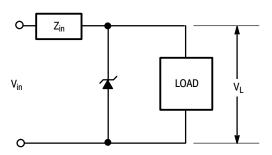


Figure 1. Typical Protection Circuit

1SMF16BT1, 1SMF16BT3, 1SMF16BT3G

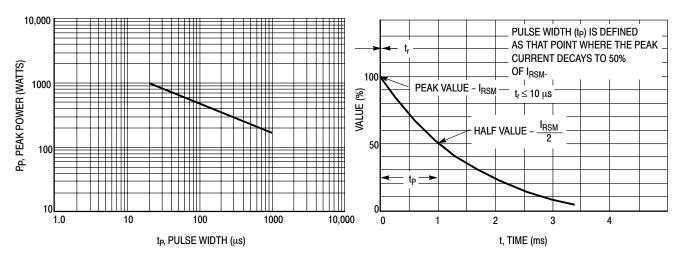


Figure 2. Pulse Rating Curve

Figure 3. 10 X 1000 µs Pulse Waveform

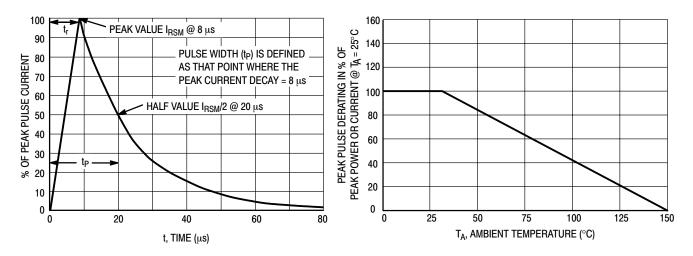


Figure 4. 8 X 20 µs Pulse Waveform

Figure 5. Pulse Derating Curve

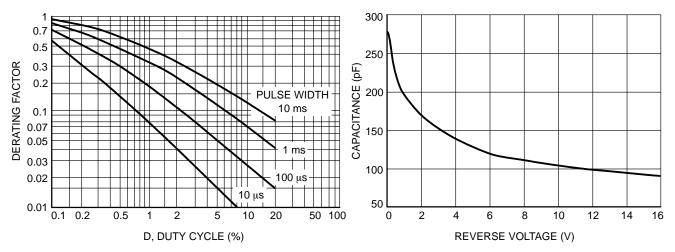


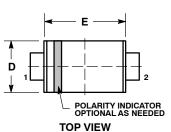
Figure 6. Typical Derating Factor for Duty Cycle

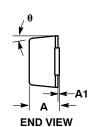
Figure 7. Capacitance versus Reverse Voltage

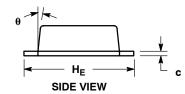


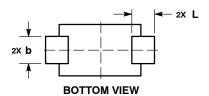
SOD-123FL **CASE 498** ISSUE D

DATE 10 MAY 2013

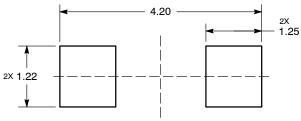








RECOMMENDED SOLDERING FOOTPRINT*



DIMENSIONS: MILLIMETERS

NOTES:

- ES:
 DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: MILLIMETER.
 DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH.
 DIMENSIONS D AND J ARE TO BE MEASURED ON FLAT SECTION
 OF THE LEAD: BETWEEN 0.10 AND 0.25 MM FROM THE LEAD TIP.

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.90	0.95	0.98	0.035	0.037	0.039	
A1	0.00	0.05	0.10	0.000	0.002	0.004	
b	0.70	0.90	1.10	0.028	0.035	0.043	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	1.50	1.65	1.80	0.059	0.065	0.071	
E	2.50	2.70	2.90	0.098	0.106	0.114	
L	0.55	0.75	0.95	0.022	0.030	0.037	
HE	3.40	3.60	3.80	0.134	0.142	0.150	
θ	0°	-	8°	0°	-	8°	

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

= Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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DESCRIPTION:	SOD-123FL		PAGE 1 OF 1		

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^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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