EMH2407 General-Purpose Switching Device Applications

Features

- Low ON-Resistance
- Best Suited for LiB Charging and Discharging Switch
- Common-Drain Type
- 2.5 V Drive
- Protection Diode In

ABSOLUTE MAXIMUM RATINGS at Ta = 25°C

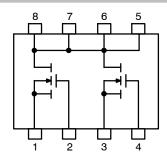
Symbol	Parameter	Conditions	Ratings	Unit
V _{DSS}	Drain to Source Voltage		20	V
V _{GSS}	Gate to Source Voltage		±12	V
I _D	Drain Current (DC)		6	Α
I _{DP}	Drain Current (Pulse)	PW ≤ 10 μs, duty cycles ≤ 1%	40	Α
P _D	Allowable Power Dissipation	When mounted on ceramic substrate (900 mm ² × 0.8 mm) 1 unit	1.3	W
P _T	Total Dissipation	When mounted on ceramic substrate (900 mm ² × 0.8 mm)	1.4	W
T _{CH}	Channel Temperature		150	°C
T _{STG}	Storage Temperature		–55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

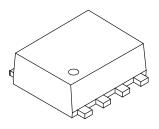


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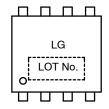


ELECTRICAL CONNECTION



EMH8 CASE 419AT

MARKING DIAGRAM



LG = Specific Device Code XX = Lot Number

ORDERING INFORMATION

Device	Package	Memo	Shipping
EMH2407-TL-H	EMH8	Pb-Free/ Halogen Free	3000 Units/ Reel

ELECTRICAL CHARACTERISTICS at Ta = 25°C

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)DSS}	Drain to Source Breakdown Voltage	I _D = 1 mA, V _{GS} = 0 V	20			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 20 V, V _{GS} = 0 V			1	μА
I _{GSS}	Gate to Source Leakage Current	V _{GS} = ±8 V, V _{DS} = 0 V			±10	μА
V _{GS} (off)	Cutoff Voltage	V _{DS} = 10 V, I _D = 1 mA	0.5		1.3	V
yfs	Forward Transfer Admittance	V _{DS} = 10 V, I _D = 3 A	3	5		S
R _{DS} (on)1	Static Drain to Source On–State Resistance	I _D = 3 A, V _{GS} = 4.5 V	13	19	25	mΩ
R _{DS} (on)2		I _D = 3 A, V _{GS} = 4 V	14	20	26	mΩ
R _{DS} (on)3		I _D = 1.5 A, V _{GS} = 2.5 V	16	28	39	mΩ
C _{iss}	Input Capacitance	V _{DS} = 10 V, f = 1 MHz		580		pF
C _{oss}	Output Capacitance			95		pF
C _{rss}	Reverse Transfer Capacitance			75		pF
t _d (on)	Turn-ON Delay Time	See specified Test Circuit.		310		ns
t _r	Rise Time			1020		ns
t _d (off)	Turn-OFF Delay Time			3000		ns
t _f	Fall Time			2250		ns
Qg	Total Gate Charge	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 6 \text{ A}$		6.3		nC
Qgs	Gate to Source Charge			0.83		nC
Qgd	Gate to Drain "Miller" Charge			1.9		nC
V _{SD}	Diode Forward Voltage	I _S = 6 A, V _{GS} = 0 V		0.78		V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

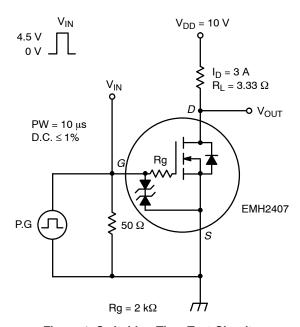
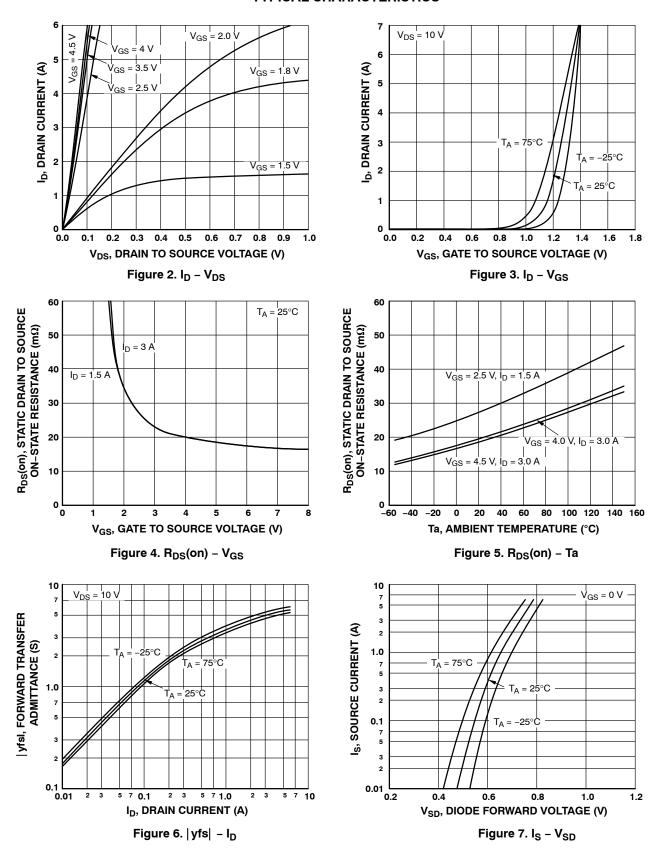


Figure 1. Switching Time Test Circuit

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (continued)

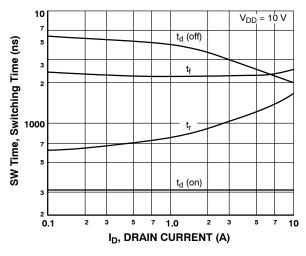


Figure 8. SW Time - ID

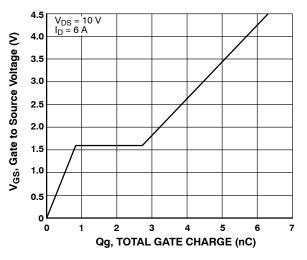
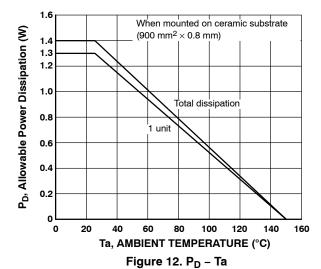


Figure 10. V_{GS} - Qg



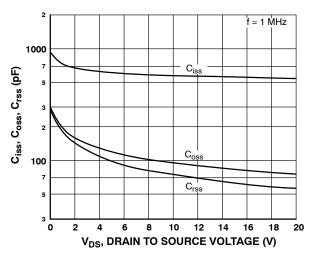


Figure 9. C_{iss} , C_{oss} , C_{rss} – V_{DS}

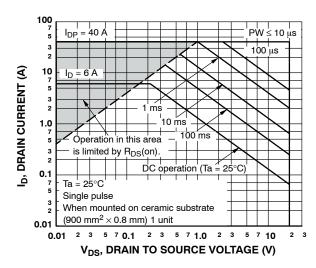
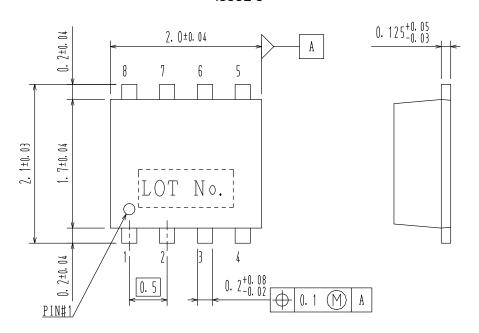
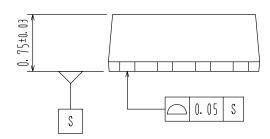


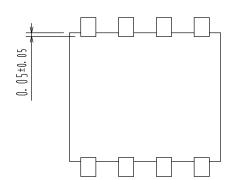
Figure 11. ASO

PACKAGE DIMENSIONS

SOT-383FL / EMH8 CASE 419AT ISSUE O







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