

# FFSB1265A

## Silicon Carbide Schottky Diode

650 V, 12 A

### Description

Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size and cost.

### Features

- Max Junction Temperature 175°C
- Avalanche Rated 72 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery/No Forward Recovery

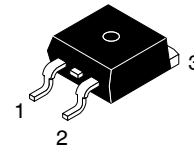
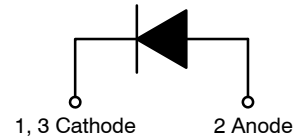
### Applications

- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits



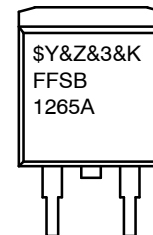
ON Semiconductor®

[www.onsemi.com](http://www.onsemi.com)



D<sup>2</sup>PAK-3 (TO-263, 3-LEAD)  
CASE 418AJ

### MARKING DIAGRAM



\$Y = On Semiconductor Logo  
&Z = Assembly Plant Code  
&3 = Numeric Date code  
&K = Lot Code  
FFSB1265A = Specific Device Code

### ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

# FFSB1265A

## ABSOLUTE MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Value	Unit	
V <sub>RRM</sub>	Peak Repetitive Reverse Voltage	650	V	
E <sub>AS</sub>	Single Pulse Avalanche Energy (Note 1)	72	mJ	
I <sub>F</sub>	Continuous Rectified Forward Current @ T <sub>C</sub> < 143°C	12	A	
	Continuous Rectified Forward Current @ T <sub>C</sub> < 135°C	14	A	
I <sub>F, Max</sub>	Non-Repetitive Peak Forward Surge Current	T <sub>C</sub> = 25°C, 10 μs	940	A
		T <sub>C</sub> = 150°C, 10 μs	890	A
I <sub>F, SM</sub>	Non-Repetitive Forward Surge Current	Half-Sine Pulse, t <sub>p</sub> = 8.3 ms	70	A
I <sub>F, RM</sub>	Repetitive Forward Surge Current	Half-Sine Pulse, t <sub>p</sub> = 8.3 ms	43	A
P <sub>tot</sub>	Power Dissipation	T <sub>C</sub> = 25°C	100	W
		T <sub>C</sub> = 150°C	17	W
T <sub>J, TSTG</sub>	Operating and Storage Temperature Range	-55 to +175	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

## THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
R <sub>θJC</sub>	Thermal Resistance, Junction to Case, Max	1.5	°C/W

## PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Marking	Package	Packing Method	Reel Size	Tape Width	Shipping <sup>†</sup>
FFSB1265A	FFSB1265A	D <sup>2</sup> -PAK	Tape and Reel	330 mm	24 mm	800 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## ELECTRICAL CHARACTERISTICS ON WAFER (T<sub>C</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
V <sub>F</sub>	Forward Voltage	I <sub>F</sub> = 12 A, T <sub>C</sub> = 25°C	-	1.50	1.75	V
		I <sub>F</sub> = 12 A, T <sub>C</sub> = 125°C	-	1.60	2.0	
		I <sub>F</sub> = 12 A, T <sub>C</sub> = 175°C	-	1.72	2.4	
I <sub>R</sub>	Reverse Current	V <sub>R</sub> = 650 V, T <sub>C</sub> = 25°C	-	-	200	μA
		V <sub>R</sub> = 650 V, T <sub>C</sub> = 125°C	-	-	400	
		V <sub>R</sub> = 650 V, T <sub>C</sub> = 175°C	-	-	600	
Q <sub>C</sub>	Total Capacitive Charge	V = 400 V	-	40	-	nC
C	Total Capacitance	V <sub>R</sub> = 1 V, f = 100 kHz	-	665	-	pF
		V <sub>R</sub> = 200 V, f = 100 kHz	-	74	-	
		V <sub>R</sub> = 400 V, f = 100 kHz	-	54	-	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

### NOTES:

- E<sub>AS</sub> of 72 mJ is based on starting T<sub>J</sub> = 25°C, L = 0.5 mH, I<sub>AS</sub> = 17 A, V = 50 V.

# FFSB1265A

## TYPICAL CHARACTERISTICS

( $T_J = 25^\circ\text{C}$  unless otherwise noted)

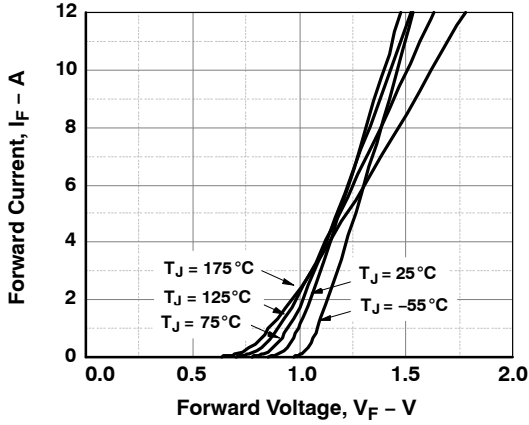


Figure 1. Forward Characteristics

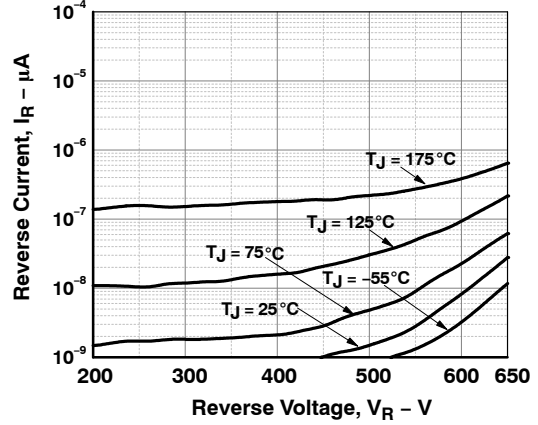


Figure 2. Reverse Characteristics

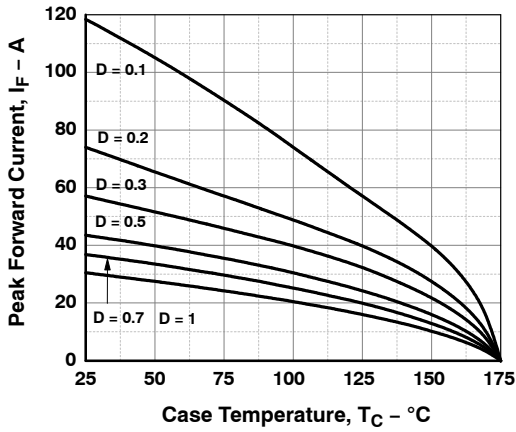


Figure 3. Current Derating

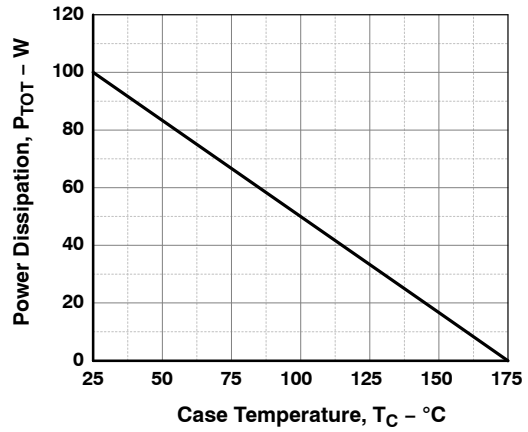


Figure 4. Power Derating

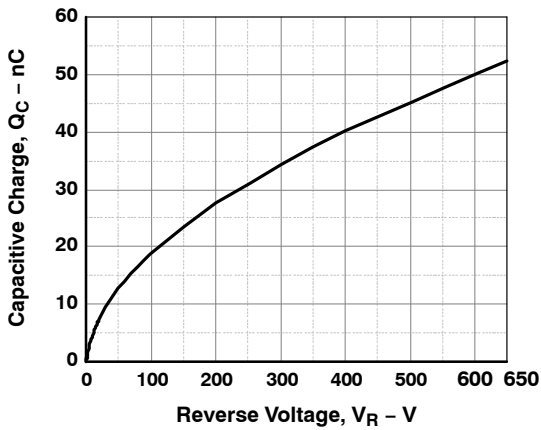


Figure 5. Capacitive Charge vs. Reverse Voltage

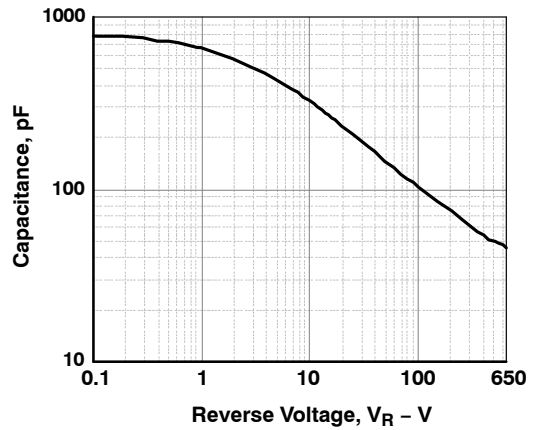


Figure 6. Capacitance vs. Reverse Voltage

# FFSB1265A

## TYPICAL CHARACTERISTICS

( $T_J = 25^\circ\text{C}$  unless otherwise noted)

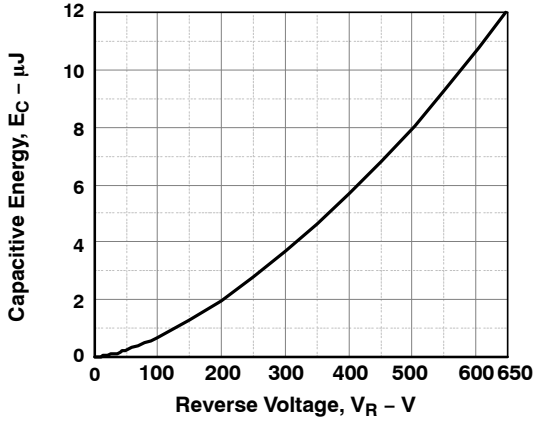


Figure 7. Capacitance Stored Energy

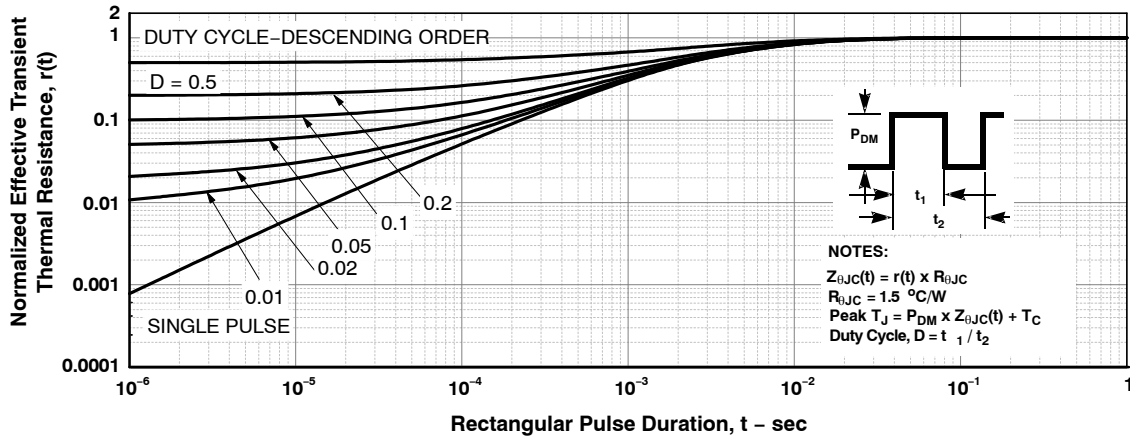


Figure 8. Junction-to-Case Transient Thermal Response Curve

## TEST CIRCUIT AND WAVEFORMS

$L = 0.5 \text{ mH}$   
 $R < 0.1 \Omega$   
 $V_{DD} = 50 \text{ V}$   
 $E_{AVL} = 1/2LI^2 [V_{R(AVL)} / (V_{R(AVL)} - V_{DD})]$   
 $Q1 = \text{IGBT} (BV_{CES} > \text{DUT } V_{R(AVL)})$

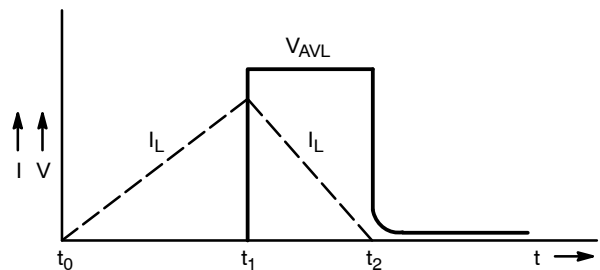
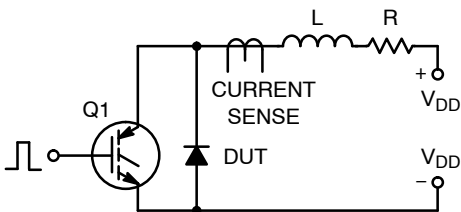


Figure 9. Unclamped Inductive Switching Test Circuit & Waveform

# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

ON Semiconductor®

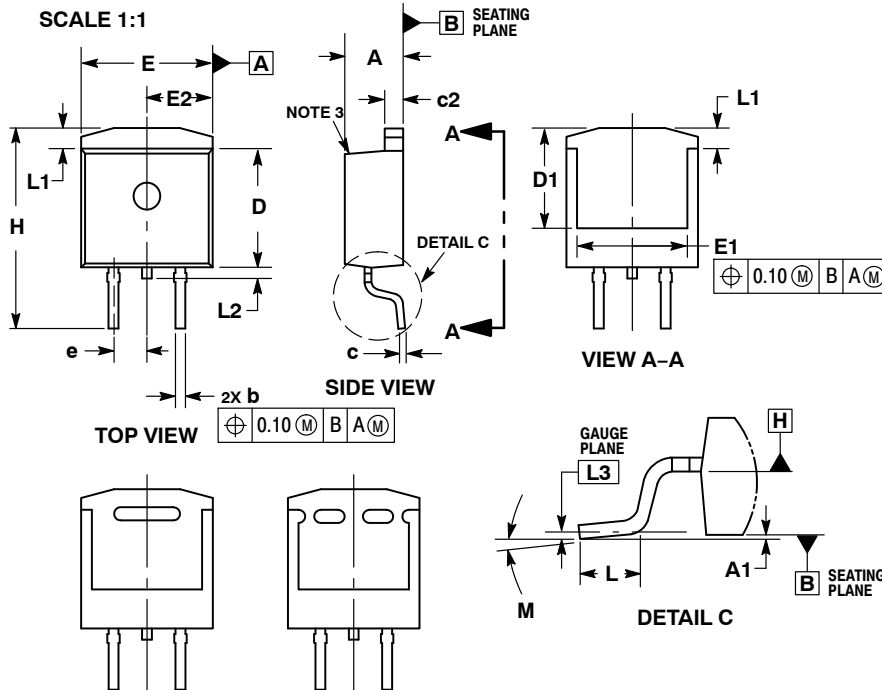
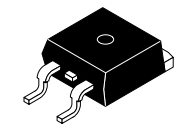


### D<sup>2</sup>PAK-3 (TO-263, 3-LEAD)

#### CASE 418AJ

#### ISSUE C

DATE 03 OCT 2018



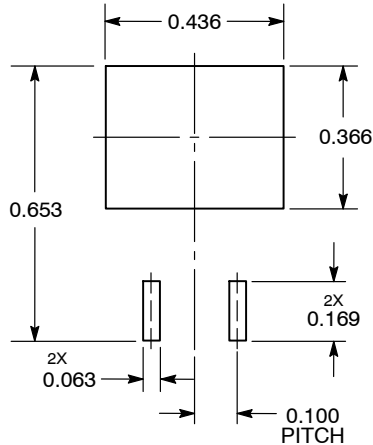
NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: INCHES.
3. CHAMFER OPTIONAL
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.005 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AT DATUM H.
5. THERMAL PAD CONTOUR IS OPTIONAL WITHIN DIMENSIONS E, L1, D1 AND E1.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.160	0.190	4.06	4.83
A1	0.000	0.010	0.00	0.25
b	0.020	0.039	0.51	0.99
c	0.012	0.029	0.30	0.74
c2	0.045	0.065	1.14	1.65
D	0.330	0.380	8.38	9.65
D1	0.260	---	6.60	---
E	0.380	0.420	9.65	10.67
E1	0.245	---	6.22	---
e	0.100 BSC		2.54 BSC	
H	0.575	0.625	14.60	15.88
L	0.070	0.110	1.78	2.79
L1	---	0.066	---	1.68
L2	---	0.070	---	1.78
L3	0.010 BSC		0.25 BSC	
M	-8°	8°	-8°	8°

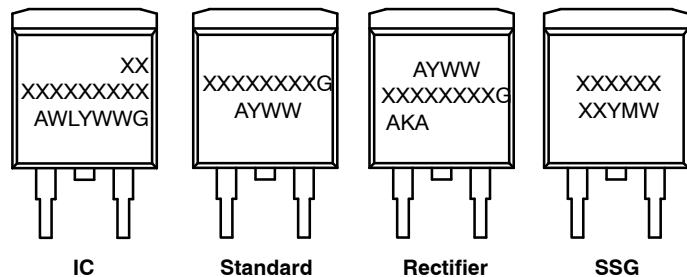
VIEW A-A  
OPTIONAL CONSTRUCTIONS

### RECOMMENDED SOLDERING FOOTPRINT\*



DIMENSIONS: INCHES

### GENERIC MARKING DIAGRAMS\*



- XXXXXX = Specific Device Code  
 A = Assembly Location  
 WL = Wafer Lot  
 Y = Year  
 WW = Work Week  
 W = Week Code (SSG)  
 M = Month Code (SSG)  
 G = Pb-Free Package  
 AKA = Polarity Indicator

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

DOCUMENT NUMBER:	98AON56370E	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	D <sup>2</sup> PAK-3 (TO-263, 3-LEAD)	PAGE 1 OF 1

ON Semiconductor and ON are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

ON Semiconductor and  are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## PUBLICATION ORDERING INFORMATION

### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor  
19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA  
**Phone:** 303-675-2175 or 800-344-3860 Toll Free USA/Canada  
**Fax:** 303-675-2176 or 800-344-3867 Toll Free USA/Canada  
**Email:** [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

**N. American Technical Support:** 800-282-9855 Toll Free  
USA/Canada  
**Europe, Middle East and Africa Technical Support:**  
Phone: 421 33 790 2910

**ON Semiconductor Website:** [www.onsemi.com](http://www.onsemi.com)

**Order Literature:** <http://www.onsemi.com/orderlit>

For additional information, please contact your local  
Sales Representative