Ultra Small Temperature Switch with Pin-Selectable Hysteresis

The MAX6501 and MAX6502 are SOT-23 temperature switches that require no external components and the design is facilitated with factory-programmed temperature thresholds. A choice of factory-trimmed temperature trip points are available. Pin selectable hysteresis of +2°C or +10°C allows flexibility to the design. These parts typically consume only 17 μ A of current and operate over the entire -55°C to +125°C temperature range while offering accuracies of ± 0.5 °C (typ) and ± 4 °C (max).

The MAX6501 has an open drain, active low output, meant for microprocessor reset control. The MAX6502 has a CMOS, active high output designed to drive a logic level MOSFET to turn on a fan or heater element.

The MAX6501/6502 are aimed for hot-temperature monitoring (+45°C to +115°C). These devices assert a logic signal when the temperature goes above the threshold.

The MAX6501 and MAX6502 are offered in five standard temperature thresholds. Available in 5-Pin SOT-23A packages, these parts are ideal for applications requiring high integration, small size, low power and low installed cost.

Features

- 5-Pin SOT-23A
- Factory-Programmed Thresholds from +45°C to +115°C in 10°C
 Increments
- Pin-Selectable +2°C or +10°C Hysteresis
- ±0.5°C (Typ) Threshold Accuracy Over Full Temperature Range
- No External Components Required
- 17 μA Supply Current

Typical Applications

- Thermal Management in PCs and Servers
- Over Temperature Fail Safe Circuits
- Simple Fan Controller
- Temperature Alarms
- Projectors/Printers
- Notebook Computers
- Network Boxes

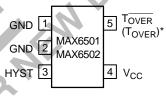


http://onsemi.com



SOT-23A CASE 1212

PIN CONNECTIONS



*For MAX6502

Note: 5-Pin SOT-23A is equivalent to EIAJ SC-74A

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 9 of this data sheet.

DEVICE MARKING INFORMATION

See general marking information in the device marking section on page 9 of this data sheet.

MAXIMUM RATINGS*

Rating	Symbol	Value	Unit
Supply Voltage	V _{CC}	-0.3 to +7.0	V
TOVER (MAX6501)	-	-0.3 to +7.0	V
TOVER (MAX6502)	-	-0.3 to (V _{CC} +0.3)	V
All Other Pins	-	-0.3 to (V _{CC} +0.3)	V
Input Current (All Pins)	-	20	mA
Output Current (All Pins)	-	20	mA
Operating Temperature Range	-	-55 to +125	°C
Storage Temperature Range	T _{stg}	-65 to +165	°C
Lead Temperature (Soldering, 10 seconds)	-	+300	°C
Power Dissipation ($T_A = +70^{\circ}C$) (Derate 7.1 mW/°C Above +70°C)	-	571	mW

^{*}Static-sensitive device. Unused devices must be stored in conductive material. Protect devices from static discharge and static fields. Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to Absolute Maximum Rating Conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS V_{CC} = +2.7 V to +5.5 V, $R_{PULL-UP}$ = 100 KΩ (MAX6501 only), $C_{COUPLING}$ = 100 pF from V_{CC} to GND, T_A = T_{MIN} to T_{MAX} , unless otherwise noted. Typical values are at T_A = +25°C.

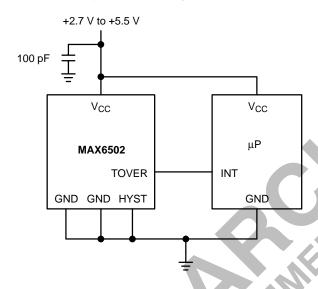
Characteristics	Test Conditions	Symbol	Min	Тур	Max	Unit
Supply Voltage Range	-	V _{CC}	2.7	-	5.5	V
Supply Current	-	Icc		17	40	μΑ
Temperature Threshold Accuracy (Note 1)	+45°C to +65°C +75°C to +115°C	ΔT_{TH}	-4.0 -6.0	± 0.5 ± 0.5	4.0 6.0	°C
Temperature Threshold Hysteresis	HYST = GND HYST = V _{CC}	T _{HYST}	-	2.0 10	-	°C
HYST Input Threshold		V_{IH}	0.8 x V _{CC}	-	-	V
HYST Input Threshold		V _{IL}	-	-	0.2 x V _{CC}	V
Output Voltage High	$I_{SOURCE} = 500 \mu A, V_{CC} > 2.7 \text{ V}$ (MAX6502 Only) $I_{SOURCE} = 800 \mu A, V_{CC} > 4.5 \text{ V}$ (MAX6502 Only)	V _{OH}	0.8 x V _{CC} V _{CC} -1.5	-	-	V
Output Voltage Low	$\begin{split} I_{SINK} = 1.2 \text{ mA}, V_{CC} > 2.7 \text{ V} \\ I_{SINK} = 3.2 \text{ mA}, V_{CC} > 4.5 \text{ V} \end{split}$	V _{OL}	-	-	0.3 0.4	V
Open-Drain Output Leakage Current	$V_{CC} = 2.7 \text{ V}, V_{TOVER} = 5.5 \text{ V}$ (MAX6501 Only)	-	-	10	-	nA

^{1.} The MAX6501 and MAX6502 are available with internal, factory-programmed temperature trip thresholds from +45°C to +115°C in +10°C increments.

PIN DESCRIPTION

MAX6501	MAX6502	Name	Description
1,2	1,2	GND	Ground. Ground both pins together close to the chip. Pin 2 provides the lowest thermal resistance to the die.
3	3	HYST	Hysteresis Input. Connect HYST to GND for +2°C hysteresis, or connect to V _{CC} for +10°C hysteresis.
4	4	V _{CC}	Supply Input (+2.7 V to +5.5 V). Recommend 100 pF or greater Coupling capacitor from V _{CC} to GND.
5	-	TOVER	Open-Drain, Active-Low Output. $\overline{\text{TOVER}}$ goes low when the die temperature exceeds the factory-programmed temperature threshold. Connect to a 100 K Ω pull-up resistor. May be pulled up to a voltage higher than V_{CC} .
-	5	TOVER	Push/Pull Active-High Output. TOVER goes high when the die temperature exceeds the factory-programmed temperature threshold.

Typical Operating Circuit



DETAILED DESCRIPTION

The MAX6501 and MAX6502 integrate a temperature sensor with a factory-programmed threshold switch. A logic signal is asserted when the die temperature crosses the factory programmed threshold. An external hysteresis input pin allows the user to select either 2°C or 10°C hysteresis to give further flexibility to the design of the application. The MAX6501 and MAX6502 are intended for a temperature range from 45°C to 115°C in a 10°C increment. The MAX6501 has an open drain output and the MAX6502 has a push/pull output stage.

The MAX6501 is intended for applications with a microprocessor reset input. The MAX6502 is intended for applications of turning on a fan or heater element.

Hysteresis Input

To prevent the output from "chattering" at or near the trip point temperature, a selectable HYST input pin is provided. Hysteresis can be externally selected at $2^{\circ}C$ (HYST = GND) or $10^{\circ}C$ (HYST = V_{DD}) by means of the CMOS compatible HYST input pin. Do not let the HYST pin float as this could cause increase supply current. The hysteresis does not depend on the part's programmed trip threshold.

Table 1. Factory-Programmed Threshold Range

Part Number	Threshold (T _{TH}) Range		
MAX6501	+45°C < T _{TH} < +115°C		
MAX6502	+45°C < T _{TH} < +115°C		

Thermal Considerations

With a very low 17 μ A supply current, the MAX6501 and MAX6502 dissipates very little power. Thus, the die temperature is basically the same as the package temperature. To minimize the error in temperature readings, the load current should be limited to a few milliamps. As an example, the typical thermal resistance of a 5-Pin SOT-23A package is 140°C/W. If the MAX6501 had to sink 1.0 mA, and the output voltage is guaranteed to be less than 0.3 V, then an additional 0.3 mW of power is dissipated within the IC. This corresponds to a 0.042°C rise in die temperature in the 5-Pin SOT-23A.

Temperature monitoring accuracy depends on the thermal resistance between the device being monitored and the temperature switch die. Heat flows primarily through the leads onto the die. Pin 2 provides the lowest thermal resistance to the die. To achieve the best temperature monitoring results, the MAX6501 and MAX6502 should be placed closest to the device being monitored. In addition, a short and wide copper trace from Pin 2 to the device should be used. In some cases, the 5- Pin SOT- 23A

package can be placed directly under the socketed microprocessor for improved thermal contact.

APPLICATIONS

The MAX6501 has an open drain output and is therefore intended to interface as a microprocessor reset input. Moreover, the combination of these two devices can be used to implement a temperature window alarm by wire-ORing the outputs and using an external pull up resistor. (See Figure 1)

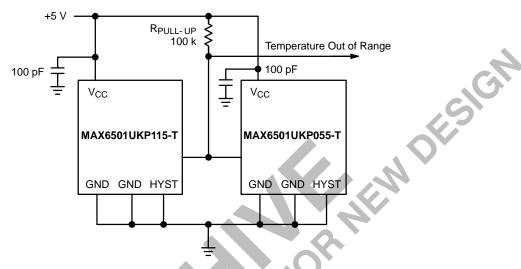


Figure 1. Over and Under Temperature Alarm

The MAX6502 can be used to control a DC fan. The fan turns on when the sensed temperature rises above the factory set threshold and remains on until the temperature falls below threshold minus the hysteresis selected. An additional

fail safe measure could be designed by using a second MAX6502 with a higher temperature threshold to alert the user of an impending thermal shutdown, should the temperature continue to rise. (See Figure 2)

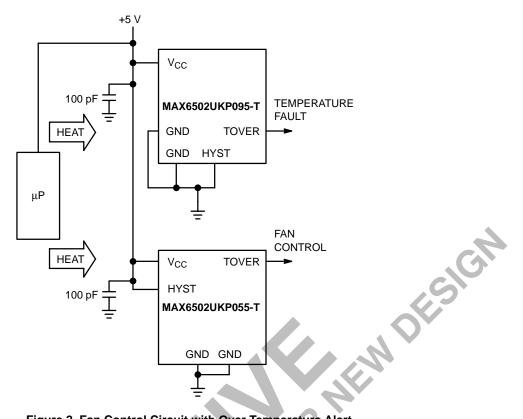


Figure 2. Fan Control Circuit with Over Temperature Alert

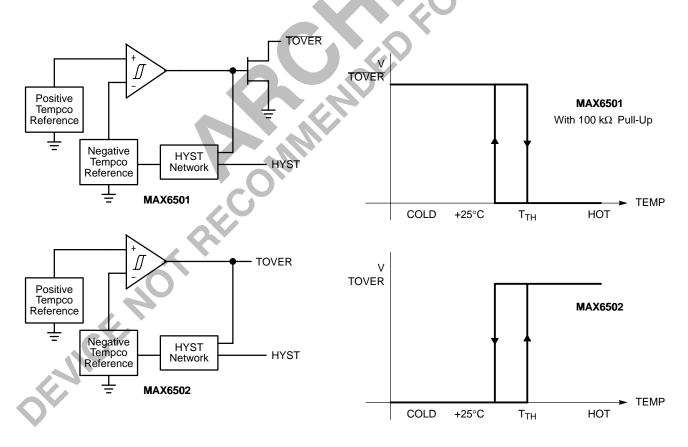
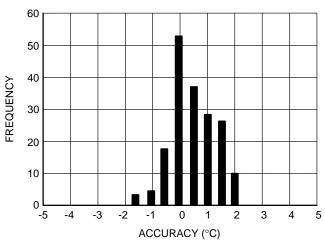


Figure 3. Functional Block Diagrams

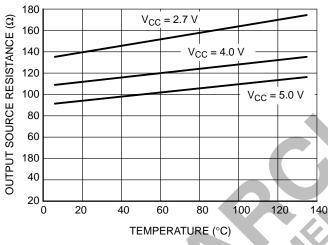
TYPICAL CHARACTERISTICS



40 35 SUPPLY CURRENT (µA) 30 25 20 15 10 5 -60 -40 -20 20 40 100 120 140 TEMPERATURE (°C)

Figure 4. Trip Threshold Accuracy

Figure 5. Supply Current vs. Temperature



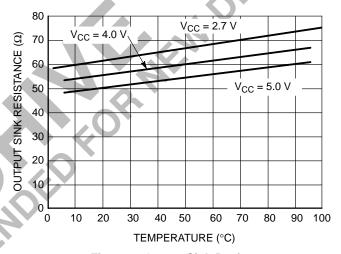


Figure 6. MAX6502 Output Source Resistance vs. Temperature

Figure 7. Output Sink Resistance vs. Temperature

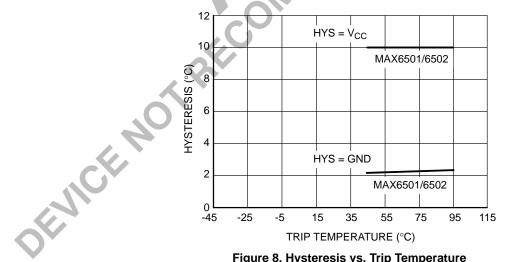


Figure 8. Hysteresis vs. Trip Temperature

TYPICAL CHARACTERISTICS

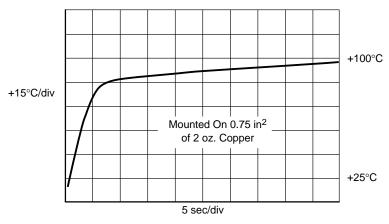


Figure 9. SOT-23 Thermal Step Response in Perfluorinated Fluid

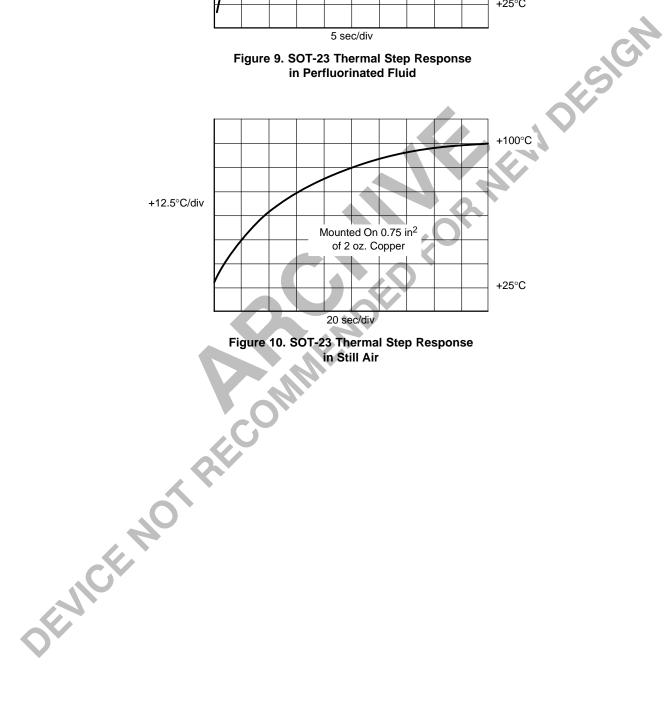
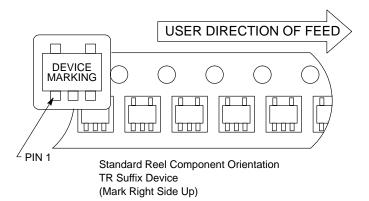
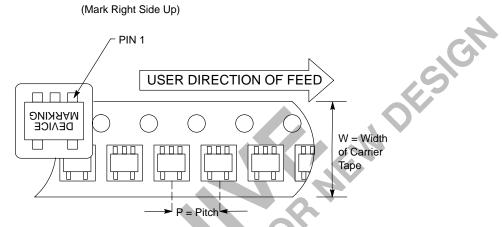


Figure 10. SOT-23 Thermal Step Response in Still Air

Component Taping Orientation for 5-Pin SOT-23A (EIAJ SC-74A) Devices





Reverse Reel Component Orientation RT Suffix Device (Mark Upside Down)

Carrier Tape, Reel Size, and Number of Components Per Reel

	Package	Carrier Width (W)	Pitch (P)	Part Per Full Reel	Reel Size
	SOT-23A	8 mm	4 mm	3000	7 inches
	CE NO	RECORD			75.155
OEVI					

MARKING DIAGRAM



- 1 and 2 = Two Letter Part Number Codes 3 = Year and Two-Month Period Code
- 4 = Lot ID Number

ORDERING INFORMATION

Device	Package	Standard Temperature Threshold	Output Stage	Marking ① and ②	Shipping
MAX6501UKP045-T		45°C	Open Drain	HA	
MAX6501UKP055-T*		55°C	Open Drain	HB	1.3
MAX6501UKP065-T		65°C	Open Drain	HC	
MAX6501UKP075-T		75°C	Open Drain	HD	
MAX6501UKP085-T		85°C	Open Drain	HE	
MAX6501UKP095-T*		95°C	Open Drain	HE	
MAX6501UKP105-T		105°C	Open Drain	HG	
MAX6501UKP115-T*	5 D' - 00T 00A	115°C	Open Drain	HH	accounts to Tana and Bank
MAX6502UKP045-T	5-Pin SOT-23A	45°C	Push/Pull	JA	3000 Units Tape and Reel
MAX6502UKP055-T*		55°C	Push/Pull	JB	
MAX6502UKP065-T		65°C	Push/Pull	JC	
MAX6502UKP075-T		75°C	Push/Pull	JD	
MAX6502UKP085-T		85°C	Push/Pull	JE	
MAX6502UKP095-T*		95°C	Push/Pull	JF	
MAX6502UKP105-T		105°C	Push/Pull	JG	
MAX6502UKP115-T*		115°C	Push/Pull	JH	
	OT PIE	OMIL			
MAX6502UKP115-T* *Default: Contact your ON S					

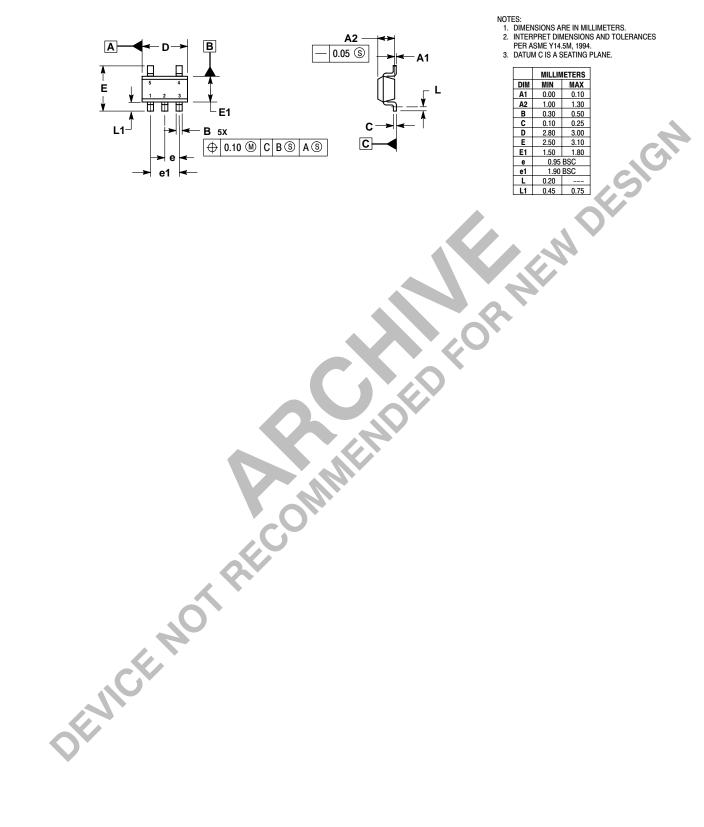
^{*}Default: Contact your ON Semiconductor sales representative for other temperature threshold options.

Notes



PACKAGE DIMENSIONS

SOT-23 CASE 1212-01 **ISSUE O**



- NOTES:
 1. DIMENSIONS ARE IN MILLIMETERS.
 2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994.
 3. DATUM C IS A SEATING PLANE.

	MILLIMETERS		
DIM	MIN	MAX	
A1	0.00	0.10	
A2	1.00	1.30	
В	0.30	0.50	
С	0.10	0.25	
D	2.80	3.00	
Е	2.50	3.10	
E1	1.50	1.80	
е	0.95 BSC		
e1	1.90 BSC		
L	0.20		



ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer.

PUBLICATION ORDERING INFORMATION

Literature Fulfillment:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada **Fax**: 303-675-2176 or 800-344-3867 Toll Free USA/Canada

Email: ONlit@hibbertco.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

JAPAN: ON Semiconductor, Japan Customer Focus Center 2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051

Phone: 81-3-5773-3850

ON Semiconductor Website: http://onsemi.com

For additional information, please contact your local

Sales Representative.