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December 2013



IRLS640A N-Channel Logic Level A-FET 200 V, 9.8 A, 180 mΩ

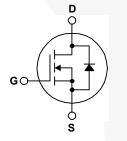
Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar, DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switching DC/DC converters, switch mode power supplies, DC-AC converters for uninterrupted power supply and motor control.

Features

- 9.8 A, 200 V, $R_{DS(on)}$ = 180 m Ω @ V_{GS} = 5 V Low Gate Charge (Typ. 40 nC)
- Low Crss (Typ. 95 pF)
- Fast Switching
- 100% Avalanche Tested
- Improved dv/dt Capability
- Logic-Level Gate Drive





Absolute Maximum Ratings

Symbol	Characteristic	Value	Units	
V _{DSS}	Drain-to-Source Voltage	200	V	
	Continuous Drain Current (T _C =25°C)	9.8		
Ι _D	Continuous Drain Current (T _c =100℃)	6.2	A	
I _{DM}	Drain Current-Pulsed (1)	63	А	
V _{GS}	Gate-to-Source Voltage	±20	V	
E _{AS} Single Pulsed Avalanche Energy ②		64	mJ	
I _{AR}	Avalanche Current (1)	18	A	
E _{AR}	Repetitive Avalanche Energy (1)	4.0	mJ	
dv/dt	Peak Diode Recovery dv/dt 3	5	V/ns	
	Total Power Dissipation (T _c =25℃)	40	W	
P _D	Linear Derating Factor	0.32	W/℃	
	Operating Junction and	55 to 1150		
T_J , T_STG	Storage Temperature Range	- 55 to +150		
	Maximum Lead Temp. for Soldering	200	°C	
TL	Purposes, 1/8 " from case for 5-seconds	300		

Thermal Resistance

Symbol	Characteristic	Тур.	Max.	Units
$R_{ extsf{ heta}JC}$	Junction-to-Case		3.13	°0111
$R_{_{ ext{ heta}JA}}$	Junction-to-Ambient		62.5	°C/W

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
IRLS640A	IRLS640A	TO-220F	Tube	N/A	N/A	50 units

Electrical Characteristics (T_c =25 °C unless otherwise specified)

Symbol	Characteristic		Characteristic Min. Typ. M		Max.	Units	Test Condition
BV _{DSS}	Drain-Source Breakdown Voltage				V	V _{GS} =0V,I _D =250µA	
$\Delta \text{BV} / \Delta \text{T}_{\text{J}}$	Breakdown Voltage Temp. Coeff.		0.17		V/℃	I _D =250µA See Fig 7	
V _{GS(th)}	Gate Threshold Voltage	1.0	-	2.0	V	V _{DS} =V _{GS} , I _D =250µA	
	Gate-Source Leakage, Forward			100	nA	V _{GS} =20V	
I _{GSS}	Gate-Source Leakage, Reverse			-100		V _{GS} =-20V	
	Drain to Source Lookage Current			10		V _{DS} =200V	
I _{DSS}	Drain-to-Source Leakage Current			100	μA	V _{DS} =160V,T _C =125 ℃	
R _{DS(on)}	Static Drain-Source On-State Resistance			0.18	Ω	V _{GS} =5V,I _D =4.9A ④	
9 _{fs}	Forward Transconductance		13.3		S	V _{DS} =40V,I _D =4.9A ④	
C _{iss}	Input Capacitance		1310	1705		(1 - 0)(1) = 25)(f - 1)(1)	
C _{oss}	Output Capacitance		200	250	pF	V_{GS} =0V, V_{DS} =25V,f =1MHz	
C _{rss}	Reverse Transfer Capacitance	-	95	120		See Fig 5	
t _{d(on)}	Turn-On Delay Time		11	30			
t _r	Rise Time Turn-Off Delay Time		8	25	ns	V _{DD} =100V,I _D =18A,	
t _{d(off)}			46	100		R _G =4.6Ω	
t _f	Fall Time		15	40		See Fig 13 4 5	
Qg	Total Gate Charge		40	56		V _{DS} =160V,V _{GS} =5V,	
Q _{gs}	Gate-Source Charge		6.8		nC	I _D =18A	
Q _{gd}	Gate-Drain("Miller") Charge		18.6			See Fig 6 & Fig 12 (4) (5)	

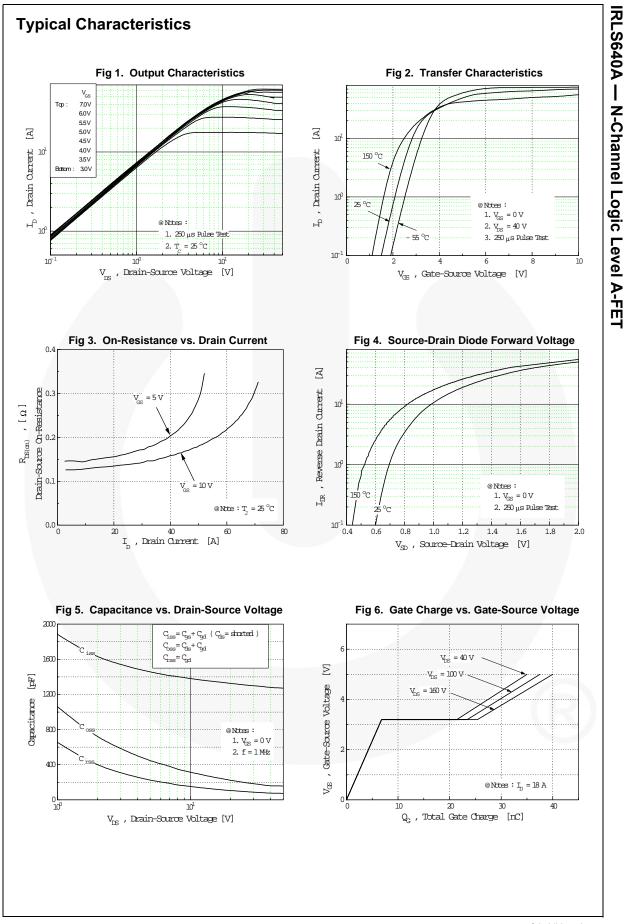
Source-Drain Diode Ratings and Characteristics

Symbol	Characteristic	Min.	Тур.	Max.	Units	Test Condition	
I _S	Continuous Source Current			18		Integral reverse pn-diode	
I _{SM}	Pulsed-Source Current (1)			63	A	in the MOSFET	
V _{SD}	Diode Forward Voltage ④			1.5	V	T _J =25℃,I _S =9.8A,V _{GS} =0V	
t _{rr}	Reverse Recovery Time		224		ns	T _J =25℃,I _F =18A	
Q _{rr}	Reverse Recovery Charge		1.55		μC	di _F /dt=100A/µs ④	

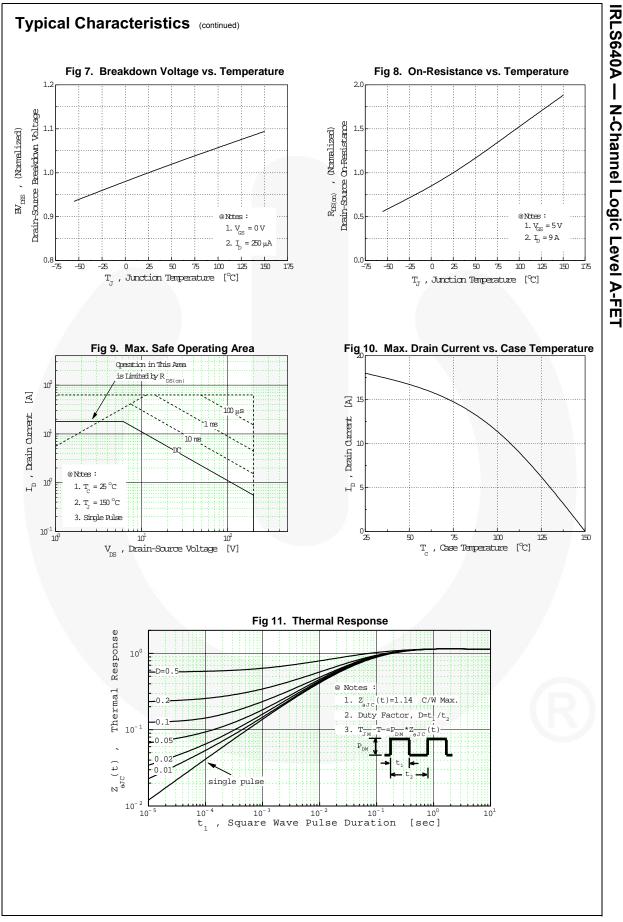
Notes :

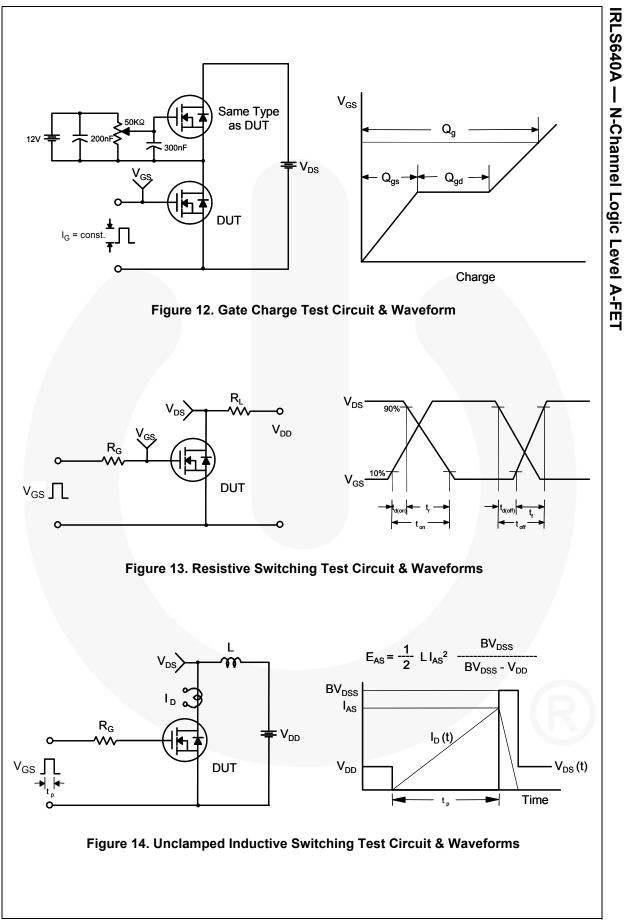
- ① Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature

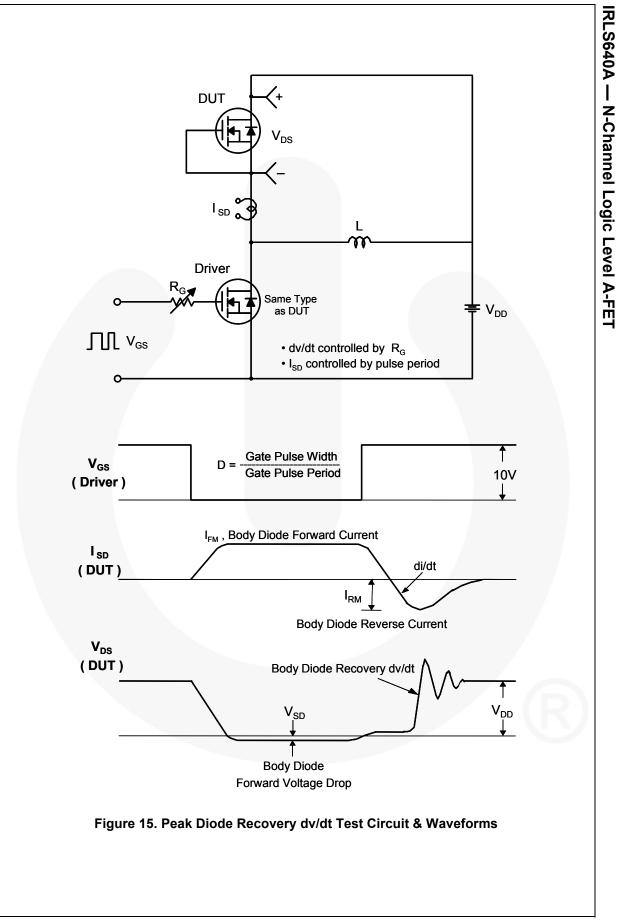
- 5 Essentially Independent of Operating Temperature

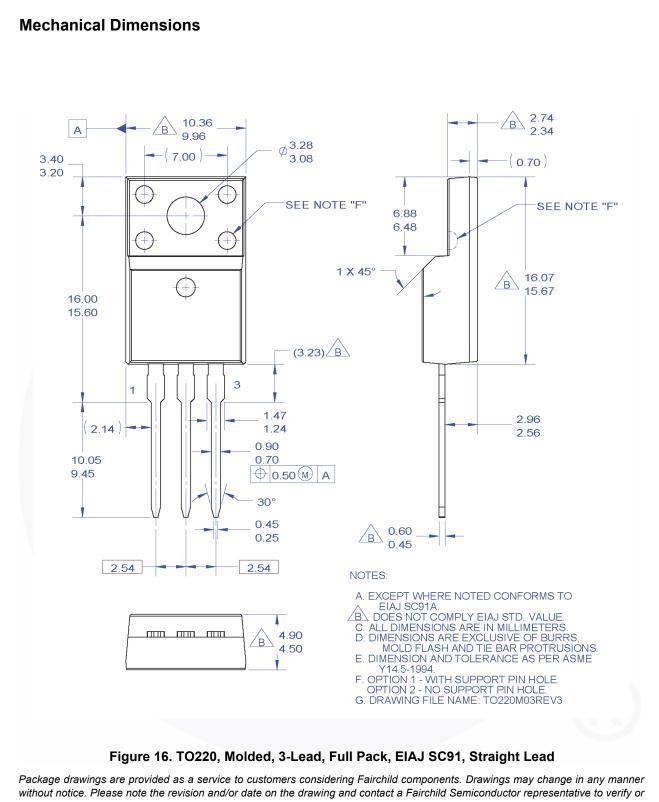


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IRLS640A —

N-Channel Logic Level A-FET



Preliminary	First Production	date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
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