60 V, 1 A NPN medium power transistors Rev. 1 — 23 June 2021

Product data sheet

1. General description

NPN medium power transistor series in a small SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

Table 1. Product overview

Type number	Package		number Package		
	Nexperia	JEITA			
BCP55-Q	SOT223	SC73	BCP52-Q		
BCP55-10-Q			BCP52-10-Q		
BCP55-16-Q			BCP52-16-Q		

2. Features and benefits

- High current
- Three current gain selections
- High power dissipation capability
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Linear voltage regulators
- Power management
- Low-side switches
- MOSFET drivers
- Battery-driven devices
- **Amplifiers**

4. Quick reference data

Table 2. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	60	V
I _C	collector current			-	-	1	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	2	Α
h _{FE}	DC current gain		,		'		
	BCP55-Q	V_{CE} = 2 V; I_{C} = 150 mA T_{amb} = 25 °C	[1]	63	-	250	
	BCP55-10-Q		[1]	63	-	160	
	BCP55-16-Q		[1]	100	-	250	

[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$



5. Pinning information

Table 3. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	4	С
2	С	collector		, /
3	Е	emitter		R—
4	С	collector	□ 1 □ 2 □ 3	Ė
				sym123

6. Ordering information

Table 4. Ordering information

Type number	Package		
	Name	Description	Version
BCP55-Q	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223
BCP55-10-Q			
BCP55-16-Q			

7. Marking

Table 5. Marking

Type number	Marking code
BCP55-Q	BCP55
BCP55-10-Q	BCP55 /10
BCP55-16-Q	BCP55 /16

8. Limiting values

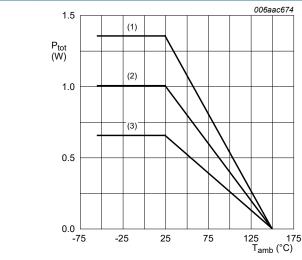
Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	60	V
V _{CEO}	collector-emitter voltage	open base		-	60	V
V_{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	2	А
I _B	base current			-	0.3	А
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	0.3	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.65	W
			[2]	-	1.00	W
			[3]	-	1.35	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

 Device mounted on an FR4 PCB, single-sided copper, tin-plated; mounting pad for collector 1 cm².
- Device mounted on an FR4 PCB, single-sided copper, tin-plated; mounting pad for collector 6 cm². [3]



- (1) FFR4 PCB, mounting pad for collector 6 cm²
- (2) FFR4 PCB, mounting pad for collector 1 cm²
- (3) FR4 PCB, standard footprint

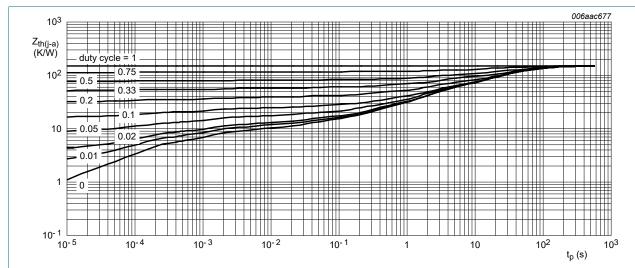
Fig. 1. Power derating curves SOT223

9. Thermal characteristics

Table 7. Thermal characteristics

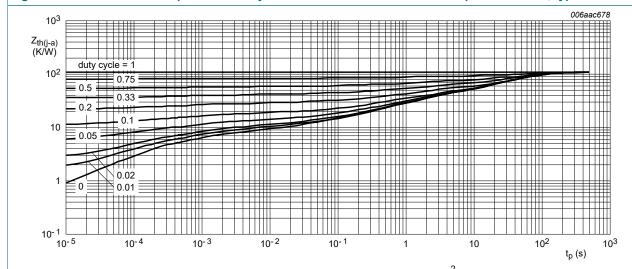
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	192	K/W
			[2]	-	-	125	K/W
			[3]	-	-	93	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	16	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated; monting pad for collector 1 cm².
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated; monting pad for collector 6 cm².



FR4 PCB, single-sided, tin-plated and standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



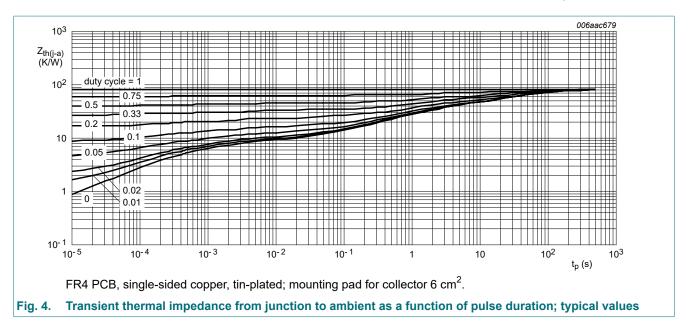
FR4 PCB, single-sided copper, tin-plated; mounting pad for collector 1 cm².

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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Nexperia BCP55-Q series

60 V, 1 A NPN medium power transistors

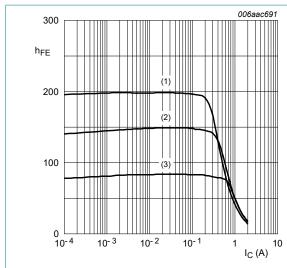


10. Characteristics

Table 8. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	$I_C = 100 \mu A; I_E = 0; T_{amb} = 25 °C$		60	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = 10 μA; I _B = 0 A; T _{amb} = 25 °C		60	-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _C = 0 A; I _E = 100 μA		5	-	-	V
I _{CBO}	collector-base	V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C		-	-	100	nA
	cut-off current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C		-	-	10	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C		-	-	100	nA
h _{FE}	DC current gain					'	_
	BCP55-Q	V _{CE} = 2 V; I _C = 5 mA; T _{amb} = 25 °C	[1]	63	-	-	
		V _{CE} = 2 V; I _C = 150 mA; T _{amb} = 25 °C	[1]	63	-	250	
		V _{CE} = 2 V; I _C = 500 mA; T _{amb} = 25 °C	[1]	40	-	-	
	BCP55-10-Q	V _{CE} = 2 V; I _C = 5 mA; T _{amb} = 25 °C	[1]	63	-	-	
		V _{CE} = 2 V; I _C = 150 mA; T _{amb} = 25 °C	[1]	63	-	160	
		V _{CE} = 2 V; I _C = 500 mA; T _{amb} = 25 °C	[1]	40	-	-	
	BCP55-16-Q	V _{CE} = 2 V; I _C = 5 mA; T _{amb} = 25 °C	[1]	63	-	-	
		V _{CE} = 2 V; I _C = 150 mA; T _{amb} = 25 °C	[1]	100	-	250	
		V _{CE} = 2 V; I _C = 500 mA; T _{amb} = 25 °C	[1]	40	-	-	
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 5 mA; T _{amb} = 25 °C	[1]	63	-	-	
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 500 mA; T _{amb} = 25 °C	[1]	40	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 500 mA; I _B = 50 mA; T _{amb} = 25 °C	[1]	-	-	0.5	V
V _{BE}	base-emitter voltage	V _{CE} = 2 V; I _C = 500 mA; T _{amb} = 25 °C	[1]	-	-	1	V
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = i_e = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 \text{ °C}$		-	6	-	pF
f _T	transition frequency	V _{CE} = 5 V; I _C = 50 mA; f = 100 MHz; T _{amb} = 25 °C		100	180	-	MHz

^[1] pulsed; $t_p \le 300 \ \mu s; \ \delta \le 0.02$

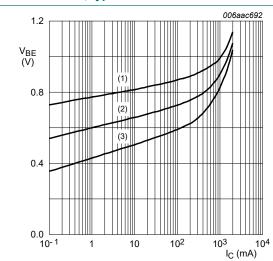


$$V_{CE} = 2 V$$

(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(3)
$$T_{amb} = -55$$
 °C

Fig. 5. DC current gain as a function of collector current; typical values



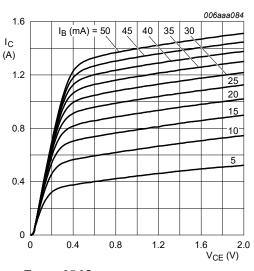
$$V_{CE} = 2 V$$

(1)
$$T_{amb} = -55$$
 °C

(2)
$$T_{amb}$$
 = 25 °C

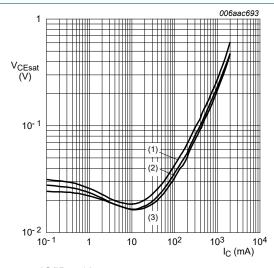
(3)
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 7. Base-emitter voltage as a function of collector current; typical values



 T_{amb} = 25 °C

Fig. 6. Collector current as a function of collectoremitter voltage; typical values



(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

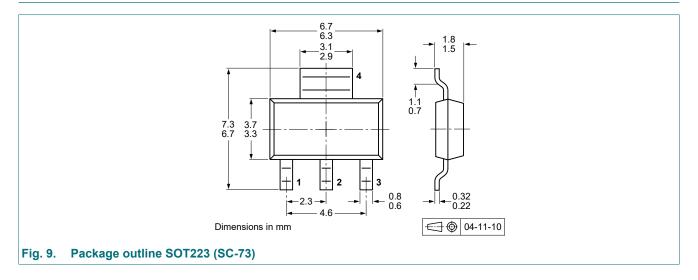
Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

11. Test information

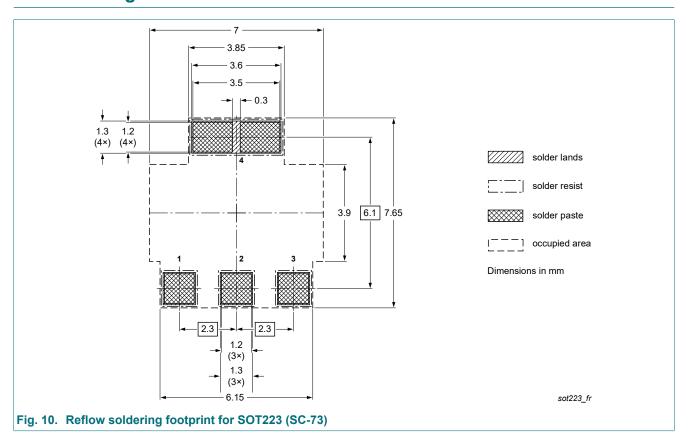
11.1. Quality information

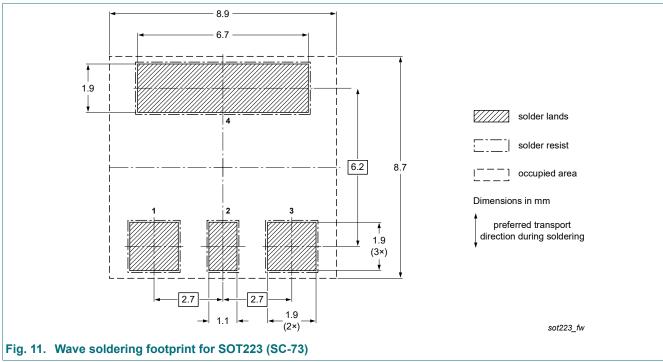
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

12. Package outline



13. Soldering





14. Revision history

Table 9. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BCP55-Q_SER v.1	20210623	Product data sheet	-	-

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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Product data sheet

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