

45 V, 100 mA NPN/NPN general-purpose transistor 8 July 2015

**Product data sheet** 

#### **General description** 1.

NPN/NPN general-purpose transistor in a leadless ultra small DFN1010B-6 (SOT1216) Surface-Mounted Device (SMD) plastic package.

PNP/PNP complement: BC857QAS.

NPN/PNP complement: BC847QAPN.

#### 2. **Features and benefits**

- Reduces component count •
- Reduces pick and place costs
- AEC-Q101 qualified •
- Low package height of 0.37 mm

#### **Applications** 3.

- General-purpose switching and amplification •
  - Mobile applications

#### Quick reference data 4.

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Table 1. Quie	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
Per transistor							
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	45	V
I <sub>C</sub>	collector current			-	-	100	mA
Per transistor							
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 5 V; I <sub>C</sub> = 2 mA; T <sub>amb</sub> = 25 °C		200	-	450	

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### 5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1		6 5 4
2	B1	base TR1		
3	C2	collector TR2	2 5	$\begin{pmatrix} TR1 \\ TR1 \end{pmatrix}$
4	E2	emitter TR2		
5	B2	base TR2		•
6	C1	collector TR1	Transparent top view	sym020
7	C1	collector TR1	DFN1010B-6 (SOT1216)	
8	C2	collector TR2		

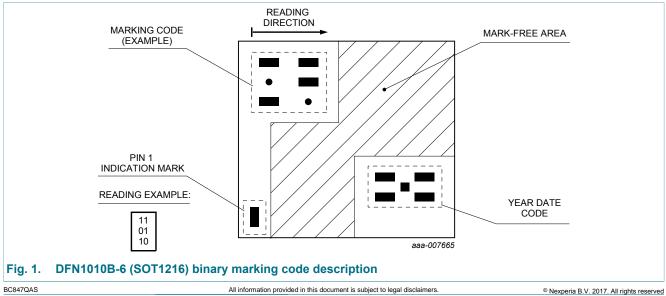
### 6. Ordering information

Table 3. Ordering in	formation		
Type number	Package		
	Name	Description	Version
BC847QAS	DFN1010B-6	DFN1010B-6: plastic thermal enhanced ultra thin small outline package; no leads; 6 terminals	SOT1216

### 7. Marking

#### Table 4.Marking codes

Type number	Marking code
BC847QAS	00 01 00



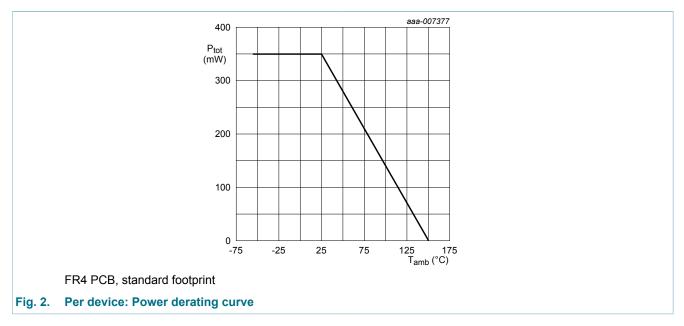
### 8. Limiting values

#### Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Мах	Unit
Per transist	tor	,	1			
V <sub>CBO</sub>	collector-base voltage	open emitter		-	50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	45	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	6	V
I <sub>C</sub>	collector current			-	100	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	200	mA
I <sub>BM</sub>	peak base current			-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	230	mW
Per device			1			
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	350	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

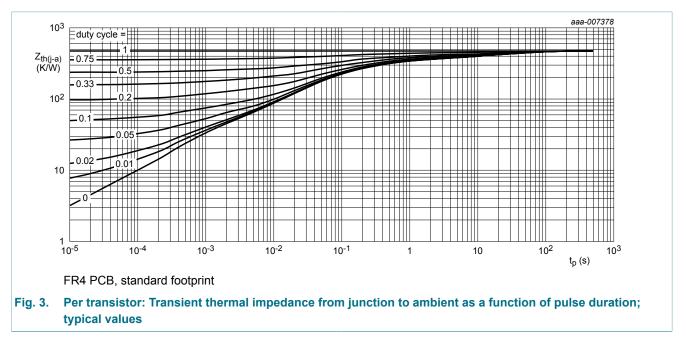


BC847QAS

### 9. Thermal characteristics

Table 6. The	rmal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
Per transistor							
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	543	K/W
Per device			· ·				
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	357	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.



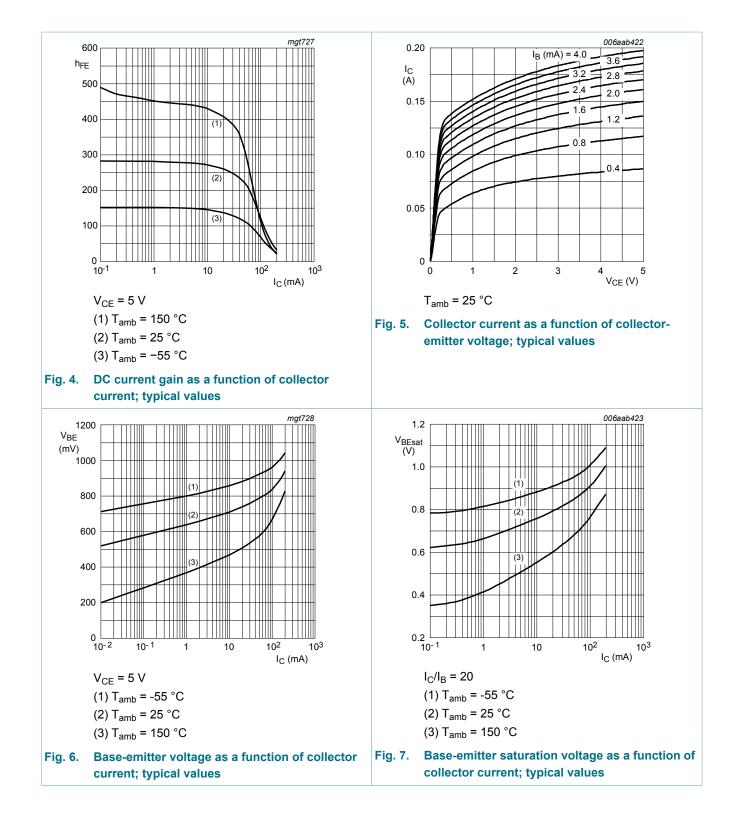
### **10. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Per transis	tor	· · _ /				_
I <sub>CBO</sub>	collector-base cut-off	$V_{CB}$ = 30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C	-	-	5	μA
	current	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	15	nA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB}$ = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	100	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = 5 V; I <sub>C</sub> = 2 mA; T <sub>amb</sub> = 25 °C	200	-	450	
V <sub>CEsat</sub>	collector-emitter	$I_{C}$ = 10 mA; $I_{B}$ = 0.5 mA; $T_{amb}$ = 25 °C	-	-	100	mV
saturation voltage	saturation voltage	$I_{C}$ = 100 mA; $I_{B}$ = 5 mA; pulsed; $t_{p} \le 300$ μs; δ ≤ 0.02; $T_{amb}$ = 25 °C	-	-	300	mV
V <sub>BEsat</sub> base-emit voltage	base-emitter saturation	$I_{C}$ = 10 mA; $I_{B}$ = 0.5 mA; $T_{amb}$ = 25 °C	-	760	-	mV
	voltage	$I_{C}$ = 100 mA; $I_{B}$ = 5 mA; pulsed; $t_{p} \le 300 \ \mu$ s; $\delta \le 0.02$ ; $T_{amb}$ = 25 °C	-	900	-	mV
V <sub>BE</sub>	base-emitter voltage	$V_{CE}$ = 5 V; I <sub>C</sub> = 2 mA; T <sub>amb</sub> = 25 °C	600	660	725	mV
		$V_{CE}$ = 5 V; $I_C$ = 10 mA; $T_{amb}$ = 25 °C	-	710	820	mV
C <sub>C</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = 0 A; i <sub>e</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	-	4	pF
C <sub>E</sub>	emitter capacitance	$V_{EB}$ = 0.5 V; I <sub>C</sub> = 0 A; f = 1 MHz; T <sub>amb</sub> = 25 °C	-	11	-	pF
f <sub>T</sub>	transition frequency	$V_{CE}$ = 5 V; I <sub>C</sub> = 10 mA; f = 100 MHz; T <sub>amb</sub> = 25 °C	100	-	-	MHz
NF	noise figure	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 0.2 mA; R <sub>S</sub> = 2 kΩ; f = 1 MHz; B = 200 Hz; T <sub>amb</sub> = 25 °C	-	-	10	dB

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#### 45 V, 100 mA NPN/NPN general-purpose transistor



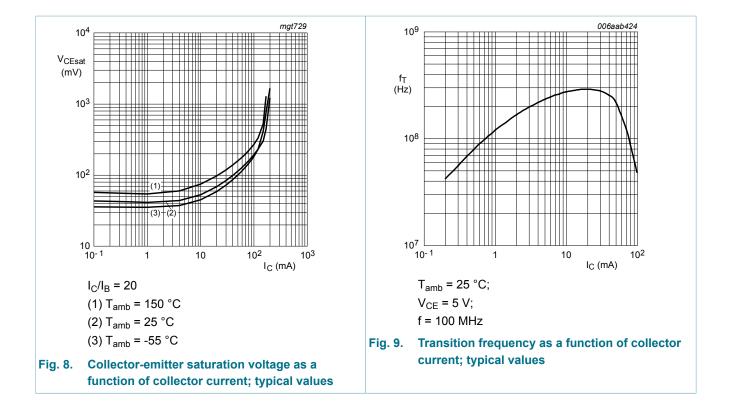
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#### 45 V, 100 mA NPN/NPN general-purpose transistor



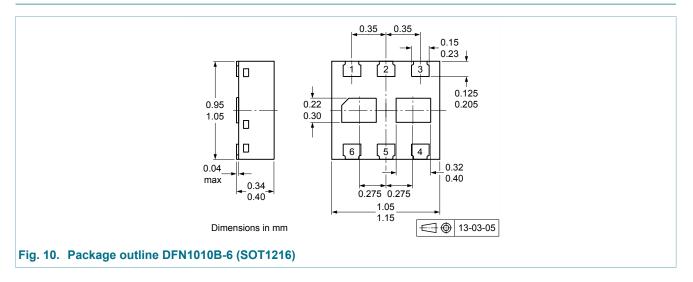
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### **11. Test information**

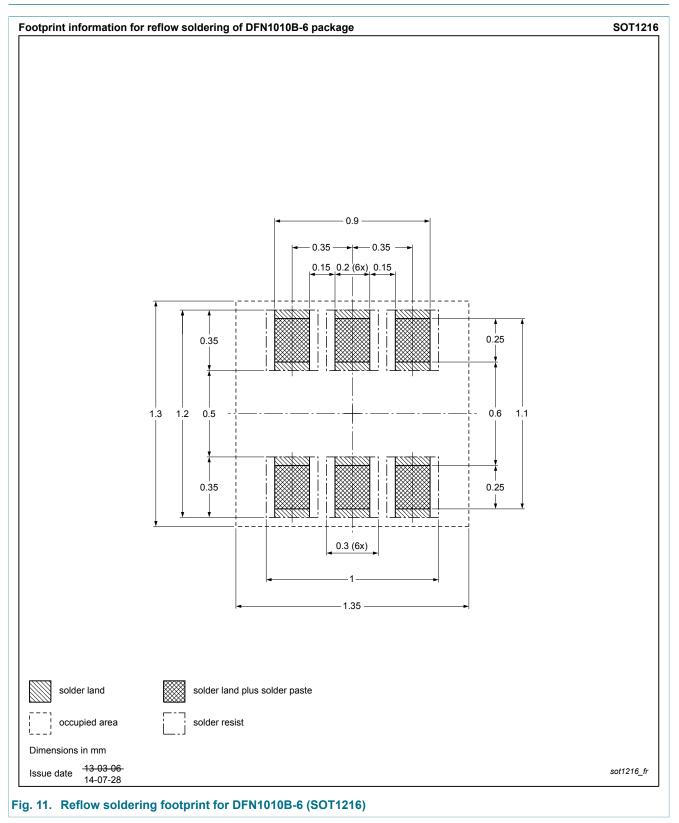
#### 11.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

### 12. Package outline



### 13. Soldering



BC847QAS

### 14. Revision history

Table 8. Revision his	story			
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BC847QAS v.2	20150708	Product data sheet	-	BC847QAS v.1
Modification:	Change of binary m	arking code position.		
BC847QAS v.1	20140729	Product data sheet	-	-

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### 15. Legal information

#### 15.1 Data sheet status

Document status [1][2]	Product status [ <u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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