

# 54AC/74AC241 • 54ACT/74ACT241 Octal Buffer/Line Driver with TRI-STATE® Outputs

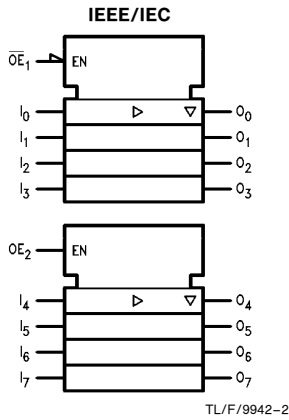
## General Description

The 'AC/'ACT241 is an octal buffer and line driver designed to be employed as a memory address driver, clock driver and bus-oriented transmitter or receiver which provides improved PC board density.

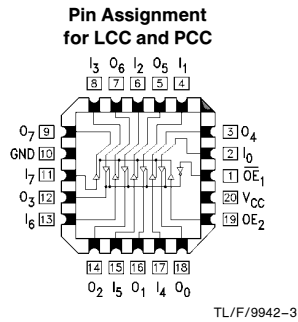
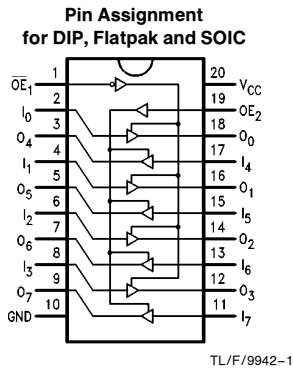
## Features

- $I_{CC}$  and  $I_{OZ}$  reduced by 50%
- Non-inverting TRI-STATE outputs drive bus lines or buffer memory address registers
- Outputs source/sink 24 mA
- 'ACT241 has TTL-compatible inputs
- Standard Military Drawing (SMD)
  - 'AC241: 5962-87551
  - 'ACT241: 5962-89847

## Logic Symbol



## Connection Diagrams



Pin Names	Description
$\overline{OE}_1$	TRI-STATE Output Enable Input
$OE_2$	TRI-STATE Output Enable Input (Active HIGH)
$I_0-I_7$	Inputs
$O_0-O_7$	Outputs

## Truth Tables

Inputs		Outputs (Pins 12, 14, 16, 18)
$\overline{OE}_1$	$I_n$	
L	L	L
L	H	H
H	X	Z

Inputs		Outputs (Pins 3, 5, 7, 9)
$OE_2$	$I_n$	
H	L	L
H	H	H
L	X	Z

H = HIGH Voltage Level  
 L = LOW Voltage Level  
 X = Immaterial  
 Z = High Impedance

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 FACT™ is a trademark of National Semiconductor Corporation.

## Absolute Maximum Rating (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage ( $V_{CC}$ )	-0.5V to +7.0V
DC Input Diode Current ( $I_{IK}$ )	
$V_I = -0.5V$	-20 mA
$V_I = V_{CC} + 0.5V$	+20 mA
DC Input Voltage ( $V_I$ )	-0.5V to $V_{CC} + 0.5V$
DC Output Diode Current ( $I_{OK}$ )	
$V_O = -0.5V$	-20 mA
$V_O = V_{CC} + 0.5V$	+20 mA
DC Output Voltage ( $V_O$ )	-0.5V to $V_{CC} + 0.5V$
DC Output Source or Sink Current ( $I_O$ )	±50 mA
DC $V_{CC}$ or Ground Current per Output Pin ( $I_{CC}$ or $I_{GND}$ )	±50 mA
Storage Temperature ( $T_{STG}$ )	-65°C to +150°C
Junction Temperature ( $T_J$ )	
CDIP	175°C
PDIP	140°C

**Note 1:** Absolute maximum ratings are those values beyond which damage to the device may occur. The databook specifications should be met, without exception, to ensure that the system design is reliable over its power supply, temperature, and output/input loading variables. National does not recommend operation of FACT™ circuits outside databook specifications.

## Recommended Operating Conditions

Supply Voltage ( $V_{CC}$ )	2.0V to 6.0V
'AC	4.5V to 5.5V
'ACT	
Input Voltage ( $V_I$ )	0V to $V_{CC}$
Output Voltage ( $V_O$ )	0V to $V_{CC}$
Operating Temperature ( $T_A$ )	
74AC/ACT	-40°C to +85°C
54AC/ACT	-55°C to +125°C
Minimum Input Edge Rate ( $\Delta V/\Delta t$ )	
'AC Devices	
$V_{IN}$ from 30% to 70% of $V_{CC}$	
$V_{CC}$ @ 3.3V, 4.5V, 5.5V	125 mV/ns
Minimum Input Edge Rate ( $\Delta V/\Delta t$ )	
'ACT Devices	
$V_{IN}$ from 0.8V to 2.0V	
$V_{CC}$ @ 4.5V, 5.5V	125 mV/ns

## DC Characteristics for 'AC Family Devices

Symbol	Parameter	$V_{CC}$ (V)	74AC		54AC		74AC		Units	Conditions
			$T_A = +25^\circ\text{C}$		$T_A = -55^\circ\text{C to } +125^\circ\text{C}$		$T_A = -40^\circ\text{C to } +85^\circ\text{C}$			
			Typ	Guaranteed Limits						
$V_{IH}$	Minimum High Level Input Voltage	3.0	1.5	2.1	2.1	2.1	2.1	2.1	V	$V_{OUT} = 0.1V$ or $V_{CC} - 0.1V$
		4.5	2.25	3.15	3.15	3.15	3.15	3.15		
		5.5	2.75	3.85	3.85	3.85	3.85	3.85		
$V_{IL}$	Maximum Low Level Input Voltage	3.0	1.5	0.9	0.9	0.9	0.9	0.9	V	$V_{OUT} = 0.1V$ or $V_{CC} - 0.1V$
		4.5	2.25	1.35	1.35	1.35	1.35	1.35		
		5.5	2.75	1.65	1.65	1.65	1.65	1.65		
$V_{OH}$	Minimum High Level Output Voltage	3.0	2.99	2.9	2.9	2.9	2.9	2.9	V	$I_{OUT} = -50 \mu\text{A}$
		4.5	4.49	4.4	4.4	4.4	4.4	4.4		
		5.5	5.49	5.4	5.4	5.4	5.4	5.4		
$V_{OL}$	Maximum Low Level Output Voltage	3.0		2.56	2.4	2.46			V	* $V_{IN} = V_{IL}$ or $V_{IH}$ -12 mA $I_{OH} -24 \text{ mA}$ -24 mA
		4.5	0.002	0.1	0.1	0.1	0.1	0.1		
		5.5	0.001	0.1	0.1	0.1	0.1	0.1		
$V_{OL}$	Maximum Low Level Output Voltage	3.0		0.36	0.50	0.44			V	* $V_{IN} = V_{IL}$ or $V_{IH}$ 12 mA $I_{OL} 24 \text{ mA}$ 24 mA
		4.5	0.002	0.1	0.1	0.1	0.1	0.1		
		5.5	0.001	0.1	0.1	0.1	0.1	0.1		
$I_{IN}$	Maximum Input Leakage Current	3.0		0.36	0.50	0.44			$\mu\text{A}$	$V_I = V_{CC}, \text{GND}$
		4.5		0.36	0.50	0.44				
		5.5		0.36	0.50	0.44				
$I_{IN}$	Maximum Input Leakage Current	5.5		±0.1	±1.0	±1.0			$\mu\text{A}$	$V_I = V_{CC}, \text{GND}$

\*All outputs loaded; thresholds on input associated with output under test.

### DC Characteristics for 'AC Family Devices (Continued)

Symbol	Parameter	V <sub>CC</sub> (V)	74AC		54AC	74AC	Units	Conditions
			T <sub>A</sub> = +25°C		T <sub>A</sub> = -55°C to +125°C	T <sub>A</sub> = -40°C to +85°C		
			Typ	Guaranteed Limits				
I <sub>OZ</sub>	Maximum TRI-STATE Leakage Current	5.5		±0.25	±5.0	±2.5	μA	V <sub>I</sub> (OE) = V <sub>IL</sub> , V <sub>IH</sub> V <sub>I</sub> = V <sub>CC</sub> , GND V <sub>O</sub> = V <sub>CC</sub> , GND
I <sub>OLD</sub>	†Minimum Dynamic Output Current	5.5			50	75	mA	V <sub>OLD</sub> = 1.65V Max
I <sub>OHD</sub>		5.5			-50	-75	mA	V <sub>OHD</sub> = 3.85V Min
I <sub>CC</sub>	Maximum Quiescent Supply Current	5.5		4.0	80.0	40.0	μA	V <sub>IN</sub> = V <sub>CC</sub> or GND

†Maximum test duration 2.0 ms, one output loaded at a time.

**Note:** I<sub>IN</sub> and I<sub>CC</sub> @ 3.0V are guaranteed to be less than or equal to the respective limit @ 5.5V V<sub>CC</sub>.  
I<sub>CC</sub> for 54AC @ 25°C is identical to 74AC @ 25°C.

### DC Characteristics for 'ACT Family Devices

Symbol	Parameter	V <sub>CC</sub> (V)	74ACT		54ACT	74ACT	Units	Conditions
			T <sub>A</sub> = +25°C		T <sub>A</sub> = -55°C to +125°C	T <sub>A</sub> = -40°C to +85°C		
			Typ	Guaranteed Limits				
V <sub>IH</sub>	Minimum High Level Input Voltage	4.5	1.5	2.0	2.0	2.0	V	V <sub>OUT</sub> = 0.1V or V <sub>CC</sub> - 0.1V
		5.5	1.5	2.0	2.0	2.0		
V <sub>IL</sub>	Maximum Low Level Input Voltage	4.5	1.5	0.8	0.8	0.8	V	V <sub>OUT</sub> = 0.1V or V <sub>CC</sub> - 0.1V
		5.5	1.5	0.8	0.8	0.8		
V <sub>OH</sub>	Minimum High Level Output Voltage	4.5	4.49	4.4	4.4	4.4	V	I <sub>OUT</sub> = -50 μA
		5.5	5.49	5.4	5.4	5.4		
V <sub>OL</sub>	Maximum Low Level Output Voltage	4.5	0.001	0.1	0.1	0.1	V	I <sub>OUT</sub> = 50 μA
		5.5	0.001	0.1	0.1	0.1		
V <sub>OL</sub>	Maximum Low Level Output Voltage	4.5		0.36	0.50	0.44	V	*V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> 24 mA I <sub>OL</sub> 24 mA
		5.5		0.36	0.50	0.44		
I <sub>IN</sub>	Maximum Input Leakage Current	5.5		±0.1	±1.0	±1.0	μA	V <sub>I</sub> = V <sub>CC</sub> , GND
I <sub>OZ</sub>	Maximum TRI-STATE Leakage Current	5.5		±0.25	±5.0	±2.5	μA	V <sub>I</sub> = V <sub>IL</sub> , V <sub>IH</sub> V <sub>O</sub> = V <sub>CC</sub> , GND
I <sub>CCT</sub>	Maximum I <sub>CC</sub> /Input	5.5	0.6		1.6	1.5	mA	V <sub>I</sub> = V <sub>CC</sub> - 2.1V
I <sub>OLD</sub>	†Minimum Dynamic Output Current	5.5			50	75	mA	V <sub>OLD</sub> = 1.65V Max
I <sub>OHD</sub>		5.5			-50	-75	mA	V <sub>OHD</sub> = 3.85V Min
I <sub>CC</sub>	Maximum Quiescent Supply Current	5.5		4.0	80.0	40.0	μA	V <sub>IN</sub> = V <sub>CC</sub> or GND

\*All outputs loaded; thresholds on input associated with output under test.

†Maximum test duration 2.0 ms, one output loaded at a time.

**Note:** I<sub>CC</sub> for 54ACT @ 25°C is identical to 74ACT @ 25°C.

## AC Electrical Characteristics

Symbol	Parameter	V <sub>CC</sub> * (V)	74AC			54AC		74AC		Units
			T <sub>A</sub> = +25°C C <sub>L</sub> = 50 pF			T <sub>A</sub> = -55°C to +125°C C <sub>L</sub> = 50 pF		T <sub>A</sub> = -40°C to +85°C C <sub>L</sub> = 50 pF		
			Min	Typ	Max	Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay Data to Output	3.3 5.0	1.5 1.5	6.0 5.0	9.0 7.0	1.0 1.0	12.0 9.5	1.5 1.0	10.0 7.5	ns
t <sub>PHL</sub>	Propagation Delay Data to Output	3.3 5.0	1.5 1.5	6.0 4.5	9.0 7.0	1.0 1.0	11.5 9.0	1.0 1.0	10.5 7.5	ns
t <sub>PZH</sub>	Output Enable Time	3.3 5.0	1.5 1.5	6.5 5.5	12.5 9.0	1.0 1.0	13.0 10.0	1.0 1.0	13.0 9.5	ns
t <sub>PZL</sub>	Output Enable Time	3.3 5.0	1.5 1.5	7.0 5.5	12.0 9.0	1.0 1.0	13.0 10.0	1.5 1.0	13.0 9.5	ns
t <sub>PHZ</sub>	Output Disable Time	3.3 5.0	2.0 1.5	8.0 6.5	12.0 10.0	1.0 1.0	13.0 11.5	2.0 1.0	12.5 10.5	ns
t <sub>PLZ</sub>	Output Disable Time	3.3 5.0	1.5 1.5	7.0 6.0	12.5 10.0	1.0 1.0	13.0 11.5	1.0 1.0	13.0 10.5	ns

\*Voltage Range 3.3 is 3.3V ± 3.3V  
Voltage Range 5.0 is 5.0V ± 0.5V

## AC Electrical Characteristics

Symbol	Parameter	V <sub>CC</sub> * (V)	74ACT			54ACT		74ACT		Units
			T <sub>A</sub> = +25°C C <sub>L</sub> = 50 pF			T <sub>A</sub> = -55°C to +125°C C <sub>L</sub> = 50 pF		T <sub>A</sub> = -40°C to +85°C C <sub>L</sub> = 50 pF		
			Min	Typ	Max	Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay Data to Output	5.0	1.5	6.5	9.0	1.0	10.0	1.5	10.0	ns
t <sub>PHL</sub>	Propagation Delay Data to Output	5.0	1.5	7.0	9.0	1.0	10.0	1.5	10.0	ns
t <sub>PZH</sub>	Output Enable Time	5.0	1.5	6.0	9.0	1.0	11.5	1.0	10.0	ns
t <sub>PZL</sub>	Output Enable Time	5.0	1.5	7.0	10.0	1.0	12.5	1.5	11.0	ns
t <sub>PHZ</sub>	Output Disable Time	5.0	1.5	8.0	10.5	1.0	12.5	1.5	11.5	ns
t <sub>PLZ</sub>	Output Disable Time	5.0	2.0	7.0	10.5	1.0	12.5	1.5	11.5	ns

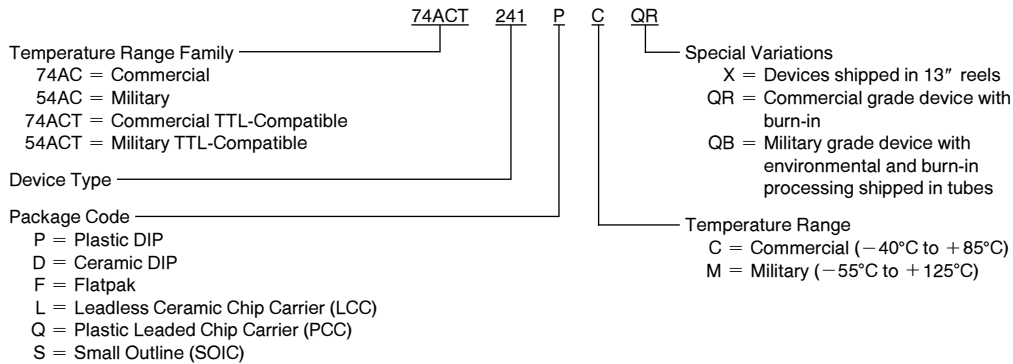
\*Voltage Range 5.0 is 5.0V ± 0.5V

## Capacitance

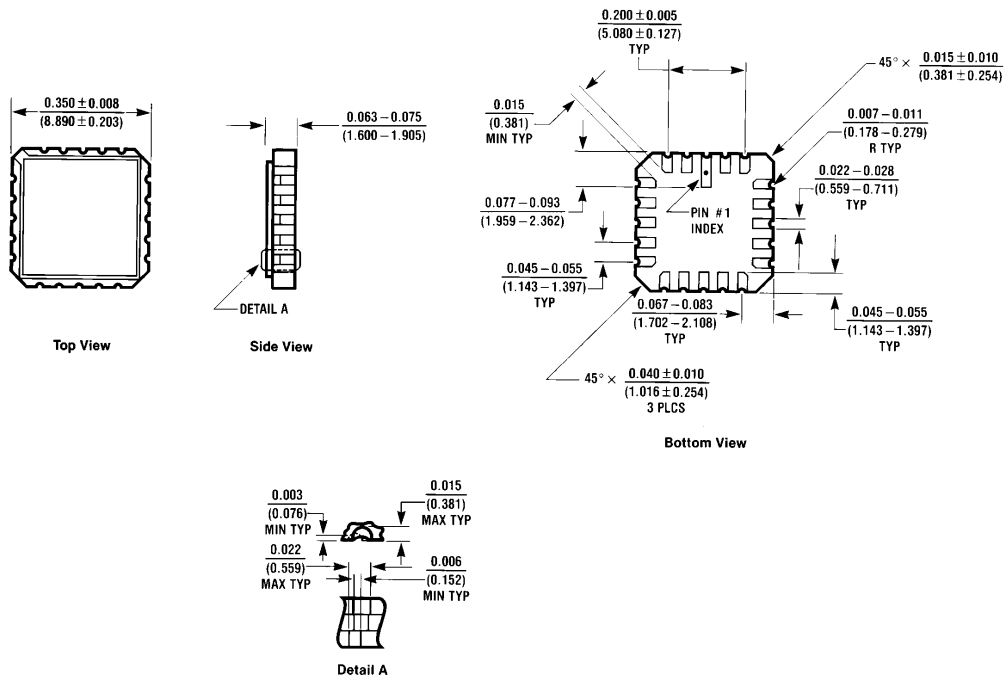
Symbol	Parameter	Typ	Units	Conditions
C <sub>IN</sub>	Input Capacitance	4.5	pF	V <sub>CC</sub> = OPEN
C <sub>PD</sub>	Power Dissipation Capacitance	45.0	pF	V <sub>CC</sub> = 5.0V

## Ordering Information

The device number is used to form part of a simplified purchasing code where the package type and temperature range are defined as follows:



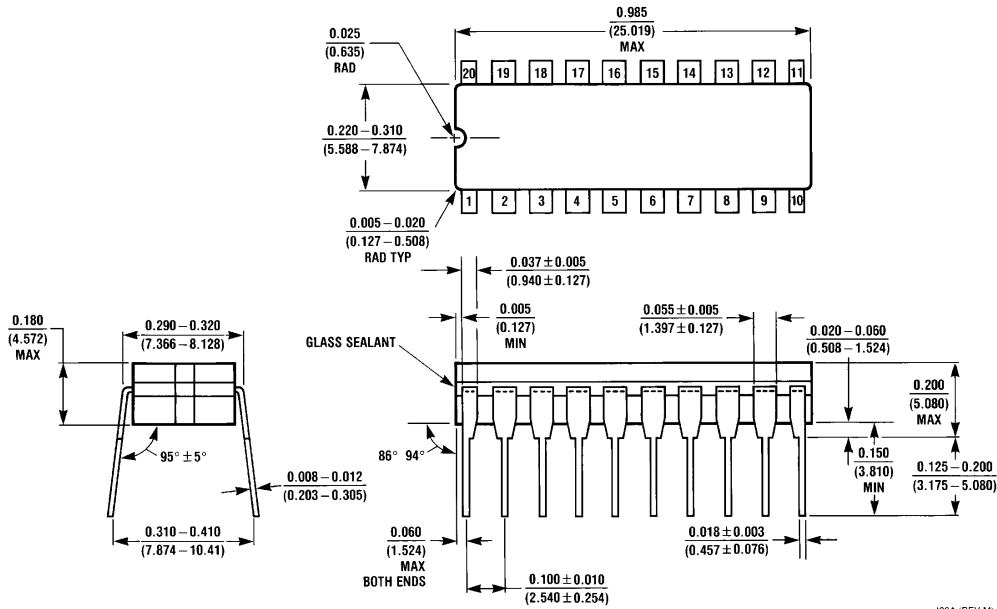
## Physical Dimensions inches (millimeters)



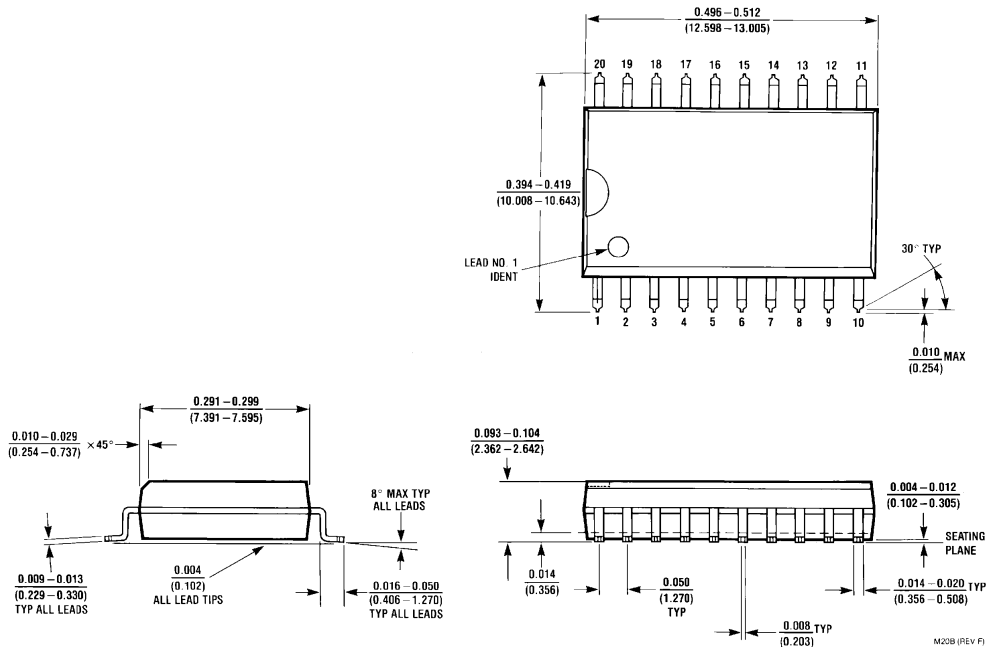
**20 Terminal Ceramic Leadless Chip Carrier (L)  
NS Package Number E20A**

E20A (REV D)

**Physical Dimensions** inches (millimeters) (Continued)

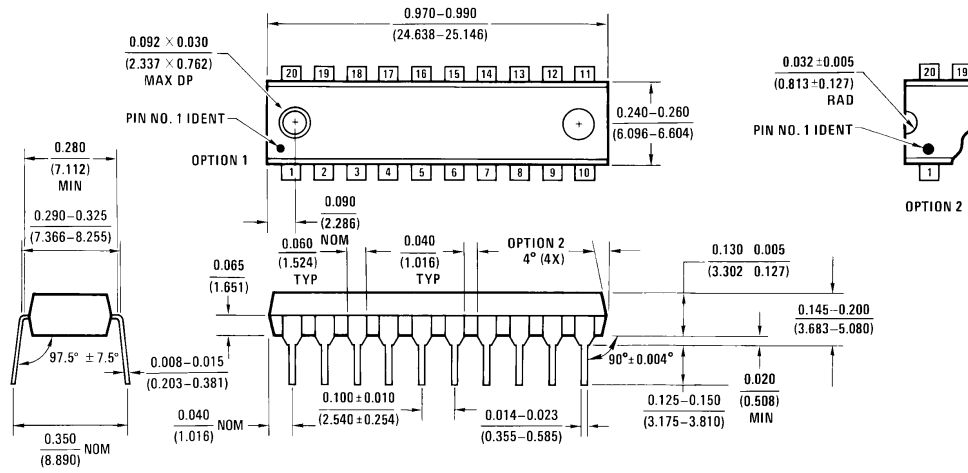


**20 Lead Ceramic Dual-In-Line Package (D)**  
NS Package Number J20A

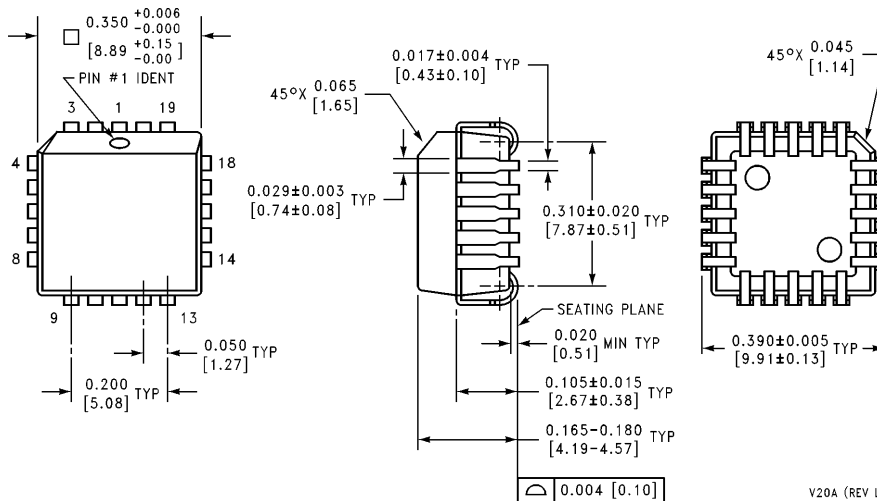


**20 Lead Small Outline Integrated Circuit (S)**  
NS Package Number M20B

# Physical Dimensions inches (millimeters) (Continued)

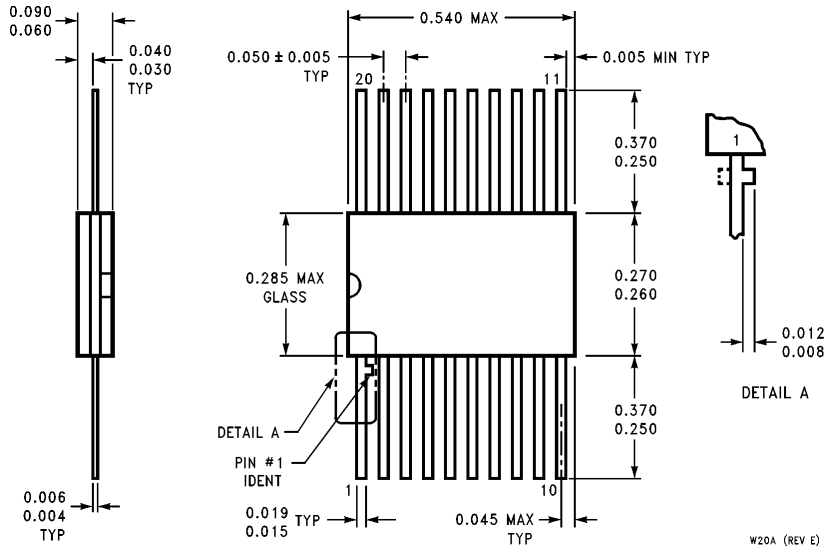


**20 Lead Plastic Dual-In-Line Package (P)**  
**NS Package Number N20B**



**20 Lead Plastic Chip Carrier (Q)**  
**NS Package Number V20A**

**Physical Dimensions** inches (millimeters) (Continued)



**20 Lead Ceramic Flatpak (F)  
NS Package Number W20A**

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