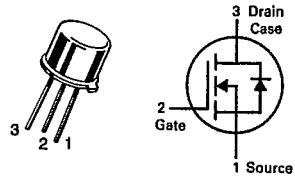


**2N6790****CASE 79-05, STYLE 6  
TO-39 (TO-205AF)****TMOS FET  
TRANSISTOR**

N-CHANNEL — ENHANCEMENT

**4****MAXIMUM RATINGS**

| Rating  | Symbol            | Value      | Unit         |
|---|-------------------|------------|--------------|
| Drain-Source Voltage  | $V_{DSS}$         | 200        | Vdc          |
| Drain-Gate Voltage ( $R_{GS} = 1.0 \text{ m}\Omega$ )                                 | $V_{DG}$          | 200        | Vdc          |
| Gate-Source Voltage   | $V_{GS}$          | $\pm 20$   | Vdc          |
| Drain Current<br>Continuous<br>Pulsed   | $I_D$<br>$I_{DM}$ | 3.5<br>14  | Adc          |
| Total Power Dissipation @ $T_C = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$ | $P_D$             | 20<br>0.16 | Watts<br>W/C |
| Operating and Storage<br>Temperature Range  | $T_J, T_{stg}$    | -55 to 150 | °C           |

**THERMAL CHARACTERISTICS**

| Thermal Resistance Junction to Case                   | $R_{\theta JC}$ | 6.25 | °C/W |
|---|-----------------|------|------|
| Thermal Resistance Junction to Ambient                | $R_{\theta JA}$ | 175  | °C/W |
| Maximum Lead Temperature<br>1.6 mm from Case for 10 s | $T_L$           | 300  | °C   |

ELECTRICAL CHARACTERISTICS ( $T_C = 25^\circ\text{C}$  unless otherwise noted.)

| Characteristic  | Symbol        | Min | Max         | Unit            |
|---|---------------|-----|-------------|-----------------|
| <b>OFF CHARACTERISTICS</b>  |               |     |             |                 |
| Drain-Source Breakdown Voltage ( $V_{GS} = 0, I_D = 0.25 \text{ mA}$ )  | $V_{(BR)DSS}$ | 200 | —           | Vdc             |
| Zero Gate Voltage Drain Current<br>( $V_{DS} = \text{Rated } V_{DSS}, V_{GS} = 0$ )<br>( $V_{DS} = 0.8 \text{ Rated } V_{DSS}, V_{GS} = 0, T_J = 125^\circ\text{C}$ ) | $I_{DSS}$     | —   | 250<br>1000 | $\mu\text{Adc}$ |
| Gate-Body Leakage Current, Forward ( $V_{GS} = 20 \text{ Vdc}, V_{DS} = 0$ )  | $I_{GSSF}$    | —   | 100         | nAdc            |
| Gate-Body Leakage Current, Reverse ( $V_{GS} = -20 \text{ Vdc}, V_{DS} = 0$ )   | $I_{GSSR}$    | —   | -100        | nAdc            |

**ON CHARACTERISTICS\***

|  |                     |     |            |      |
|--|---------------------|-----|------------|------|
| Gate Threshold Voltage ( $V_{DS} = V_{GS}, I_D = 1.0 \text{ mA}$ )                         | $V_{GS(\text{th})}$ | 2.0 | 4.0        | Vdc  |
| Static Drain-Source On-Resistance<br>( $V_{GS} = 10 \text{ Vdc}, I_D = 2.25 \text{ Adc}$ ) | $r_{DS(\text{on})}$ | —   | 0.8<br>1.5 | Ohm  |
| Drain-Source On-Voltage ( $V_{GS} = 10 \text{ V}, I_D = 3.5 \text{ Adc}$ )                 | $V_{DS(\text{on})}$ | —   | 2.8        | Vdc  |
| Forward Transconductance ( $V_{DS} = 5.0 \text{ V}, I_D = 2.25 \text{ Adc}$ )              | $g_{fs}$            | 1.5 | 4.5        | mhos |

**DYNAMIC CHARACTERISTICS**

|                              |   |           |     |     |    |
|------------------------------|---|-----------|-----|-----|----|
| Input Capacitance            | $(V_{DS} = 25 \text{ V}, V_{GS} = 0,$<br>$f = 1.0 \text{ MHz})$ | $C_{iss}$ | 200 | 600 | pF |
| Output Capacitance           |   | $C_{oss}$ | 60  | 300 |    |
| Reverse Transfer Capacitance |   | $C_{rss}$ | 15  | 80  |    |

**SWITCHING CHARACTERISTICS\***

|                     |   |              |   |    |    |
|---------------------|---|--------------|---|----|----|
| Turn-On Delay Time  | $(V_{DD} \approx 74 \text{ V}, I_D = 2.25 \text{ A},$<br>$R_{gen} = 50 \text{ ohms})$ | $t_{d(on)}$  | — | 40 | ns |
| Rise Time           |   | $t_r$        | — | 50 |    |
| Turn-Off Delay Time |   | $t_{d(off)}$ | — | 50 |    |
| Fall Time           |   | $t_f$        | — | 50 |    |

**SOURCE-DRAIN DIODE CHARACTERISTICS\***

|                       |   |          |     |            |     |
|-----------------------|---|----------|-----|------------|-----|
| Forward Diode Voltage | $(I_S = \text{Rated } I_{D(\text{on})},$<br>$V_{GS} = 0)$ | $V_{SD}$ | 0.7 | 1.5        | Vdc |
| Forward Turn-On Time  |   | $t_{on}$ | —   | Negligible | ns  |
| Reverse Recovery Time |   | $t_{rr}$ | —   | 350        | ns  |

\*Pulse Test: Pulse Width  $\leq 300 \mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ .