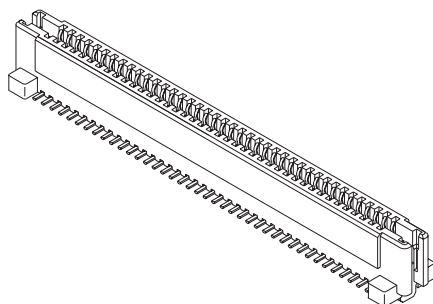


1.00mm (.039") Pitch SlimStack™ Board-to-Board Receptacle

52584

SMT, Dual Row Vertical Stacking



Features and Benefits

- Stacking heights: 8.0 and 18.0mm
- Sizes 20 to 140 circuits
- Leaf-style design protects pins
- Anti-flux design
- Low insertion force
- Polarization pegs assist placement

Reference Information

Packaging: Tube
UL File No.: E29179
Mates With: 53395 (H=8.0mm)
53408 (H=18.0mm)
Designed In: Millimeters

Electrical

Voltage: 100V
Current: 0.5A
Contact Resistance:
40 milliohms max. (H=8.0, 9.0, 10.0mm)
70 milliohms max. (H=18.0mm)
Dielectric Withstanding Voltage: 200V AC
Insulation Resistance: 1000 Megohms min.

Physical

Housing: White glass-filled LCP, UL 94V-0
Contact: Phosphor Bronze
Plating: Gold (0.25mm min.) over Nickel
Operating Temperature: -55 to +85°C

Circuits	Order No.	Lead-free
20	52584-0279	Yes
34	52584-0379	
50	52584-0579	
60	52584-0679	
66	52584-6679	

Note: Contact Molex for embossed tape specifications

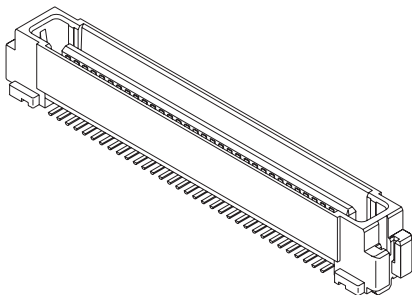
Note: Use only one connector per daughterboard in order to insure proper mating alignment

Circuits	Order No.	Lead-free
80	52584-0879	Yes
100	52584-1079	
120	52584-1279	
140	52584-1479	

1.00mm (.039") Pitch SlimStack™ Board-to-Board Plug

53395

SMT, Dual Row Vertical Stacking



Features and Benefits

- Stacking heights: 8.0mm
- Sizes 20 to 140 circuits
- Leaf-style design protects pins
- Anti-flux design
- Low insertion force
- Polarization pegs assist placement

Reference Information

Packaging: Tube
Mates With: 52584
Designed In: Millimeters

Electrical

Voltage: 100V
Current: 0.5A
Contact Resistance: 40 milliohms max.
Dielectric Withstanding Voltage: 200V AC
Insulation Resistance: 1000 Megohms min.

Physical

Housing: White glass-filled LCP, UL 94V-0
Contact: Phosphor Bronze
Plating: Gold (0.25µm) over Nickel
Operating Temperature: -55 to +85°C

Circuits	Order No.	Lead-free
20	53395-0279	Yes
34	53395-0379	
66	53395-6679	
100	53395-1079	
140	53395-1479	

Note: Contact Molex for embossed tape specifications

Note: Use only one connector per daughterboard in order to insure proper mating alignment