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200W isolated DC-DC converter Wide input and regulated single output



Patent Protection RoHS

FEATURES

- Wide 2:1 input voltage range
- High efficiency up to 93%
- I/O isolation test voltage 1.5k VDC
- Operating ambient temperature range: -40°C to +100°C
- Input under-voltage protection, output short circuit, over-current, over-voltage, over-temperature protection
- Industry standard package: 1/4 brick
- Meet IEC/UL/EN62368 standards

VCB48_QBO-200WR3(-N) series of isolated 200W DC-DC converter products with an wide 2:1 input voltage range. They feature efficiencies of up to 93%, input to output isolation is tested with 1500VDC and the converters safely operate ambient temperature of -40°C to +100°C, input under-voltage protection, output short circuit, over-current, over-voltage, over-temperature protection. They are ideally and widely used in applications such as industrial control, electric power, instruments and communication fields.

Selection Cuide

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		Input Voltage (VDC)		Output		Full Load	
Certification	Part No.	Nominal (Range)	Max.®	Voltage (VDC)	Current (A) Max./Min.	Efficiency [©] (%) Min./Typ.	Max. Capacitive Load(µF)
	VCB4805QBO-200WR3(-N)	312QBO-200WR3(-N) 48	75	5	36.00/0	89/91	6000
	VCB4812QBO-200WR3(-N)			12	16.67/0	91/93	2000
	VCB4815QBO-200WR3(-N)		75	15	13.33/0	91/93	2000
	VCB4824QBO-200WR3(-N)			24	8.33/0	90/92	1000

Notes:

① Exceeding the maximum input voltage may cause permanent damage;

2 Efficiency is measured in nominal input voltage and rated output load;

3 N" means negative logic.

Item	Operating Conditio	ons	Min.	Тур.	Max.	Unit
Input Current (full load / no-load)				4579/20	4682/35	•
Reflected Ripple Current	Nominal input volto		30	120	mA	
Surge Voltage (1sec. max.)			-0.7		80	VDC
Start-up Voltage					36	VDC
Start-up Current					8.8	А
Input Under-voltage Protection			26	29		VDC
Start-up Time	Nominal input voltage & constant resistance load				100	ms
Input Filter			Pi filter			
Hot Plug			Unavailable			
	Ne shile an	VCB48_QBO-200WR3	Ctrl pin open or pulled high (TTL 3.5-12VDC)			DC)
	Module on	VCB48_QBO-200WR3-N	Ctrl pin pulled low to GND (0-1.2VDC)			
	Module off VCB48_QBO-200WR3 VCB48_QBO-200WR3-N Input current when off		Ctrl pin pulled low to GND (0-1.2VDC)			
			Ctrl pin open or pulled high (TTL 3.5-12VDC)			
				3	10	mA

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DC/DC Converter VCB48_QBO-200WR3(-N) Series

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Output Specifications	;					
Item	Operating Conditions	Min.	Тур.	Max.	Unit	
Voltage Accuracy	0%-100% load			±l	±3	
Linear Regulation	Input voltage variation from Ic	w to high at full load		±0.2	±0.5	%Vo
Load Regulation	5%-100% load			±0.5	±0.75	
Transient Recovery Time	25% load step change	25% load step change			500	μs
	25% load step change	5V output		±3	±8	%Vo
Transient Response Deviation		Others		±3	±7	
Temperature Coefficient	Full load				±0.03	%/°C
Ripple & Noise [®]	20MHz bandwidth, nominal in Ioad		100	150	mV p-p	
Trim			90		110	0().(-
Sense					105	%Vo
Over Temperature	Product surface max. temper		130		°C	
Over-voltage Protection		110	125	160	%Vo	
Over-current Protection	Input voltage range	110	140	190	%lo	
Short-circuit Protection		Continu	ous, self-reco	very, time≤3	seconds	

Note: ①The "Tip and barrel" method is used for ripple and noise test, please refer to *DC-DC Converter Application Notes* for specific information. Ripple & Noise at <5% load is 5% Vo max.

Item	Operating Conditions	Min.	Тур.	Max.	Unit	
Isolation Input-output Electric Strength test for 1 minute with a leakage current of 1mA max.		1500		-	VDC	
Insulation Resistance	Input-output resistance at 500VDC	1000			MΩ	
Isolation Capacitance	Input-output capacitance at 100KHz/0.1V		1000		pF	
Operating Temperature	See typical characteristic curves	-40		+100	ĉ	
Storage Temperature		-55		+125		
Storage Humidity	Non-condensing	5		95	%RH	
Din Soldoring Desistance	Wave soldering, 10 seconds			+260		
Pin Soldering Resistance Temperature	Soldering spot is 1.5mm away from case for 10 seconds			+300	Ċ	
Shock and Vibration Test		10-150Hz, 10G, 30Min. along X, Y and Z				
Switching Frequency ⁽¹⁾ PWM Mode			200		KHz	
Altitude		Atm		≪2000m, ssure: 80~110)KPa	
MTBF	MIL-HDBK-217F@25°C				K hours	

Note: ①Switching frequency is measured at full load. The module reduces the switching frequency for light load (below 50%) efficiency improvement.

Mechanical Specifications				
Dimensions	57.9 x 36.80 x 10.05mm			
Weight	43.6g(Typ.)			
Cooling Method	Natural convection or forced air convection			

Electron	Electromagnetic Compatibility (EMC)						
Fraissiana	CE	CISPR32/EN55032	CLASS A (see Fig.3 for recommended circuit)/CLASS B (see Fig	.4 for recommended circuit)			
Emissions RE CISPR32/EN55032 CLASS A (see Fig.3 for recommended circuit)/CLASS B (see Fig.4 for recom			.4 for recommended circuit)				
	ESD	IEC/EN61000-4-2	Contact ±6KV	perf. Criteria B			
	RS	IEC/EN61000-4-3	10V/m	perf. Criteria A			
Immunity	EFT	IEC/EN61000-4-4	±2KV (see Fig.3 for recommended circuit)	perf. Criteria B			
	Surge	IEC/EN61000-4-5	line to line ± 2 KV(see Fig.3 for recommended circuit)	perf. Criteria B			
	CS	IEC/EN61000-4-6	3 Vr.m.s	perf. Criteria A			

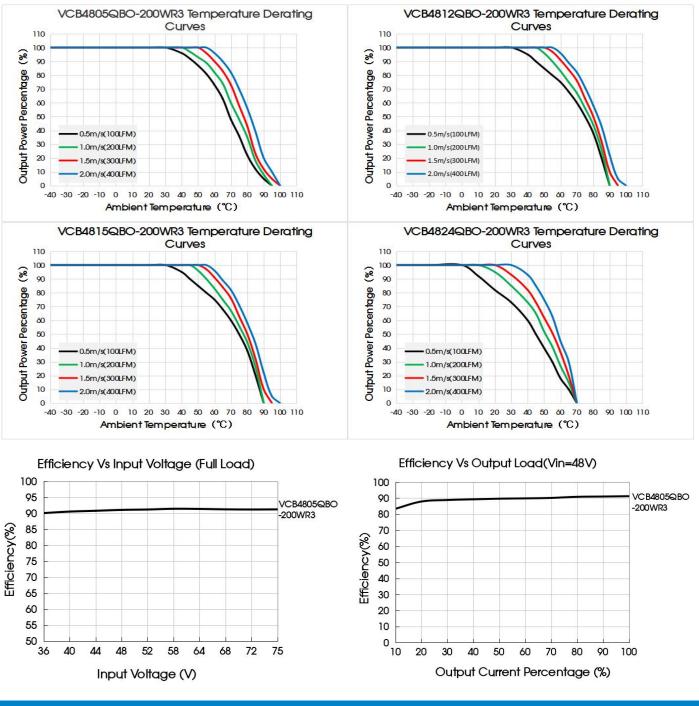
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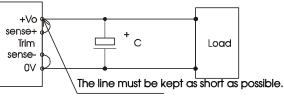
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Typical Characteristic Curves



Remote Sense Application

1. Remote Sense Connection if not used



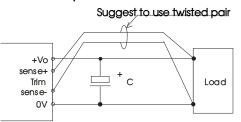
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Notes:

- (1) If the sense function is not used for remote regulation the user must connect the +Sense to + Vo and -Sense to 0V at the DC-DC converter pins and will compensate for voltage drop across pins only.
- (2) The connections between Sense lines and their respective power lines must be kept as short as possible, otherwise they may be picking up noise, interference and/or causing unstable operation of the power module.
- 2. Remote Sense Connection used for Compensation



Notes:

(1) Using remote sense with long wires may cause unstable output, please contact technical support if long wires must be used.

(2) PCB-tracks or cables/wires for Remote Sense must be kept as short as possible. Twisted pair or shielded wairs are suggested for remote compensation and must be kept as short as possible.

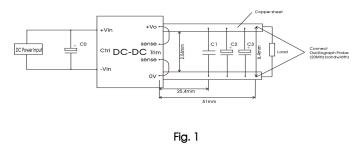
(3) We recommend using adequate cross section for PCB-track layout and/or cables to connect the power supply module to the load in order to keep the voltage drop below 0.3V and to make sure the power supply's output voltage remains within the specified range.

(4) Note that large wire impedance may cause oscillation of the output voltage and/or increased ripple. Consult technical support or factory for further advice of sense operation.

Design Reference

1. Ripple & Noise

All the DC-DC converters of this series are tested before delivery using the recommended circuit shown in Fig. 1.

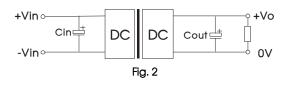


Capacitors value Output voltage	C0	C1	C2	СЗ	
5VDC		1uF/50V			
12VDC	100uF/10		10uF/50V	220uF/63	
15VDC	0V			V	
24VDC					

2. Typical application

We recommended using Mornsun's EMC circuit, otherwise please ensure that at least a 100µF electrolytic capacitors is connected at the input in order to ensure adequate voltage surge suppression and protection.

Input and/or output ripple can be further reduced by appropriately increasing the input & output capacitor values Cin and Cout and/or by selecting capacitors with a low ESR (equivalent series resistance). Also make sure that the capacitance is not exceeding the specified max. capacitive load value of the product.



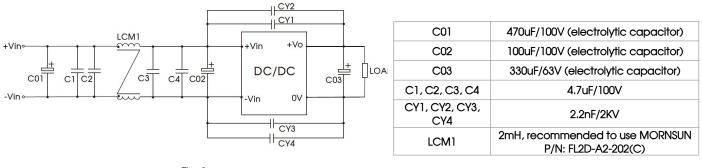
Capacitors value Output voltage	Cin	Cout	
5VDC			
12VDC	100.07/100//	2200 15 (6 2) (
15VDC	100uF/100V	220uF/63V	
24VDC			



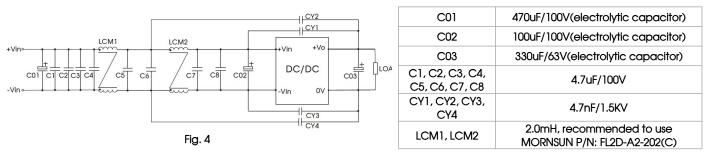
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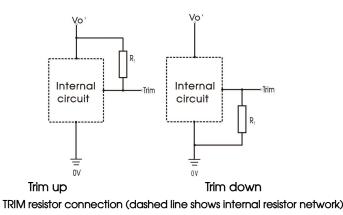
3. EMC compliance recommended circuit







4.Trim Function for Output Voltage Adjustment (open if unused)



Calculating Trim resistor values:

Trim up

$$R_{T} = \left(\frac{5.11V_{nom}(100 + \Delta\%)}{1.225\Delta\%} - \frac{511}{\Delta\%} - 10.22\right)(k\Omega)$$

Trim down

$$R_T = \left(\frac{511}{\Delta\%}\right) - 10.22(k\Omega)$$

Note: RT = Trim Resistor value $\Delta \% = \left| \frac{V_{nom} - V_{out}}{V_{nom}} \right| \times 100$ $\frac{V_{nom}}{V_{out}} = \text{nominal output voltage}$ $V_{out} = \text{desired output voltage}$

When the output voltage is 12V, the up-regulated voltage is +10%, that is, the output voltage set to 13.2V:

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$$\Delta\% = \left|\frac{12 - 13.2}{12}\right| *100 = 10 \qquad \qquad \mathbf{R}_{\mathrm{T}} = \frac{5.11 * 12 * (100 + 10)}{1.225 * 10} - \frac{511}{10} - 10.22 = 489K\Omega$$

When the output voltage is 12V, the down-regulated voltage is -10%, that is, the output voltage set to 10.8V:

$$\Delta\% = \left|\frac{12 - 10.8}{12}\right| * 100 = 10 \qquad R_{\rm T} = \frac{511}{10} - 10.22 = 40.88K\Omega$$

5. Hot Test Point

The thermal element is installed on the top surface of the product and dissipates heat to the surrounding environment through conduction, convection and radiation. Sufficient heat dissipation conditions should be provided to ensure the reliable operation of the product.

By measuring the temperature of the thermal test point 1 in Fig. 5, it can be verified whether the heat dissipation conditions are met.



Note:

The temperature of the hot test point ① cannot exceed 130°C, otherwise the product will trigger protection due to excessive temperature and cannot work normally.

6. The products do not support parallel connection of their output

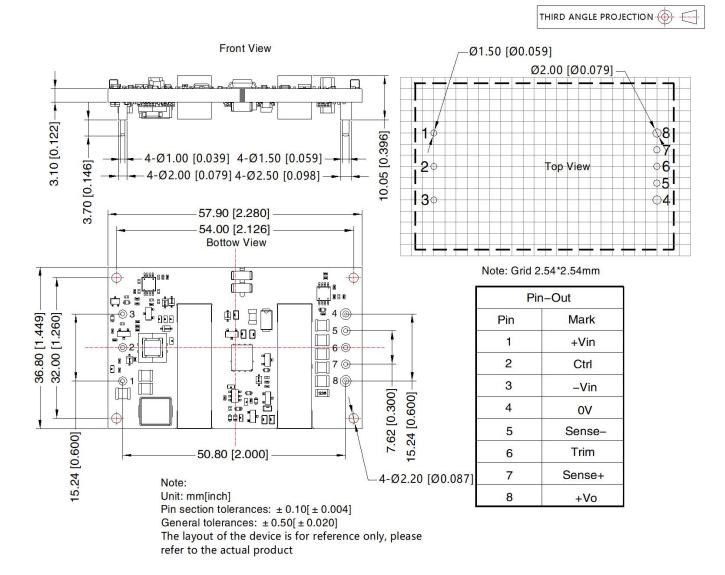
7. For additional information please refer to DC-DC converter application notes on <u>www.mornsun-power.com</u>



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Dimensions and Recommended Layout



Notes:

- 1. For additional information on Product Packaging please refer to www.mornsun-power.com. Packaging Bag Number: 58010113;
- 2. The maximum capacitive load offered were tested at input voltage range and full load;
- 3. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 4. All index testing methods in this datasheet are based on company corporate standards;
- 5. We can provide product customization service, please contact our technicians directly for specific information;
- 6. Products are related to laws and regulations: see "Features" and "EMC";
- 7. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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