

Product Change Notification - IIRA-27ERUU581

Date: 09 Sep 2014

Product Category: Memory; Analog (Linear & Mixed Signal) AND Interface; Analog (Thermal, Power Management & Safety); 8-bit Microcontrollers

Notification subject: CCB 1381.03 Initial Notice: Qualification of palladium coated copper (PdCu) bond wire in selected products of 160K and 150K wafer technologies available in 14L and 8L TSSOP package at MMT

Notification text: **PCN Status:**
Initial notification

Microchip Parts Affected:

See attachments of affected catalog part numbers (CPN) labeled as...

PCN_IIRA-27ERUU581_Affected_CPN.xls

PCN_IIRA-27ERUU581_Affected_CPN.pdf

Description of Change:

Qualification of palladium coated copper (PdCu) bond wire in selected products of 160K and 150K wafer technologies available in 14L and 8L TSSOP package at MMT assembly site.

NOTE: Selected products are non-automotive standalone memory, analog, and PIC MCU devices. Please review the affected CPN lists (attached) to identify the actual parts affected.

Impacts to Data Sheet:

None

Reason for Change:

To improve manufacturability and qualify PdCu wire at MMT assembly site.

Change Implementation Status:

In Progress

Estimated First Ship Date:

October 30, 2014 (date code: 1444)

NOTE: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Markings to Distinguish Revised from Unrevised Devices:

Traceability code

Revision History:

March 17, 2014: Issued initial notification.

September 9, 2014: Revised the initial notification. Updated the scope of change from **14 TSSOP** package to **160K and 150K technologies available in 14L and 8L TSSOP** package. Revised the estimated first ship date from June 10, 2014 to October 30, 2014

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

Attachment(s):

[PCN_IIRA-27ERUU581_Qual Plan.pdf](#) [PCN_IIRA-27ERUU581_Affected_CPN.pdf](#) [PCN_IIRA-27ERUU581_Affected_CPN.xlsx](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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PCN_IIRA-27ERUU581
CATALOG_PART_NBR
24AA014H-I/ST
24AA014HT-I/ST
24AA014-I/ST
24AA014T-I/ST
24AA01H-I/ST
24AA01HT-I/ST
24AA01-I/ST
24AA01T-I/ST
24AA02/ST
24AA024H-I/ST
24AA024HT-I/ST
24AA024-I/ST
24AA024T-I/ST
24AA025-I/ST
24AA025T-I/ST
24AA02H-I/ST
24AA02HT-I/ST
24AA02-I/ST
24AA02T-I/ST
24AA04H-I/ST
24AA04HT-I/ST
24AA04-I/ST
24AA04T-I/ST
24AA08H-I/ST
24AA08HT-I/ST
24AA08-I/ST
24AA08T-I/ST
24AA128-I/ST
24AA128T-I/ST
24AA16-E/ST
24AA16H-I/ST
24AA16HT-I/ST
24AA16-I/ST
24AA16T-E/ST
24AA16T-I/ST
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24AA256-I/STRVE
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24AA32AFT-I/ST
24AA32A-I/ST
24AA32A-I/STB31
24AA32AT-I/ST
24AA32AT-I/STB31
24AA32AX-I/ST

24AA32AXT-I/ST
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24AA64X-I/ST
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24C01CT-E/ST
24C01CT-I/ST
24C02C-E/ST
24C02C-I/ST
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24C02CT-I/ST
24FC128-E/ST
24FC128-I/ST
24FC128-I/STA31
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24FC128T-I/ST
24FC128T-I/STA31
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24FC256T-E/ST
24FC256T-I/ST
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24LC024HT-E/ST
24LC024HT-I/ST
24LC024-I/ST
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24LC64FT-I/ST
24LC64-I/ST
24LC64-I/STRVE
24LC64T-E/ST
24LC64T-I/ST
24LC64T-I/STRVE
24LC64X-E/ST
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24LC64X-I/STRVE
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25AA128XT-I/ST
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93AA56A-I/ST
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93C86CT-I/ST
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93LC46A-E/ST
93LC46A-I/ST
93LC46AT/ST
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93LC46AT-I/ST
93LC46B/ST
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93LC56CT-I/ST
93LC66A/ST
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93LC86AT-E/ST
93LC86AT-I/ST
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MCP7940MT-I/ST
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PIC16F636T-E/ST
PIC16F636T-I/ST
PIC16F636T-I/ST047
PIC16F676-E/ST
PIC16F676-I/ST
PIC16F676-I/STAU
PIC16F676-I/STC05
PIC16F676T-E/ST
PIC16F676T-I/ST
PIC16F676T-I/ST053
PIC16F676T-I/STAU
PIC16F684-E/ST
PIC16F684-E/STC13
PIC16F684-I/ST
PIC16F684-I/ST041
PIC16F684-I/ST102
PIC16F684-I/STAU
PIC16F684T-E/ST
PIC16F684T-E/ST061
PIC16F684T-I/ST
PIC16F684T-I/ST034
PIC16F684T-I/ST050
PIC16F684T-I/ST068
PIC16F684T-I/ST069
PIC16F684T-I/ST089
PIC16F684T-I/ST096
PIC16F684T-I/ST102
PIC16F684T-I/STAU
PIC16F688-E/ST
PIC16F688-I/ST
PIC16F688-I/ST029
PIC16F688-I/ST049
PIC16F688-I/ST050
PIC16F688T-E/ST
PIC16F688T-I/ST
PIC16F688T-I/ST029
PIC16F688T-I/ST036

PIC16F688T-I/ST044
PIC16F688T-I/ST045
PIC16F688T-I/ST049
PIC16F688T-I/ST050
PIC16F688T-I/ST054
PIC16F688T-I/ST056
PIC16HV616-E/ST
PIC16HV616-I/ST
PIC16HV616T-E/ST
PIC16HV616T-I/ST



MICROCHIP

QUALIFICATION PLAN

PCN #: IIRA-27ERUU581

Date:

Feb 26, 2014

Qualification of palladium coated copper (PdCu) bond wire at MMT assembly site for 160K wafer technology in 14L TSSOP package. The 150K wafer technology and 8L TSSOP package will qualify by similarity.

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Purpose: _____ Qualification of palladium coated copper (PdCu) bond wire at MMT assembly site for 160K wafer technology in 14L TSSOP package. The 150K wafer technology and 8L TSSOP package will qualify by similarity.

MP code: _____ DE0447D4XB04

Part No.: _____ PIC16F688-I/ST

BD No.: _____ BDM-000465 rev.B (Engineering BD)

CCB No.: _____ 1381.03

Package:

Type _____ 14L TSSOP

Width or Size _____ 4.4 mm

Die thickness: _____ 11 mils

Die size: _____ 77.50 x 100.30 mils

Lead frame:

Paddle size: _____ 118x153 mils (ASM Singapore)

Material _____ C7025

Surface _____ Bare Cu

Process _____ Stamped

Lead Lock _____ No

Part Number _____ 10101406

Treatment _____ BOT

Wire:

Material _____ PdCu (Nippon-Japan)

Die Attach Epoxy:

Part Number _____ 2200D

Conductive _____ Yes

Mold Compound: _____ G600V (Sumitomo-Singapore)

Reliability Test plan: _____ See attached, STD Package Reliability Test plan on each package.

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days	Special Instructions
Wire Bond Pull - WBP	Mil. Std. 883-2011	5	0	1	5	0 fails after TC	5	30 bonds from a minimum of 5 devices.
Wire Bond Shear - WBS	CDF-AEC-Q100-001	5	0	1	5	0	5	30 bonds from a minimum of 5 devices.
Wire Sweep		5	1	3	15	0		Required for any reduction in wire bond thickness.
External Visual	Mil. Std. 883-2009/2010	All devices prior to submission for qualification testing	0	3	ALL	0	5	
HTSL (High Temp Storage Life)	+175 C for 504 hours or 150°C for 1008 hrs. Electrical test pre and post stress at +25C and hot temp. (1 lot to be tested at 85C)	45	5	1	50	0	10	Must be in progress at time of package release to production, but completion is not required for release to production. For hot temp testing, pre/post test 1 lot at 85°C
Preconditioning - Required for surface mount devices	+150°C Bake for 24 hours, moisture loading requirements per MSL level + 3X reflow at peak reflow temperature per Jedec-STD-020D for package type; Electrical test pre and post stress at +25°C. Perform SAM analysis using the standard sample size. MSL1 260C	231	15	3	738	0	15	Spares should be properly identified. 77 parts from each lot to be used for HAST, Autoclave, Temp Cycle test.
HAST	+130°C/85% RH for 96 hours. Electrical test pre and post stress at +25°C and hot temp. Extend HAST to 192 hrs post stress test at 25C 1 lot to be tested at 85C	77	5	3	246	0	10	Spares should be properly identified. Use the parts which have gone through Pre-conditioning. For hot temp testing, pre/post test 1 lot at 85°C
Unbiased HAST	+130°C/85% RH for 96 hrs. Electrical test pre and post stress at +25°C. Extend uHAST to 192hrs. Post stress test at 25C	77	5	3	246	0	10	Spares should be properly identified. Use the parts which have gone through Pre-conditioning.

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days	Special Instructions
Temp Cycle	-65°C to +150°C for 500 cycles. Electrical test pre and post stress at hot temp; 3 gram force WBP, on 5 devices from 1 lot, Test by following readpoint TC50,TC250 and TC 500. Extend temp cycle to 1000 cycle post stress test at 25C 1 lot to be tested at 85C	77	5	3	246	0	15	Spares should be properly identified. Use the parts which have gone through Pre-conditioning. For hot temp testing, pre/post test 1 lot at 85°C