



Product Change Notification - SYST-30FYSR726

Date: 30 Oct 2012

Product Category: 8-bit Microcontrollers

Device Family:  

Notification subject: Data Sheet - PIC32MX1XX/2XX Family Data Sheet Data Sheet Document Revision

Notification text: SYST-30FYSR726

Microchip has released a new DeviceDoc for the PIC32MX1XX/2XX Family Data Sheet of devices. If you are using one of these devices please read the document located at [PIC32MX1XX/2XX Family Data Sheet](#).

Notification Status: Final

Description of Change: This revision includes the following updates: * All singular pin diagram occurrences of CVREF were changed to: CVREFOUT * Minor text and formatting changes were incorporated throughout the document All other changes are referenced by their respective section: "32-bit Microcontrollers (up to 128 KB Flash and 32 KB SRAM) with Audio and Graphics Interfaces, USB, and Advanced Analog" * Updated Operating Conditions * Updated Communication Interfaces 2.0 "Guidelines for Getting Started with 32-bit MCUs" * Removed Section 2.8 "Configuration of Analog and Digital Pins During ICSP Operations" 3.0 "CPU" * Removed references to GPR shadow registers in 3.1 "Features" and 3.2.1 "Execution Unit" 4.0 "Memory Organization" * Updated the BRG bit range in the SPI1 and SPI2 Register Map (see Table 4-8) * Added the PWP<6> bit to the Device Configuration Word Summary (see Table 4-17) 5.0 "Flash Program Memory" * Added a note with Flash page size and row size information 7.0 "Interrupt Controller" * Updated the TPC<2:0> bit definitions (see Register 7-1) * Updated the IPTMR<31:0> bit definition (see Register 7-3) 8.0 "Oscillator Configuration" * Updated the PIC32MX1XX/2XX Family Clock Diagram (see Figure 8-1) * Updated the RODIV<14:0> bit definitions (see Register 8-3) 10.0 "USB On-The-Go (OTG)" * Updated the Notes in the USB Interface Diagram (see Figure 10-1) 18.0 "Universal Asynchronous Receiver Transmitter (UART)" * Updated the baud rate range in the list of primary features 26.0 "Special Features" * Added the PWP<6> bit to the Device Configuration Word 0 (see Register 26-1) 29.0 "Electrical Characteristics" * Added Note 1 to Operating MIPS vs. Voltage (see Table 29-1) * Added Note 2 to DC Temperature and Voltage Specifications (see Table 29-4) * Updated the Conditions for parameter DC25 in DC Characteristics: Operating Current (I_{dd}) (see Table 29-5) * Added Note 2 to Electrical Characteristics: BOR (see Table 29-10) * Added Note 4 to Comparator Specifications (see Table 29-12) * Added Note 5 to ADC Module Specifications (see Table 29-32) * Updated the 10-bit Conversion Rate Parameters and added Note 3 (see Table 29-33) * Added Note 4 to the Analog-to-Digital Conversion Timing Requirements (see Table 29-34) * Added Note 3 to CTMU Current Source Specifications (see Table 29-39) 30.0 "50 MHz Electrical Characteristics" * New chapter with electrical characteristics for 50 MHz devices 31.0 "Packaging Information" * The 36-pin and 44-pin VTLA packages have been updated

Pre Change: N/A

Post Change: N/A

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 30 Oct 2012

NOTE: Please be advised that this is a change to the document only the product has not been changed..

Markings to Distinguish Revised from Unrevised Devices:N/A

Attachment(s): [PIC32MX1XX/2XX Family Data Sheet](#)

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Parts Affected

PIC32MX110F016B
PIC32MX120F032D
PIC32MX120F032B
PIC32MX110F016D
PIC32MX210F016B
PIC32MX210F016D
PIC32MX220F032B
PIC32MX220F032D
PIC32MX110F016C
PIC32MX210F016C
PIC32MX120F032C
PIC32MX220F032C
PIC32MX230F064D
PIC32MX150F128D
PIC32MX250F128D
PIC32MX150F128B
PIC32MX130F064B
PIC32MX130F064D
PIC32MX230F064B
PIC32MX250F128B
PIC32MX230F064C
PIC32MX250F128C
PIC32MX130F064C
PIC32MX150F128C