

Product Change Notification - IIRA-02LFGN963

Date: 11 Oct 2013

Product Category: 16-bit Microcontrollers and Digital Signal Controllers

Notification subject: CCB 1139 Additional Fabrication Site Initial Notice: Qualification of masks UDAAx, UFACx, UGABx and YGAYx at an additional wafer fabrication site.

Notification text:

PCN Status:

Initial notification

Microchip Parts Affected:

See attachments of affected catalog part numbers (CPN) labeled as...

PCN_IIRA-02LFGN963_Affected_CPN.xls

PCN_IIRA-02LFGN963_Affected_CPN.pdf

Description of Change:

Qualification of masks UDAAx, UFACx, UGABx and YGAYx at an additional wafer fabrication site.

Impacts to Data Sheet:

None

Reason for Change:

To improve productivity

Change Implementation Status:

In Progress

Estimated First Ship Date:

January 3, 2014 (date code: 1401)

NOTE: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Markings to Distinguish Revised from Unrevised Devices:

Traceability code

Revision History:

October 11, 2013: Issued initial notification.

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

Attachment(s):

[PCN_IIRA-02LFGN963_Affected CPN.pdf](#) [PCN_IIRA-02LFGN963_Qual Plan.pdf](#) [PCN_IIRA-02LFGN963_Affected CPN.xlsx](#)

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PCN_IIRA-02LFGN963
CATALOG_PART_NBR
HA96R99T-I/CS028
MTR1202-I/PT
MTR5934-I/PT
MTR9416-I/PT
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MICROCHIP

QUALIFICATION PLAN

PCN#: IIRA-02LFGN963

**Date:
October 3, 2013**

**Qualification of masks UDAAx, UFACx, UGABx and
YGAYx at an additional wafer fabrication site.**

Distribution

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Microchip Technology (Thailand) Co., Ltd.
14 Moo 1 T. Wangtakien A. Muangchacherngsao,
Chacherngsao, Thailand, 24000
Tel. (6638) 857119-45, 857311-19 ext. 1231
Fax (6638) 857149-50

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (must be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days
ELFR	+150°C for 24 hours or 125°C for 48 hours; Electrical test pre and post stress at -40°C, +25°C and hot temp.	800	5	3	2415	0	7
HTOL/DLT	+150°C for 408 hours or 125°C for 1008 hours; Electrical test pre and post stress at -40°C, +25°C, and hot temp. Readout at 0, 96 and 408 hours if fresh samples used.	600	5	3	1815	0	30
EDR Endurance Cycling (Retention Bake)	Specified E/W cycles of Data EE and/or Program Memory at either +25°C or +85°C followed by 150°C, 48hrs (or 175°C, 24 hrs) bake I and bake II tests. Electrical test pre and post stress at -40°C, 25°C and hot temp.	231	5	3	708	0	10
EDR High Temp Bake(Retention Bake)	+175°C for 504 hours; Electrical test pre and post stress at -40°C, 25°C and hot temp. Readouts at 0, 96, 504 hours.	231	5	3	708	0	40
EDR Endurance Cycling (DLT)	Specified E/W cycles of Data EE and/or Program Memory at either +25°C or +85°C optionally followed by 150°C, 48hrs (or 175°C, 24 hrs) bake I and bake II tests. Electrical test pre and post stress at 25°C and hot temp.	77	5	3	246	0	10
EDR HTOL(DLT)	+150°C for 408 hours; Electrical test pre and post stress at -40°C, 25°C and hot temp. Readouts at 0, 96, 408 hours.	77	5	3	246	0	40

ESD - HBM	Electrical test pre and post stress at 25°C and hot temp. Test at each voltage: 500V, 1KV, 2KV, 4KV	12	0	1	12	0	5
ESD - MM	Electrical test pre and post stress at 25°C and hot temp. Test at each voltage: 100V, 200V, 300V, 400V	12	0	1	12	0	5
ESD Induced Latch-Up	Tested at +25°C at MAX operating device voltage per QCI 30561. Electrical test pre and post stress at 25°C and hot temp.	5	0	1	5	N/A	5
ESD-CDM	Tested per ESDA STM 5.3.1-1999 spec at 250V, 500V, 750V, 1000V, 1500V, 2000V. Electrical test pre and post stress at 25°C and hot temp.	18	0	1	18	0	5
Latch up	Over voltage and over current. Latch-up stress testing should be performed at room and max operating temperature. Electrical test pre and post stress at 25°C and hot temp per Q100-004.	12	0	1	12	0	5
Electrical Distributions	Characterize device electrical parameters at -40°C, +25°C and hot temp.	30	0	1	30	0	10
Preconditioning - Required for surface mount devices	+150°C Bake for 24 hours, moisture loading requirements per MSL level + 3X reflow at peak reflow temperature per Jedec-STD-020C for package type; Electrical test pre and post stress at +25°C. Perform SAM analysis using the standard sample size.	231	15	3	738	0	15
HAST	+130°C/85% RH for 96 hours. Electrical test pre and post stress at 25°C and hot temp.	77	5	3	246	0	10