

Product Change Notification - CYER-09HJXI365

Date: 18 Sep 2013

Product Category: Analog (Linear & Mixed Signal) AND Interface; 8-bit Microcontrollers; Analog (Thermal, Power Management & Safety); 16-bit Microcontrollers and Digital Signal Controllers; Touch Sensing Technologies; RF and Security

Notification subject: CCB 1155.05 Final Notification: Qualification of selected products available in the 28L SPDIP, 18L PDIP, and 8L PDIP packages with palladium coated copper (PdCu) bond wire at MTAI assembly site.

Notification text: **PCN Status:**
Final notification

Microchip Parts Affected:

See attachments of affected catalog part numbers (CPN) labeled as...

PCN_CYER-09HJXI365_Affected_CPN.xls

PCN_CYER-09HJXI365_Affected_CPN.pdf

Description of Change:

Qualification of selected products available in the 28L SPDIP, 18L PDIP, and 8L PDIP packages with palladium coated copper (PdCu) bond wire at MTAI assembly site.

NOTE: Selected products are non-automotive only for both

standalone analog and PIC MCU product. Please review the affected CPN lists (attached) to identify the actual parts affected.

Pre Change:

Gold (Au) wire

Post Change:

Palladium coated copper (PdCu) bond wire

Impacts to Data Sheet:

None

Reason for Change:

To improve manufacturability

Change Implementation Status:

In progress

Estimated First Ship Dates:

October 18, 2013 (Date code: 1342)

NOTE: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Markings to Distinguish Revised from Unrevised Devices:

Traceability code

Revision History:

August 23, 2013: Initial notification issue date.

September 18, 2013: Final notification issue date. Attached qualification report.

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

Attachment(s): [PCN_CYER-09HJXI365_Affected CPN.pdf](#) [PCN_CYER-09HJXI365_Qual Report.pdf](#) [PCN_CYER-09HJXI365_Affected CPN.xlsx](#)

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PCN_CYER-09HJXI365
CATALOG_PART_NBR
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DSPIC30F1010-30I/SP
DSPIC30F2010-20E/SP
DSPIC30F2010-20I/SP
DSPIC30F2010-30I/SP
DSPIC30F2010-30I/SPG
DSPIC30F2012-20E/SP
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DSPIC30F2020-30I/SP
DSPIC30F3010-20E/SP
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HCS201-I/P
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HCS500/P
HCS500-I/P
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MCP2122-E/P
MCP3001-I/P

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MCP6242-E/P
MCP6271-E/P
MCP6272-E/P
MCP6273-E/P
MCP6275-E/P
MCP6281-E/P
MCP6282-E/P
MCP6283-E/P
MCP6285-E/P
MCP6291-E/P
MCP6292-E/P
MCP6293-E/P
MCP6295-E/P
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PIC12F683-I/P069
PIC12F683-I/P074
PIC12F683-I/P078
PIC12F752-E/P

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TC2185-5.0VPA
CF745-04/P
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DSPIC30F2011-30I/P
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DSPIC30F3012-20I/P
DSPIC30F3012-30I/P
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HS12-100D0/12DS2F
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PIC24HJ12GP201-E/P
PIC24HJ12GP201-I/P



MICROCHIP

**QUALIFICATION REPORT
RELIABILITY LABORATORY**

PCN #: CYER-09HJXI365

Date

September 11, 2013

Qualification of selected products available in the 28L SPDIP package with palladium coated copper (PdCu) bond wire at MTAI assembly site. The 8L PDIP and 18L PDIP packages will qualify at MTAI assembly site by similarity.

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MICROCHIP PACKAGE QUALIFICATION REPORT

Purpose	Qualification of selected products available in the 28L SPDIP package with palladium coated copper (PdCu) bond wire at MTAI assembly site. The 8L PDIP and 18L PDIP packages will qualify at MTAI assembly site by similarity.
CN	BC130710
QUAL ID	Q13069
MP CODE	A5AF14M3XE04
Part No.	PIC16F876-04E/SP
Bonding No.	A-040997 Rev. C
CCB No.	1155.05
<u>Package</u>	
Type	28L SPDIP
Package size	300 mils
Die thickness	15 mils
Die size	179.20 x 199.60 mils
<u>Lead Frame</u>	
Paddle size	196 x 300 mils
Material	CDA194
Surface	Ag spot
Process	Stamp
Lead Lock	Yes
Part Number	10102812
<u>Die attach material</u>	
Epoxy	CRM-1064L
Wire	PdCu wire
Mold Compound	GE800
Plating Composition	Matte Tin



MICROCHIP PACKAGE QUALIFICATION REPORT

Manufacturing Information

Assembly Lot No.	Wafer Lot No.	Date Code
MTAI140703111	TMPE213526809.200	1320PMY
MTAI140703112	TMPE213526809.300	1320PP0
MTAI140703113	TMPE213526809.400	1320PP1

Result

Pass Fail _____

28L SPDIP (.300") assembled by MTAI pass reliability test per QCI-39000.

Prepared By: _____ **Date:** September 03, 2013 **(Reliability Engineer)**

(Mr.Thinnapol Nakkasun)

Approved By: _____ **Date:** September 03, 2013 **(Reliability Manager)**

(Mr. Somnuek Thongprasert)

PACKAGE QUALIFICATION REPORT

Test Number (Reference)	Test Condition	Standard/ Method	Qty. (Acc.)	Def/SS.	Result	Remarks
Electrical Test	Electrical Test: +25°C and 125°C System: J750	JESD22- A113	693(0)	693		Good Devices
Temp Cycle	Stress Condition: (Standard) -65°C to +150°C, 50, 250, 500 Cycles System : TABAI ESPEC TSA-70H Electrical Test: + 125°C System: J750	JESD22- A104	231(0)	0/231	Pass	
UNBIASED-HAST	Stress Condition: (Standard) +130°C/85%RH, 96 hrs. System: HAST 6000X Electrical Test: +25°C System: J750	JESD22- A118	231(0)	0/231	Pass	
HAST	Stress Condition: (Standard) +130°C/85%RH, 96 hrs. Bias Volt: 5.7 Volts System: HAST 6000X Electrical Test: + 25°C and 125°C System: J750	JESD22- A110	231(0)	0/231	Pass	

PACKAGE QUALIFICATION REPORT

Test Number (Reference)	Test Condition	Standard/ Method	Qty. (Acc.)	Def/SS.	Result	Remarks
High Temperature Storage Life	Stress Condition: Bake 175 °C, 504 hrs System: SHEL LAB	JESD22- A103		45		45 units
	Electrical Test: + 25 °C and 125 °C System: J750		45(0)	0/45	Pass	
Bond Strength Data Assembly	Wire Pull (> 2.5 grams)	JESD22- B116	30 (0) Wires	0/30	Pass	
	Bond Shear (15.00 grams)		30 (0) bonds	0/30	Pass	