

# Product Change Notification - CYER-15WDGG555

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**Date:** 22 Aug 2013

**Product Category:** 8-bit Microcontrollers; Analog (Linear & Mixed Signal) AND Interface; 16-bit Microcontrollers and Digital Signal Controllers

**Notification subject:** CCB 1321 Initial Notice: Qualification of 28L PDIP and 40L PDIP packages at LPI assembly site.

**Notification text:** **PCN Status:**  
Initial notification

## **Microchip Parts Affected:**

See attachments of affected catalog part numbers (CPN) labeled as...

PCN\_CYER-15WDGG555\_Affected\_CPN.xls

PCN\_CYER-15WDGG555\_Affected\_CPN.pdf

## **Description of Change:**

Qualification of 28L PDIP and 40L PDIP packages at LPI assembly site.

## **Impacts to Data Sheet:**

None

**Reason for Change:**

To improve productivity

**Change Implementation Status:**

In Progress

**Estimated First Ship Date:**

September 27, 2013 (date code: 1339)

NOTE: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

**Markings to Distinguish Revised from Unrevised Devices:**

Traceability code

**Revision History:**

**August 22, 2013:** Issued initial notification.

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

**Attachment(s):**

[PCN\\_CYER-15WDGG555\\_Affected CPN.pdf](#) [PCN\\_CYER-15WDGG555\\_Qual Plan.pdf](#) [PCN\\_CYER-15WDGG555\\_Affected CPN.xlsx](#)

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PCN_CYER-15WDGG555
CATALOG_PART_NBR
AY0438/P
AY0438-I/P
CF775-04/P
DSPIC30F3011-20E/P
DSPIC30F3011-20I/P
DSPIC30F3011-30I/P
DSPIC30F3014-20E/P
DSPIC30F3014-20I/P
DSPIC30F3014-30I/P
DSPIC30F4011-20E/P
DSPIC30F4011-20I/P
DSPIC30F4011-30I/P
DSPIC30F4013-20E/P
DSPIC30F4013-20I/P
DSPIC30F4013-30I/P
PIC16C55-10/P
PIC16C55-10I/P
PIC16C557-I/P
PIC16C558-ME
PIC16C55A-04/P
PIC16C55A-04I/P
PIC16C55A-20/P
PIC16C55A-20I/P
PIC16C55A-40/P
PIC16C55-HS/P
PIC16C55-HSI/P
PIC16C55-LP/P
PIC16C55-LPI/P
PIC16C55-RC/P
PIC16C55-RCI/P
PIC16C55-XT/P
PIC16C55-XTI/P
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PIC16C57-LPI/P
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PIC16F877-10E/P
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PIC16F877A-E/P
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PIC18F4455-I/PC03



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PIC18LF46K80-I/P
SST89E54RDA-40-C-PIE
SST89E54RDA-40-I-PIE-ZZ131
SST89E58RDA-40-C-PIE
SST89E58RDA-40-C-PIE-ZZ132
SST89E58RDA-40-I-PIE-ZZ132
TC7106ACPL
TC7106AIPL
TC7106ARCPL
TC7106CPL
TC7106IPL
TC7106RCPL
TC7107ACPL
TC7107AIPL
TC7107ARCPL
TC7107CPL
TC7107IPL
TC7107RCPL
TC7109ACPL
TC7109ACPL514
TC7109CPL
TC7116ACPL
TC7116CPL
TC7117ACPL
TC7117CPL
TC7117RCPL
TC7126ACPL
TC7126ARCPL
TC7126CPL
TC7126IPL
TC7126RCPL
TC7129CPL
TC7135CPI
TC835CPI
TC850CPL



**MICROCHIP**

# **QUALIFICATION PLAN**

**PCN #: CYER-15WDGG555**

**Date:  
Aug 14, 2013**

**Qualification of 28L PDIP and 40L PDIP packages at  
LPI assembly site.**

Distribution

Somnuek T.  
Wanphen L.  
Wichai K.  
Mitch R.  
Chalermpon P.

Rangsun K.  
A. Navarro  
C. Fernando  
P. Greg

Microchip Technology (Thailand) Co., Ltd.  
14 Moo 1 T. Wangtakien A. Muangchacherngsao,  
Chacherngsao, Thailand, 24000  
Tel. (6638) 857119-45, 857311-19 ext. 1231  
Fax (6638) 857149-50

**Purpose:** \_\_\_\_\_ Qualification of 28L PDIP and 40L PDIP packages at  
LPI assembly site.

**MP code:** \_\_\_\_\_ DEDB14S2XAXF

**Part No.:** \_\_\_\_\_ PIC18F4680

**BD No:** \_\_\_\_\_ BDM-000376

**CCB No:** \_\_\_\_\_ 1321

**Package:**

**Type** \_\_\_\_\_ 40L PDIP

**Width or Size** \_\_\_\_\_ 600 mils

**Die thickness:** \_\_\_\_\_ 15 mils

**Die size:** \_\_\_\_\_ 178.0 x 185.8 mils,

**Lead frame:**

**Paddle size:** \_\_\_\_\_ 200 x 200

**Material** \_\_\_\_\_ A194

**Surface** \_\_\_\_\_ Ag plate

**Process** \_\_\_\_\_ Stamp

**Lead Lock** \_\_\_\_\_ No

**Part Number** \_\_\_\_\_ 09P004006

**Wire:**

**Material** \_\_\_\_\_ Au

**Die Attach Epoxy:**

**Part Number** \_\_\_\_\_ 8340

**Conductive** \_\_\_\_\_ Yes

**Mold Compound:** \_\_\_\_\_ G600

**Reliability Test plan:** \_\_\_\_\_ See attached, STD Package Reliability Test plan on  
each package.

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days	Special Instructions
Wire Bond Pull - WBP	Mil. Std. 883-2011	5	0	1	5	0 fails after TC	5	30 bonds from a minimum of 5 devices.
Wire Bond Shear - WBS	CDF-AEC-Q100-001	5	0	1	5	0	5	30 bonds from a minimum of 5 devices.
Physical Dimensions	Measure per JESD22 B100 and B108	10	0	3	30	0	5	
Lead Integrity	JESD22 B105	5	0	1	5	0 (No lead breakage or cracks)	5	10 leads from each of 5 parts. Not required for SMD, only required for through-hole.
External Visual	Mil. Std. 883-2009/2010	All devices prior to submission for qualification testing	0	3	ALL	0	5	
HTSL (High Temp Storage Life)	+175 C for 504 hours or 150°C for 1008 hrs. Electrical test pre and post stress at +25C and hot temp 85C and 125C.	45	5	1	50	0	10	Must be in progress at time of package release to production, but completion is not required for release to production. For hot temp testing, pre/post test 1 lot at 85°C and 125°C (if applicable)
HAST	+130°C/85% RH for 96 hours. Electrical test pre and post stress at +25°C and hot temp 85C and 125C.	77	5	3	246	0	10	Spares should be properly identified. Use the parts which have gone through Pre-conditioning. For hot temp testing, pre/post test 1 lot at 85°C and 125°C (if applicable)
Unbiased HAST	+130°C/85% RH for 96 hrs Electrical test pre and post stress at +25°C.	77	5	3	246	0	10	Spares should be properly identified. Use the parts which have gone through Pre-conditioning
Temp Cycle	-65°C to +150°C for 500 cycles. Electrical test pre and post stress at hot temp 85C and 125C; 3 gram force WBP, on 5 devices from 1 lot, test following Temp Cycle stress.	77	5	3	246	0	15	Spares should be properly identified. Use the parts which have gone through Pre-conditioning. For hot temp testing, pre/post test 1 lot at 85°C and 125°C (if applicable).