


Product Change Notification - SYST-03PSRW649

Date: 03 Apr 2013
Product Category: 8-bit Microcontrollers
Device Family: 
Notification subject: Data Sheet - PIC32MX5XX/6XX/7XX Family Data Sheet Data Sheet Document Revision
Notification text: SYST-03PSRW649

Microchip has released a new DeviceDoc for the PIC32MX5XX/6XX/7XX Family Data Sheet of devices. If you are using one of these devices please read the document located at [PIC32MX5XX/6XX/7XX Family Data Sheet](#).

Notification Status: Final

Description of Change: Removed the shading for all D- and D+ pins in all pin diagrams. 1.0 "Device Overview" Updated the Vbus description in Table 1-1. 2.0 "Guidelines for Getting Started with 32-bit Microcontrollers" Added 2.11 "Referenced Sources". 4.0 "Memory Organization" Added Note 3 to the Interrupt Register Map tables (see Table 4-2 through Table 4-7. 22.0 "10-bit Analog-to-Digital Converter (ADC)" Updated the ADC Conversion Clock Period Block Diagram (see Figure 22-2). 26.0 "Comparator Voltage Reference (CVref)" Updated the Comparator Voltage Reference Block Diagram (see Figure 26-1). 28.0 "Special Features" Removed the second paragraph from 28.3.1 "On-Chip Regulator and POR". 31.0 "Electrical Characteristics" Added the new V-Temp temperature range (-40°C to +105°C) to the heading of all specification tables. Updated the Ambient temperature under bias, updated the Voltage on any 5V tolerant pin with respect to Vss when Vdd < 2.3V, and added Voltage on Vbus with respect to Vss in Absolute Maximum Ratings. Added the characteristic, DC5a to Operating MIPS vs. Voltage (see Table 31-1). Updated or added the following parameters to the Operating Current (Idd) DC Characteristics: DC20, DC20b, DC23, and DC23b (see Table 31-5). Added the following parameters to the Idle Current (Idle) DC Characteristics: DC30b, DC33b, DC34c, DC35c, and DC36c (see Table 31-6). Added the following parameters to the Power-down Current (Ipd) DC Characteristics: DC40g, DC40h, DC40i, and DC41g, (see Table 31-7). Added parameter IM51 and Note 3 to the I2Cx Bus Data Timing Requirements (Master Mode) (see Table 31-32). Updated parameter AD57 (Tsamp) in the Analog-to-Digital Conversion Timing Requirements (see Table 31-38). 32.0 "Packaging Information" Updated the 64-Lead Plastic Quad Flat, No Lead Package (MR) – 9x9x0.9 mm Body [QFN] packing diagram. Product Identification System Added the new V-Temp (V) temperature information.

Pre Change: N/A**Post Change:** N/A**Impacts to Data Sheet:** None**Reason for Change:** To Improve Productivity**Change Implementation Status:** Complete**Date Document Changes Effective:** 03 Apr 2013**NOTE:** Please be advised that this is a change to the document only the product has not been changed..**Markings to Distinguish Revised from Unrevised Devices:**N/A**Attachment(s):** [PIC32MX5XX/6XX/7XX Family Data Sheet](#)

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Parts Affected

PIC32MX795F512L
PIC32MX775F512L
PIC32MX795F512H
PIC32MX775F512H
PIC32MX775F256H
PIC32MX575F512L
PIC32MX575F512H
PIC32MX675F512H
PIC32MX675F256H
PIC32MX775F256L
PIC32MX675F512L
PIC32MX675F256L
PIC32MX575F256H
PIC32MX575F256L
PIC32MX695F512H
PIC32MX695F512L
PIC32MX664F064L
PIC32MX564F128L
PIC32MX764F128L
PIC32MX664F128L
PIC32MX534F064L
PIC32MX564F064L
PIC32MX534F064H
PIC32MX564F064H
PIC32MX664F064H
PIC32MX564F128H
PIC32MX664F128H
PIC32MX764F128H