

Product Change Notification - CYER-11GVRH526

Date: 21 Jun 2013

Product Category: 16-bit Microcontrollers and Digital Signal Controllers; 32-bit PIC Microcontrollers; Touch Sensing Technologies; 8-bit Microcontrollers

Notification subject: CCB 1313 Additional Fabrication Site Initial Notice: Qualification of .18um embedded flash 20K technology at an additional fabrication site.

Notification text:

PCN Status:
Initial notification

Microchip Parts Affected:
See attachments of affected catalog part numbers (CPN) labeled as...
[PCN_CYER-11GVRH526_Affected_CPN.xls](#)
[PCN_CYER-11GVRH526_Affected_CPN.pdf](#)

Description of Change:
Qualification of .18um embedded flash 20K technology at an additional fabrication site.

Impacts to Data Sheet:
None

Reason for Change:
To improve manufacturability

Change Implementation Status:
In Progress

Estimated First Ship Date:
January 17, 2014 (date code: 1442)

NOTE: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Markings to Distinguish Revised from Unrevised Devices:
Traceability code

Revision History:
June 21, 2013: Issued initial notification.

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

Attachment(s):

[PCN_CYER-11GVRH526_Affected CPN.pdf](#) [PCN_CYER-11GVRH526_Qual Plan.pdf](#)
[PCN_CYER-11GVRH526_Affected CPN.xls](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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PCN_CYER-11GVRH526
CATALOG_PART_NBR
HA1620-I/PT
HA1620T-I/PT
MTCH6301-I/ML
MTCH6301-I/PT
MTCH6301T-I/ML
MTCH6301T-I/PT
PIC18F65J94-I/MR
PIC18F65J94-I/PT
PIC18F65J94T-I/MR
PIC18F65J94T-I/PT
PIC18F66J94-I/MR
PIC18F66J94-I/PT
PIC18F66J94T-I/MR
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PIC18F67J94-I/MR
PIC18F67J94-I/PT
PIC18F67J94T-I/MR
PIC18F67J94T-I/PT
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PIC18F85J94T-I/PT
PIC18F86J94-I/PT
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PIC24FJ128GA308-I/PTC03
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PIC24FJ128GA310-I/PT
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PIC24FJ128GB206T-I/PT
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PIC32MX450F256H-V/PT
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PIC32MX450F256L-V/PT
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PIC32MX534F064HT-I/MR
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PIC32MX534F064L-I/PF
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PIC32MX534F064LT-I/BG
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PIC32MX564F064LT-V/PT
PIC32MX564F064L-V/BG
PIC32MX564F064L-V/PF
PIC32MX564F064L-V/PT
PIC32MX564F128H-I/MR
PIC32MX564F128H-I/PT
PIC32MX564F128HT-I/MR
PIC32MX564F128HT-I/MRC21
PIC32MX564F128HT-I/PT
PIC32MX564F128HT-V/MR
PIC32MX564F128HT-V/PT
PIC32MX564F128H-V/MR
PIC32MX564F128H-V/PT
PIC32MX564F128L-I/BG
PIC32MX564F128L-I/PF
PIC32MX564F128L-I/PT
PIC32MX564F128LT-I/BG
PIC32MX564F128LT-I/PF
PIC32MX564F128LT-I/PT
PIC32MX564F128LT-V/BG
PIC32MX564F128LT-V/PF
PIC32MX564F128LT-V/PT
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PIC32MX664F064LT-I/PF
PIC32MX664F064LT-I/PT
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PIC32MX664F064LT-V/PF
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PIC32MX764F128H-I/PT
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PIC32MX764F128L-I/BG
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PIC32MX764F128L-V/PF
PIC32MX764F128L-V/PT



MICROCHIP

QUALIFICATION PLAN

PCN#: CYER-11GVRH526

**Date:
June 7, 2013**

**Qualification of .18um embedded flash 20K technology
at an additional fabrication site.**

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Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days
ELFR	+125°C for 24 hours (equivalent to 150°C for 24 hrs due to higher junction temperature); Electrical test pre and post stress at -40°C, +25°C and hot temp.	300	5	4	1220	0	7
HTOL/DLT	+125°C for 408 hours (equivalent to 150°C for 408 hrs due to higher junction temperature); Electrical test pre and post stress at -40°C, +25°C, and hot temp. Readout at 0, 96 and 408 hours if fresh samples used.	200	5	4	820	0	30
EDR Endurance Cycling (Retention Bake)	Specified E/W cycles of Data EE and/or Program Memory at +25°C followed by 150°C, 48hrs bake I and bake II tests.	77	5	4	328	0	10
EDR High Temp Bake(Retention Bake)	+175°C for 504 hours; Electrical test pre and post stress at 25°C and hot temp. Readouts at 0, 96, 504 hours.	77	5	4	328	0	40
EDR Endurance Cycling (DLT)	Specified E/W cycles of Data EE and/or Program Memory at +25°C followed by 150°C, 48hrs bake I and bake II tests.	77	5	4	328	0	10

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days
EDR HTOL(DLT)	+125°C for 408 hours (equivalent to 150°C for 408 hrs due to higher junction temperature); Electrical test pre and post stress at 25°C and hot temp. Readouts at 0, 96, 408 hours.	77	5	4	328	0	40
ESD - HBM	Electrical test pre and post stress at 25°C and hot temp. Test at each voltage: 500V, 1KV, 2KV, 4KV	12	0	2	24	0	5
ESD - MM	Electrical test pre and post stress at 25°C and hot temp. Test at each voltage: 100V, 200V, 300V, 400V	12	0	2	24	0	5
ESD Induced Latch-Up	Tested at 25°C at MAX operating voltage per QCI30561. Electric test pre and post stress at 25°C and hot temp.	5	0	2	10	NA	5
ESD-CDM	Tested per ESDA STM 5.3.1-1999 spec at 250V, 500V, 750V, 1000V, 1500V, and 2000V. Electric test pre and post stress at 25°C and hot temp.	18	0	2	36	0	5
Latch up	Over voltage and over current. Latch-up stress testing should be performed at room and max operating temperature. Electrical test pre and post stress at 25°C and hot temp per Q100-004.	12	0	2	24	0	5

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days
Electrical Distributions	Characterize device electrical parameters at -40°C, 25°C, and hot temp.	30	0	2	60	NA	10