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Product Change Notification - SYST-27NJIG074

Date:	27 Feb 2012
Product Category:	All 16-bit Microcontrollers; 16-bit Microcontrollers and Digital Signal Controllers; 8-bit Microcontrollers
Device Family:	
Notification subject:	Data Sheet - dsPIC33EPXXX(GP/MC/MU)806/810/814 and PIC24EPXXX(GP/GU)810/814 Data Sheet Data Sheet Document Revision
Notification text:	

SYST-27NJIG074

Microchip has released a new DeviceDoc for the dsPIC33EPXXX(GP/MC/MU)806/810/814 and PIC24EPXXX(GP/GU)810/814 Data Sheet of devices. If you are using one of these devices please read the document located at dsPIC33EPXXX(GP/MC/MU)806/810/814 and PIC24EPXXX(GP/GU)810/814 Data Sheet.

Notification Status: Final

Description of Change: This revision includes typographical and formatting changes throughout the data sheet text. In addition, where applicable, new sections were added to each peripheral chapter that provide information and links to related resources, as well as helpful tips. For examples, see Section 18.1 "SPI Helpful Tips" and Section 18.2 "SPI Resources". The major changes are as follows: "16-bit Microcontrollers and Digital Signal Controllers (up to 512 KB Flash and 52 KB SRAM) with High-Speed PWM, USB, and Advanced Analog" The content on the first page of this section was extensively reworked to provide the reader with the key features and functionality of this device family in an "at-a-glance" format. The following devices were added to the Controller Families table (see Table 1 and the "Pin Diagrams" section): * dsPIC33EP512MC806 * dsPIC33EP512GP806 * PIC24EP512GP806 Section 2.0 "Guidelines for Getting Started with 16-bit Digital Signal Controllers and Microcontrollers" Added Section 2.9 "Application Examples" Section 3.0 "CPU" Updated the Status Register information in the Programmer's Model (see Figure 3-2). Section 4.0 "Memory Organization" Added Interrupt Controller Register Maps (see Table 4-6 and Table 4-7). Added Peripheral Pin Select Output Register Map (see Table 4-39). Added PMD Register Maps (see Table 4-50 and Table 4-51). Added PORTF Register Map (see Table 4-64). Added PORTG Register Map (see Table 4-67). Updated the second note in Section 4.7 "Bit-Reversed Addressing (dsPIC33EPXXXMU806/810/814 Devices Only)". Section 11.0 "I/O Ports" Added RPOR10: Peripheral Pin Select Output Register 10 (see Register 11-54). Section 14.0 "Input Capture" Updated the Input Capture Module Block Diagram (see Figure 14-1). Section 15.0 "Output Compare" Updated the Output Compare Module Block Diagram (see Figure 15-1). Section 25.0 "Op amp/Comparator Module" Updated the User-programmable Blanking Function Block Diagram (see Figure 25-3). Updated the bit definitions in the Comparator Mask Gating Control Register (see Register 25-4). Section 29.0 "Special Features" Added Note 3 to the Configuration Bits Description (see Table 29-2). Section 32.0 "Electrical Characteristics" Updated the I/O pin Absolute Maximum Ratings. Updated Note 1 in the DC Characteristics: Operating Current (see Table 32-5). Updated Note 1 in the DC Characteristics: Idle Current (see Table 32-6). Updated Note 1 in the DC Characteristics: Power-down Current (see Table 32-7). Updated Note 1 in the DC Characteristics: Doze Current (see Table 32-8). Removed parameters DO16 and DO26, added parameter DO26a, updated parameters DO10 and DO20, and added Note 1 in the DC Characteristics: I/O Pin Output Specifications (see Table 32-10).

Pre Change: N/A

Post Change: N/A

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 27 Feb 2012

NOTE: Please be advised that this is a change to the document only the product has not been changed..

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachment(s):	dsPIC33EPXXX(GP/MC/MU)806/810/814 and PIC24EPXXX(GP/GU)810/814 Data Sheet

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Parts Affected

PIC24EP512GU814
dsPIC33EP256MU814
PIC24EP256GU814
dsPIC33EP512MU814
dsPIC33EP512MU810
dsPIC33EP256MU810
PIC24EP512GU810
PIC24EP256GU810
dsPIC33EP512GP806
dsPIC33EP512MC806
PIC24EP512GP806
dsPIC33EP256MU806

Date: Tuesday, February 28, 2012