

Date: 02 Aug 2012

Product Category: 16-bit Microcontrollers and Digital Signal Controllers; 8-bit Microcontrollers

Device Family:  

Notification subject: Additional Fabrication Site: Qualification of YGANx, YGAYx, YGAWx, YGAUx, YGASx, YGARx, and YGAQx masks at an additional wafer fabrication site.

Notification text: PCN Status:
Final notification

Microchip Part#s Affected:
See attachments of affected catalog part numbers (CPN) labeled as...
PCN_CYER-09IGBQ791_Affected_CPN.xls
PCN_CYER-09IGBQ791_Affected_CPN.pdf

Description of Change:
Qualification of YGANx, YGAYx, YGAWx, YGAUx, YGASx, YGARx, and YGAQx masks at an additional wafer fabrication site.

Impacts to Data Sheet:
None

Reason for Change:
To Improve production cycle time and on-time delivery performance

Change Implementation Status:
In Progress

Estimated First Ship Date:
YGANx: June 30, 2011 (Date code 1127)
YGAYx: June 30, 2011 (Date code 1127)
YGAWx: May 20, 2011 (Date code 1121)
YGAUx: January 14, 2011 (Date code 1103)
YGASx: May 20, 2011 (Date code 1121)
YGARx: May 20, 2011 (Date code 1121)
YGAQx: June 30, 2011 (Date code 1127)

NOTE: Please be advised that after the first ship date customers may receive the affected products from either qualified wafer fab.

Markings to Distinguish Revised from Unrevised Devices:

Traceability code

Revision History:

March 10, 2011: Final notification issued.

August 2, 2012: No changes to PCN. Reposting to archives.

Attachment(s):

[PCN_CYER-09IGBQ791_Affected_CPN.xls](#) [PCN_CYER-09IGBQ791_Affected_CPN.pdf](#) [PCN_CYER-09IGBQ791_Qual Data.pdf](#)

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Product Change Notification - CYER-09IGBQ791

Parts Affected

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dsPIC33FJ06GS102
dsPIC33FJ06GS202
dsPIC33FJ128GP202
dsPIC33FJ128GP204
dsPIC33FJ128GP206A
dsPIC33FJ128GP306A
dsPIC33FJ128GP310A
dsPIC33FJ128GP706A
dsPIC33FJ128GP708A
dsPIC33FJ128GP710A
dsPIC33FJ128GP802
dsPIC33FJ128GP804
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PCN_CYER-09IGBQ791
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PIC24HJ64GP206AT-E/PTA27
PIC24HJ64GP206AT-I/MR

PIC24HJ64GP206AT-I/MRA28
PIC24HJ64GP206AT-I/PT
PIC24HJ64GP206-E/PTA26
PIC24HJ64GP206T-E/PTA26
PIC24HJ64GP210A-E/PF
PIC24HJ64GP210A-E/PT
PIC24HJ64GP210A-I/PF
PIC24HJ64GP210A-I/PT
PIC24HJ64GP210AT-E/PF
PIC24HJ64GP210AT-E/PT
PIC24HJ64GP210AT-I/PF
PIC24HJ64GP210AT-I/PT
PIC24HJ64GP502-E/MM
PIC24HJ64GP502-E/SO
PIC24HJ64GP502-E/SP
PIC24HJ64GP502-H/MM
PIC24HJ64GP502-H/SO
PIC24HJ64GP502-I/MM
PIC24HJ64GP502-I/SO
PIC24HJ64GP502-I/SOA21
PIC24HJ64GP502-I/SP
PIC24HJ64GP502T-E/MM
PIC24HJ64GP502T-E/SO
PIC24HJ64GP502T-H/MM
PIC24HJ64GP502T-H/SO
PIC24HJ64GP502T-I/MM
PIC24HJ64GP502T-I/SO
PIC24HJ64GP502T-I/SOA21
PIC24HJ64GP504-E/ML
PIC24HJ64GP504-E/PT
PIC24HJ64GP504-I/ML
PIC24HJ64GP504-I/PT
PIC24HJ64GP504-I/PTA23
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PIC24HJ64GP504T-E/PT
PIC24HJ64GP504T-I/ML
PIC24HJ64GP504T-I/PT
PIC24HJ64GP504T-I/PTA23
PIC24HJ64GP506A-E/MR
PIC24HJ64GP506A-E/PT
PIC24HJ64GP506A-I/MR
PIC24HJ64GP506A-I/PT
PIC24HJ64GP506AT-E/MR
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PIC24HJ64GP506AT-I/PT
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PIC24HJ64GP510A-I/PF
PIC24HJ64GP510A-I/PT
PIC24HJ64GP510AT-E/PF
PIC24HJ64GP510AT-E/PT

PIC24HJ64GP510AT-I/PF

PIC24HJ64GP510AT-I/PT



MICROCHIP

QUALIFICATION DATA

PCN #: CYER-09IGBQ791

**Date:
November 15, 2010**

**Qualification of YGAZx, UFACx, UCACx, and
UGAFx masks at an additional wafer fabrication
site. YGANx, YGAYx, YGAWx, YGAUx, YGASx,
YGARx, and YGAQx masks qualify by similarity.**

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Die Level Results

The results of die level testing are shown below.

EARLY FAILURE RATE TESTING AT 150°C

	24 Hours
Lot 1	0/803 ¹
Lot 2	0/806 ¹
Lot 3	0/805

¹ One device was discounted due visual/mechanical damage.

DYNAMIC LIFE TESTING AT 150°C

	408 Hours
Lot 1	0/621 ¹
Lot 3	0/600
Lot 4	0/603 ²

¹ Fourteen devices were removed due to visual/mechanical damage during the stress test.

² Three devices were removed due to visual/mechanical damage during the stress test.

	Infant Mortality	Total Life	MTBF (Years)
Device Hours	59,664	760,080	N/A
Fit Rate - 50% Confidence	45	4	32,430
Fit Rate - 60% Confidence	59	5	24,527
Fit Rate - 90% Confidence	149	12	9,762

	Best Estimated Failure Rate (%/KHR)
Infant Mortality	0.0045
Total Life	0.0004

ENDURANCE CYCLING AT 25°C AND BAKES AT 150°C

	10,000 E/W cycles Program Memory Cycles	Bake 1	Bake 2
Lot 3	0/256	0/78	0/78
Lot 5	0/232	0/224 ¹	0/224
Lot 6	0/234 ²	0/234	0/234

¹ Eight devices were discounted due visual/mechanical damage during post testing

² Two devices were discounted due visual/mechanical damage

HIGH TEMPERATURE RETENTION BAKE AT 175°C

	96 Hours	504 Hours
Lot 5	0/224	0/224
Lot 6	0/234	0/234
Lot 7	0/90	0/90
Lot 8	0/90	0/90
Lot 9	0/128	0/128

Activation Energy	0.6 eV
Derated Temperature	55°C

	Infant Mortality	Total Life	MTBF (Years)
Device Hours	73,536	386,064	N/A
Fit Rate - 50% Confidence	32	6	18,644
Fit Rate - 60% Confidence	43	8	14,100
Fit Rate - 90% Confidence	107	20	5,612

	Best Estimated Failure Rate (%/KHR)
Infant Mortality	0.0032
Total Life	0.0006

ELECTROSTATIC DISCHARGE TEST

	Human Body Model	Machine Model
Lot 10	Pass up to 4000V	Pass up to 400V

LATCH-UP TEST

	@ 25°C	@ 125°C
Lot 10	Passed	Passed

Note: All pins meet or exceed QCI-39000 guidelines.

CHARGED DEVICE MODEL TEST

	ESDA Model
Lot 10	±2000V