



Date: 27 Sep 2011

Product Category: 16-bit Microcontrollers and Digital Signal Controllers; 8-bit Microcontrollers; All 16-bit Microcontrollers

Device Family:  

Notification subject: CCB 1033.02: Qualification of YGAN, YGAZ, YGAH, YGAQ, YGAR, YGAS, YGAU, YGAW, YGAY, UCAA, UFAC, and UFAD masks without polyimide process.

Notification text: PCN Status:
Final notification

Microchip Parts Affected:
See attachments of affected catalog part numbers (CPN) labeled as...
PCN_CYER-22HAFC735_Affected_CPN.xls
PCN_CYER-22HAFC735_Affected_CPN.pdf

Description of Change:
Qualification of YGAN, YGAZ, YGAH, YGAQ, YGAR, YGAS, YGAU, YGAW, YGAY, UCAA, UFAC, and UFAD masks without polyimide process.

Impacts to Data Sheet:
None

Reason for Change:
To Improve Productivity

Change Implementation Status:
In Progress

Estimated First Ship Date:
November 11, 2011 (date code: 1146)

NOTE: Please be advised that during the transition period customers may receive pre and post change parts, due to existing inventory of the pre changed parts.

Markings to Distinguish Revised from Unrevised Devices:
Traceability code

Revision History:
March 4, 2011: Issued initial notification.

September 27, 2011: Issued final notification, attached qualification data, and revised estimated first ship date.

Attachment(s):

[PCN_CYER-22HAFC735_Affected_CPN.pdf](#) [PCN_CYER-22HAFC735_Affected_CPN.xls](#)
[PCN_CYER-22HAFC735_Qual Data.pdf](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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Product Change Notification - CYER-22HAFC735

Parts Affected

dsPIC33FJ06GS101
dsPIC33FJ06GS102
dsPIC33FJ06GS202
dsPIC33FJ128GP202
dsPIC33FJ128GP204
dsPIC33FJ128GP206A
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dsPIC33FJ128GP310A
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dsPIC33FJ128GP708A
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MICROCHIP

QUALIFICATION DATA

PCN #: CYER-22HAFC735

**Date:
August 1, 2011**

**Qualification of YGAN, YGAZ, YGAH, YGAQ,
YGAR, YGAS, YGAU, YGAW, YGAY, UCAA, UFAC,
and UFAD masks without polyimide process**

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**Test Conditions**

Tests are performed in accordance with Microchip specification QCI-39000, "Worldwide Quality Conformance Requirements". The various conditions are listed below.

TEST	METHOD	CONDITION	SAMPLE SIZE RUN	CRITERIA
Early Life Failure Rate (ELFR)	AEC-Q100	150°C, 24 Hrs. Electrical Test at -40°C, +25°C, +85°C	804,810, 815,820	0/800 per lot 3 Lots
High Temperature Operating Life / Dynamic Life Test	MIL-STD 883 Method 1005	150°C, 408 Hrs Electrical Test at -40°C, +25°C and +85°C	210,210, 210,220	0/200 per lot 3 Lots
Endurance Cycling and Bakes	MIL-STD 883 Method 1033	10K/1K Program Memory E/W cycles at 25°C Electrical Test at +25°C and +85°C	81,82,82, 99	0/77 per lot 3 Lots
High Temperature Storage Life / Retention Bake	MIL-STD 883 Method 1033	175°C, 504 Hrs Electrical Test at +25°C and +85°C	77,82,82, 92	0/77 per lot 3 Lots
Preconditioning	Jedec-STD-020C	150°C Bake for 24 hours. 85°C/85% R.H. for 168hrs Convection Reflow 260°C for 3 cycles Electrical Test at +25°C and +85°C	306,306, 306,305	0/all surface mount devices
Temperature Cycling	JESD22-A104	-65°C to 150°C/500 cycles Electrical Test at +25°C and +85°C	82, 82, 82, 82	3 Lots 0/77
Autoclave	JESD22-A102	121.5C/15psig/96hrs Electrical Test at +25°C and +85°C	82, 82, 82, 82	3 Lots 0/77
HAST	JESD22-A110	130°C/85%/96hrs Electrical Test @ 25°C and 85°C	82, 82, 81, 81	3 Lots 0/77

Die Level Results

The results of die level testing are shown below.

EARLY FAILURE RATE TESTING AT 150°C

	24 Hours
Lot 1	0/804
Lot 3	0/810
Lot 5	0/813 ¹
Lot 7	0/817 ²

¹ Two devices were removed due to visual/mechanical damage during post testing

² Three devices were removed due to visual/mechanical damage during post testing

DYNAMIC LIFE TESTING AT 150°C

	408 Hours
Lot 1	0/210
Lot 3	0/210
Lot 5	0/210
Lot 7	0/216 ¹

¹ Four devices were removed due to visual/mechanical damage during post testing

Activation Energy	0.7 eV
Derated Temperature	55°C

	Infant Mortality	Total Life	MTTF (Years)
Device Hours	77,976	400,536	N/A
Fit Rate - 50% Confidence	34	7	17,090
Fit Rate - 60% Confidence	45	9	12,925
Fit Rate - 90% Confidence	114	22	5,144

	Best Estimated Failure Rate (%/KHR)
Infant Mortality	0.0034
Total Life	0.0007

ENDURANCE CYCLING AT 25°C AND BAKE AT 175°C

	10K E/W cycles Program Memory Cycles	1K E/W cycles Program Memory Cycles	100 E/W cycles Program Memory Cycles	96 Hours	504 Hours
Lot 2	0/81	N/A	N/A	0/77 ¹	0/77
Lot 4	N/A	0/82	N/A	0/82	0/82
Lot 6	N/A	N/A	0/82	0/82	0/82
Lot 8	0/99	N/A	N/A	0/92 ²	0/91

¹ Four devices were removed due to visual/mechanical damage during post testing

² Seven devices were removed due to visual/mechanical damage during post testing

Activation Energy	0.6 eV
Derated Temperature	55°C

	Infant Mortality	Total Life	MTTF (Years)
Device Hours	31,872	167,328	N/A
Fit Rate - 50% Confidence	74	14	8,081
Fit Rate - 60% Confidence	98	19	6,111
Fit Rate - 90% Confidence	246	47	2,433

	Best Estimated Failure Rate (%/KHR)
Infant Mortality	0.0074
Total Life	0.0014

PRECONDITIONING

Lot 9	0/306
Lot 10	0/306
Lot 11	0/306
Lot 12	0/305

TEMPERATURE CYCLE

	500 cycles
Lot 9	0/82
Lot 10	0/82
Lot 11	0/82
Lot 12	0/82

HAST

	96 hours
Lot 9	0/82
Lot 10	0/82
Lot 11	0/81
Lot 12	0/81

AUTOCLAVE

	96 hours
Lot 9	0/82
Lot 10	0/82
Lot 11	0/82
Lot 12	0/82