



Date: 04 Mar 2011

Product Category: 16-bit Microcontrollers and Digital Signal Controllers; 8-bit Microcontrollers; All 16-bit Microcontrollers

Device Family:  

Notification subject: CCB 1033.02: Qualification of YGAN, YGAZ, YGAH, YGAQ, YGAR, YGAS, YGAU, YGAW, YGAY, UCAA, UFAC, and UFAD masks without polyimide process.

Notification text: PCN Status:
Initial notification

Microchip Parts Affected:
See attachments of affected catalog part numbers (CPN) labeled as...
PCN_CYER-22HAFC735_Affected_CPN.xls
PCN_CYER-22HAFC735_Affected_CPN.pdf

Description of Change:
Qualification of YGAN, YGAZ, YGAH, YGAQ, YGAR, YGAS, YGAU, YGAW, YGAY, UCAA, UFAC, and UFAD masks without polyimide process.

Impacts to Data Sheet:
None

Reason for Change:
To Improve Productivity

Change Implementation Status:
In Progress

Estimated First Ship Date:
June 1, 2011 (date code: 1123)

NOTE: Please be advised that during the transition period customers may receive pre and post change parts, due to existing inventory of the pre changed parts.

Markings to Distinguish Revised from Unrevised Devices:
Traceability code

Revision History:

March 4, 2011: Issued initial notification.

Attachment(s):

[PCN_CYER-22HAFC735_Affected_CPN.pdf](#) [PCN_CYER-22HAFC735_Affected_CPN.xls](#) [PCN_CYER-22HAFC735_Qual Plan.pdf](#)

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Parts Affected

dsPIC33FJ06GS101
dsPIC33FJ06GS102
dsPIC33FJ06GS202
dsPIC33FJ128GP202
dsPIC33FJ128GP204
dsPIC33FJ128GP206A
dsPIC33FJ128GP306A
dsPIC33FJ128GP310A
dsPIC33FJ128GP706A
dsPIC33FJ128GP708A
dsPIC33FJ128GP710A
dsPIC33FJ128GP802
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PCN_CYER-22HAFC735
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MICROCHIP

QUALIFICATION PLAN

PCN #: CYER-22HAFC735

February 21, 2011

Qualification of YGAN and YGAZ masks without polyimide process. Masks YGAH, YGAQ, YGAR, YGAS, YGAU, YGAW, YGAY, UCAA, UFAC, and UFAD will qualify by similarity.

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days	Special Instructions
Wire Bond Pull - WBP	Mil. Std. 883-2011	5	0	1	10	0 fails after TC	5	30 bonds from a min. 5 devices.
Wire Bond Shear - WBS	CDF-AEC-Q100-001	5	0	1	10	0 fails after TC	5	30 bonds from a min. 5 devices.
External Visual	Mil. Std. 883-2009/2010	All devices prior to submission for qualification testing	0	1	ALL	0	5	
HTSL (High Temp Storage Life)	+175 C for 500 hours. Electrical test pre and post stress at +25C and +85C.	45	5	1	100	0	10	Spares should be properly identified
Preconditioning - Required for surface mount devices	+150°C Bake for 24 hours. 85°C/85% RH for 168 hrs + 3X reflow at 260°C peak (MSL 1); Electrical test pre and post stress at +25°C.	231	5	1	472	0	15	Spares should be properly identified

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail/Accept Qty	Est. Dur. Days	Special Instructions
HAST	+130°C/85% RH for 96 hours. Electrical test pre and post stress at +25°C and +85°C.	77	5	1	164	0	10	Spares should be properly identified. Use the parts which have gone through Pre-conditioning.
Autoclave	+121°C/15 psig for 96 hours. Electrical test pre and post stress at +25°C	77	5	1	164	0	10	Spares should be properly identified. Use the parts which have gone through Pre-conditioning.
Temp Cycle	-65°C to +150°C for 500 cycles. Electrical test pre and post stress at +85°C; 3 gram force WBP, on 5 devices from 1 lot, test following each read point.	77	5	1	164	0	15	Spares should be properly identified. Use the parts which have gone through Pre-conditioning.