

Product Change Notification - GBNG-15KQFZ896

Date: 11 Jul 2017
Product Category:
Notification subject: CCB 2971 Final Notice: Implement marking changes for virtually all Atmel products
Notification text: **PCN Status:** Final notification.

Microchip Parts Affected:
All virtually Atmel products.

Description of Change:
Implement marking changes for virtually all Atmel products

Pre Change:
Atmel top marking format and with bottom marking.

Post Change:
Microchip top marking format, traceability code and without bottom mark.

Pre and Post Change Summary:

	Pre Change	Post Change
Top Mark	Atmel top marking format	Microchip top marking format and traceability code
Bottom Mark	With bottom marking	Without bottom marking
	See attached pre and post marking guidelines	

Impacts to Data Sheet:
Where applicable

Change Impact:
None

Reason for Change:
To improve manufacturability and traceability by standardizing marking format for virtually all Atmel products as part of the integration of Microchip and Atmel.

Change Implementation Status:
In Progress

Estimated First Ship Date:
September 01, 2017

Note: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Time Table Summary:

Workweek	July 2017					-->	September 2017				
	27	28	29	30	31		35	36	37	38	39
Final PCN Issue Date		X									
Estimated Implementation Date							X				

Method to Identify Change:
Top marking and Traceability Code

Qualification Report:
Not applicable

Revision History:
July 11, 2017: Issued final notification.

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

Attachment(s): [PCN_GBNG-15KQFZ896_Part Marking Guideline_Part1.pdf](#)
[PCN_GBNG-15KQFZ896_Part Marking Guideline_Part2.pdf](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.



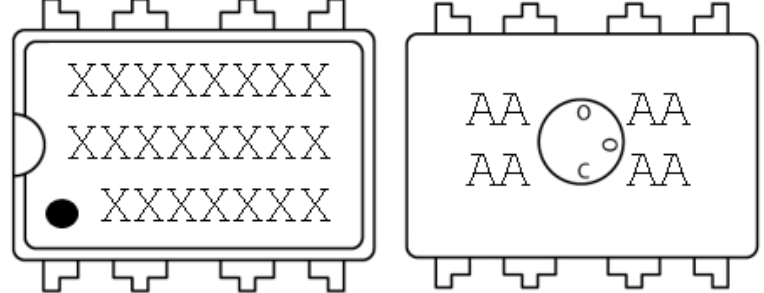
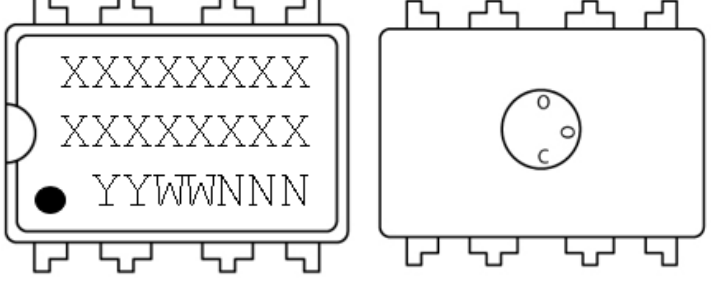
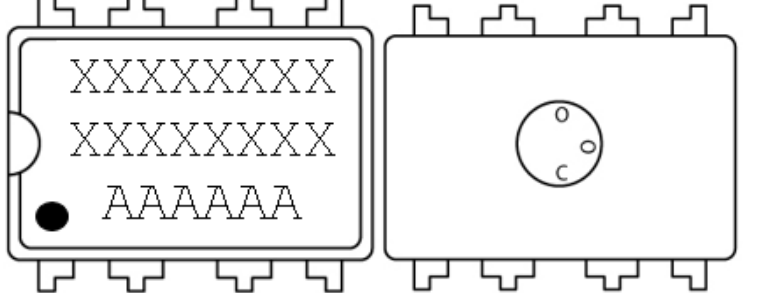
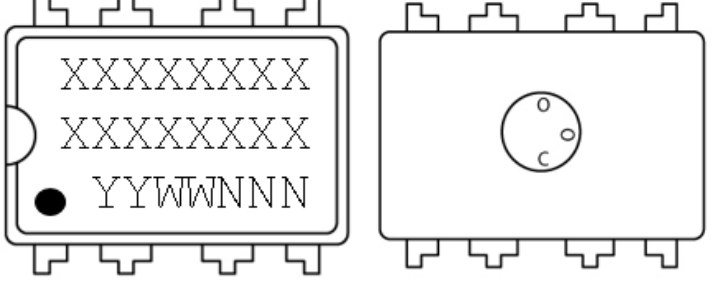
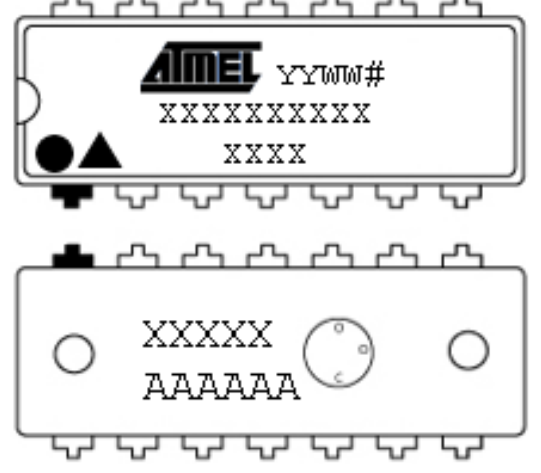
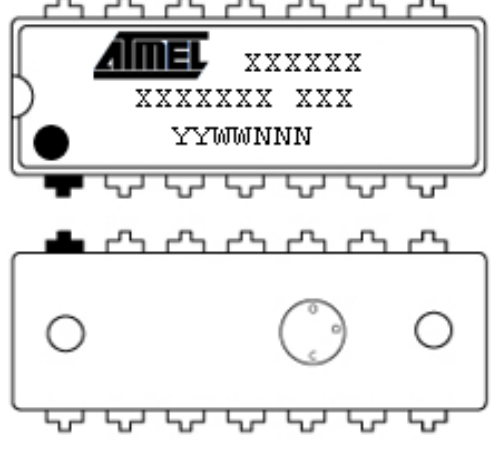
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PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
8	PDIP 300 MIL		<p>Top Mark Line 1= Device Name Line 2 = Device Information Line 3 = Class Code, Date Code ● = Pin 1 indicator</p> <p>OR</p> <p>Line 1= ATMEL, Date Code Line 2 = Device Name Line 3 = Device Information</p> <p>Bottom Mark Lot Number Country of Origin in the injector mold</p>		<p>Top Mark Line 1= Device Name, Class Code Line 2 = Device Information Line 3 = Lot Traceability ● = Pin 1 indicator</p> <p>OR</p> <p>Line 1= ATML, Class Code, Date Code Line 2 = Device Information, Country of Origin Line 3 = Lot Traceability</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
			<p>Top Mark Line 1= ATML, Class Code, Date Code Line 2 = Device Information Line 3 = Lot Traceability</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>		<p>Top Mark Line 1= Device Name, Class Code Line 2 = Device Information Line 3 = Lot Traceability ● = Pin 1 indicator</p> <p>OR</p> <p>Line 1= ATML, Class Code, Date Code Line 2 = Device Information, Country of Origin Line 3 = Lot Traceability</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
			<p>Top Mark Line 1= Atmel Logo, Date Code, MRL (if shown in ABI) Line 2 = Device Name Line 3 = Device Information ● ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Line 2 = Lot Traceability Country of Origin in injector mold</p>		<p>Top Mark Line 1= Atmel Logo, Die ID, Revision Line 2 = Device Name, Device Information Line 3 = Lot Traceability ● = Pin 1 location</p> <p>OR</p> <p>Top Mark Line 1= Atmel Logo, Device Information Line 2 = Device Name Line 3 = Lot Traceability</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>



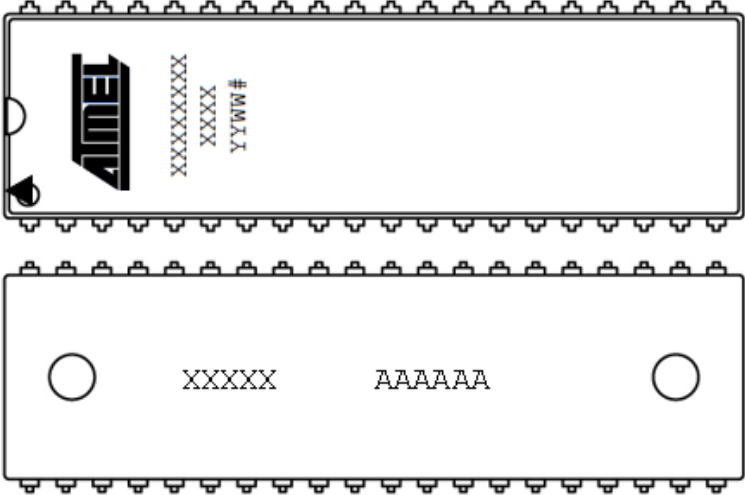
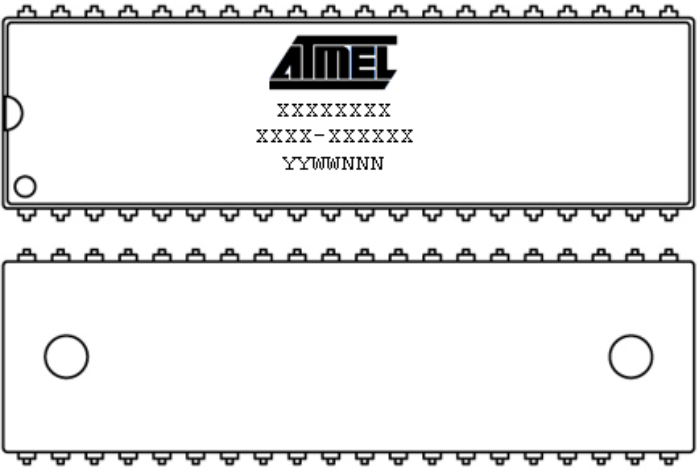
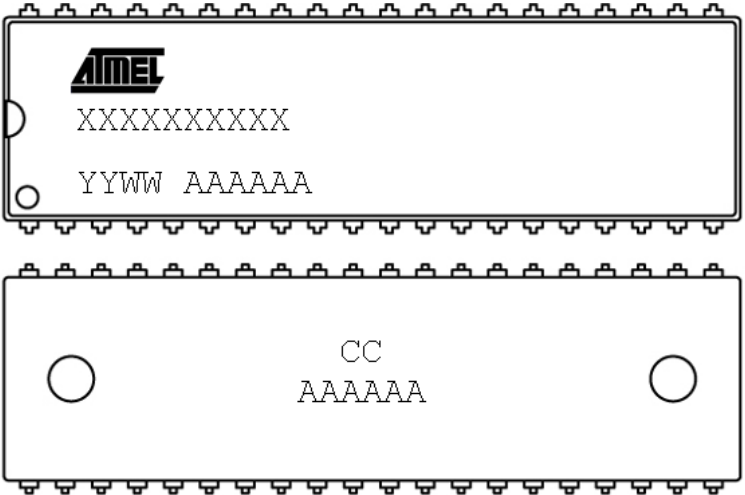
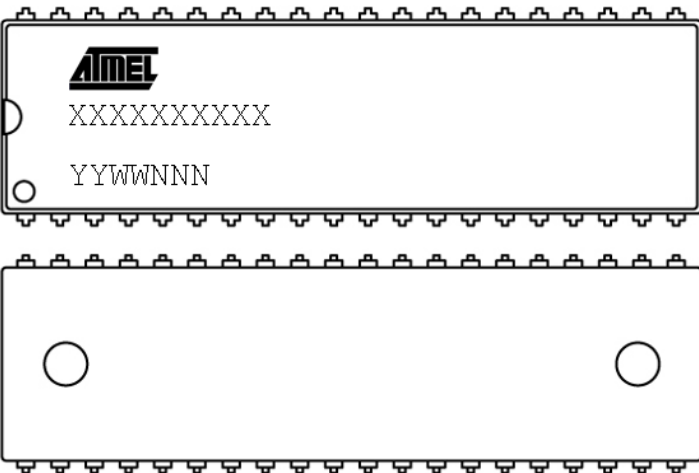
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		Atmel®		MICROCHIP	
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
14	PDIP 300 MIL		<p>Top Mark Line 1= Atmel Logo, Date Code, MRL (if shown in ABI) Line 2 = Device Name, Device Information ● ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Line 2 = Lot Traceability Country of Origin in injector mold</p>		<p>Top Mark Line 1= Atmel Logo, Die ID, Revision Line 2 = Device Name, Device Information Line 3 = Lot Traceability ● = Pin 1 location</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
20	PDIP 300 MIL		<p>Top Mark Line 1= Atmel Logo, Date Code, MRL (if shown in ABI) Line 2 = Device Name, Device Information ● ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Line 2 = Lot Traceability Country of Origin in injector mold</p>		<p>Top Mark Line 1= Atmel Logo, Die ID, Revision Line 2 = Device Name, Device Information Line 3 = Lot Traceability ● = Pin 1 location</p> <p>OR</p> <p>Top Mark Line 1= Atmel Logo, Country of Origin Line 2 = Device Name, Device Information Line 3 = Lot Traceability</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
24	PDIP300 MIL		<p>Top Mark Line 1= Atmel Logo, Date Code, MRL (if shown in ABI) Line 2 = Device Name, Device Information ● ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Line 2 = Lot Traceability Country of Origin in injector mold</p>		<p>Top Mark Line 1= Atmel Logo, Country of Origin Line 2 = Device Name, Device Information Line 3 = Lot Traceability ● = Pin 1 location</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>



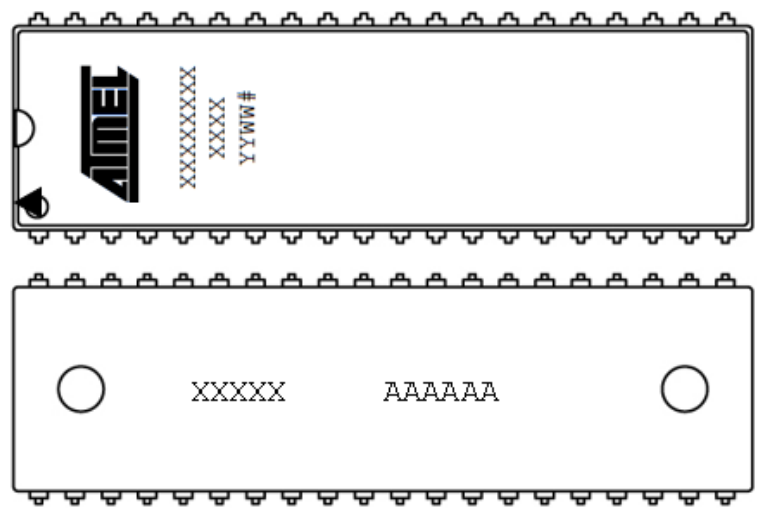
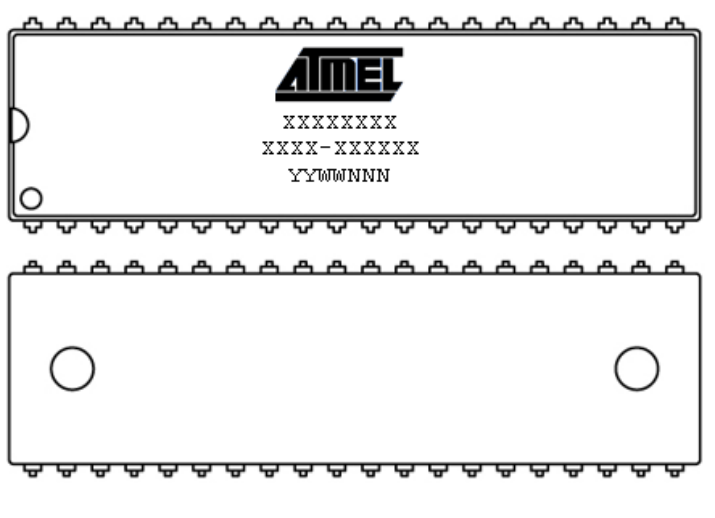


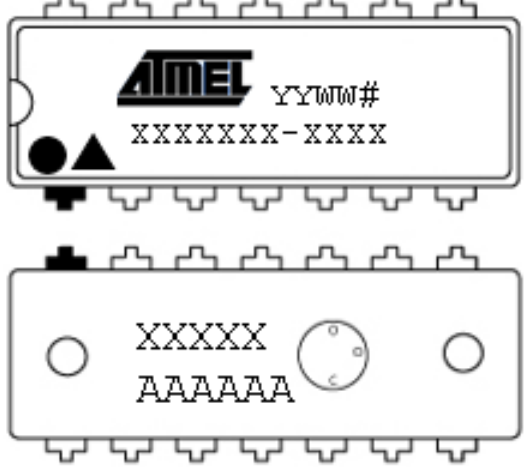
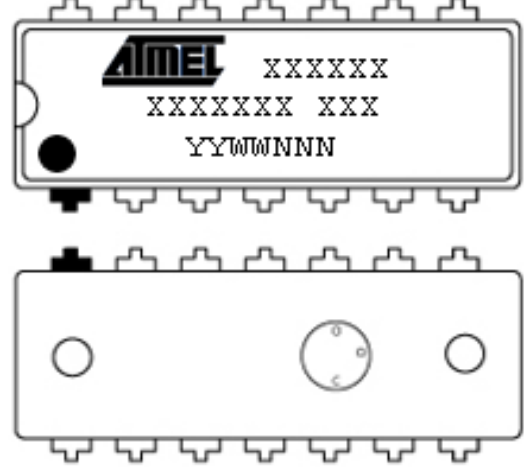
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28 / 32	PDIP 600 MIL		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin in injector mold, Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Die ID, Revision Line 4 = Lot Traceability o = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
40	PDIP 600 MIL		<p>Top Mark Line 1= ATMEL Logo Line 2 = Device Name Line 3 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= ATMEL Logo Line 2 = Device Name Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>



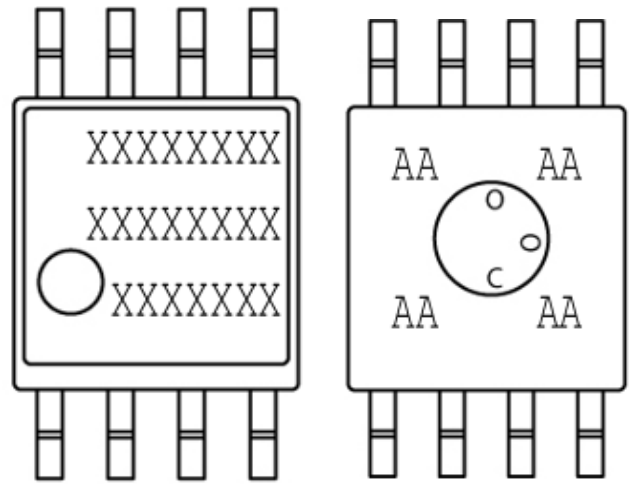
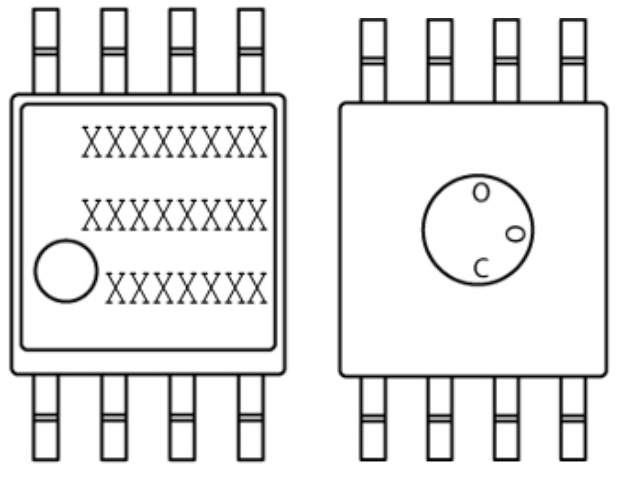
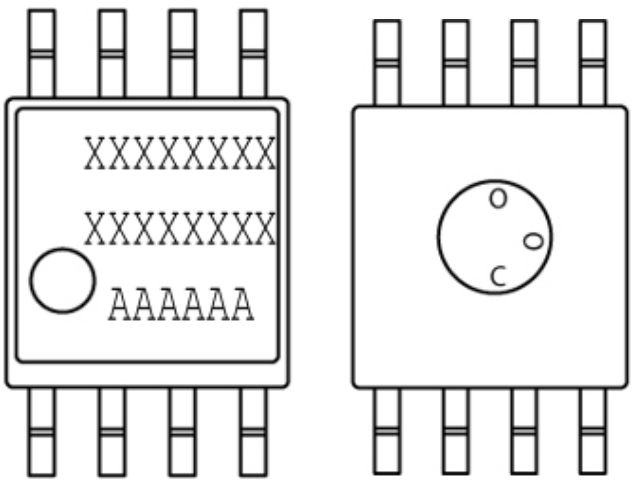
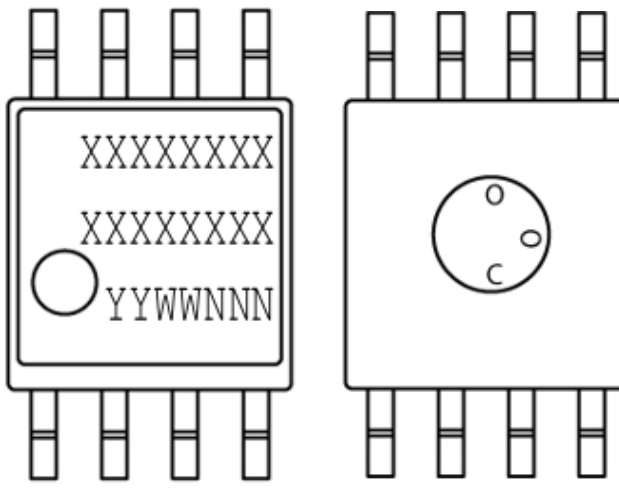
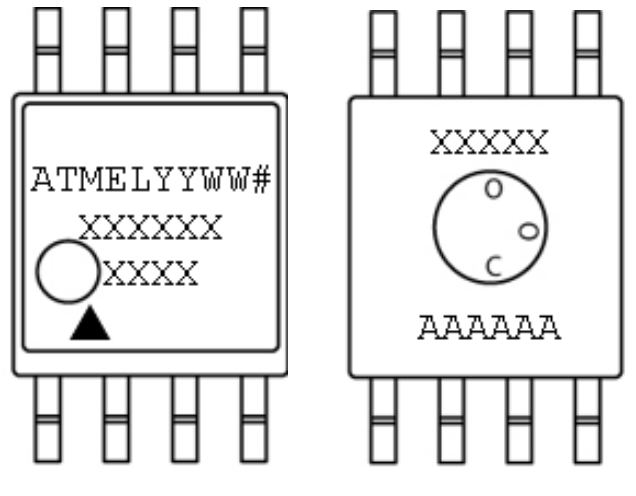
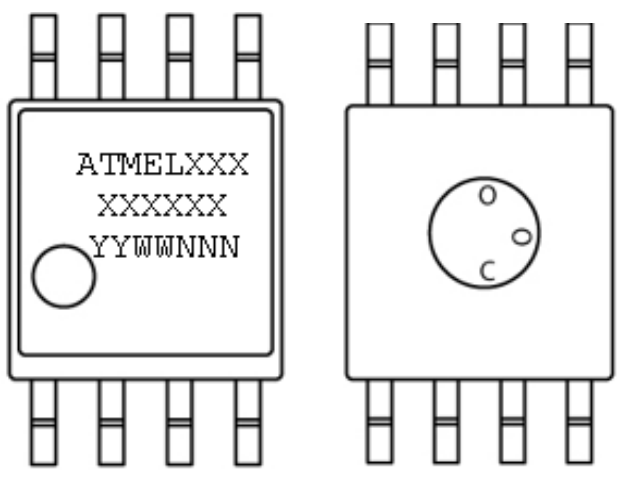
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			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold, Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Die ID, Revision Line 4 = Lot Traceability o = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold, Lot Traceability</p>		<p>Top Mark Line 1 = Atmel Logo Line 2 = Device Information, Country of Origin Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No Bottom Mark</p>
28	SPDIP		<p>Top Mark Line 1= Atmel Logo, Date Code, MRL (if shown in ABI) Line 2 = Device Name, Device Information ●▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Line 2 = Lot Traceability Country of Origin in injector mold</p>		<p>Top Mark Line 1= Atmel Logo, Die ID, Revision Line 2 = Device Name, Device Information Line 3 = Lot Traceability ● = Pin 1 location</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>



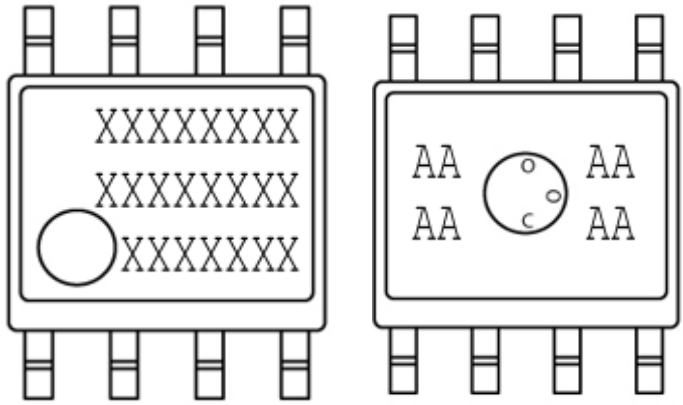
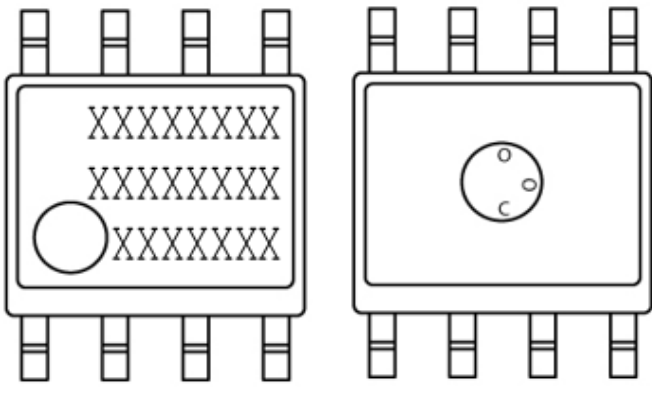
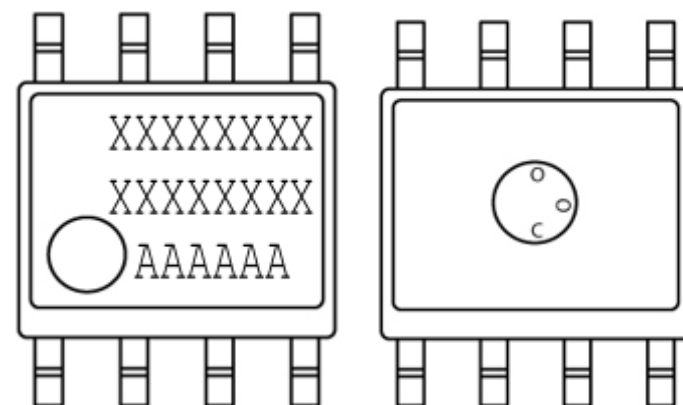
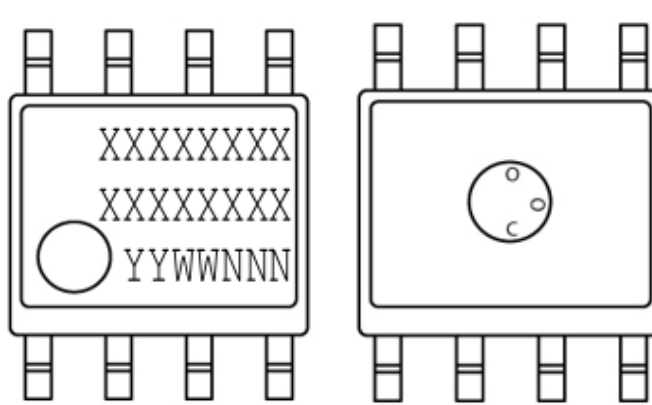
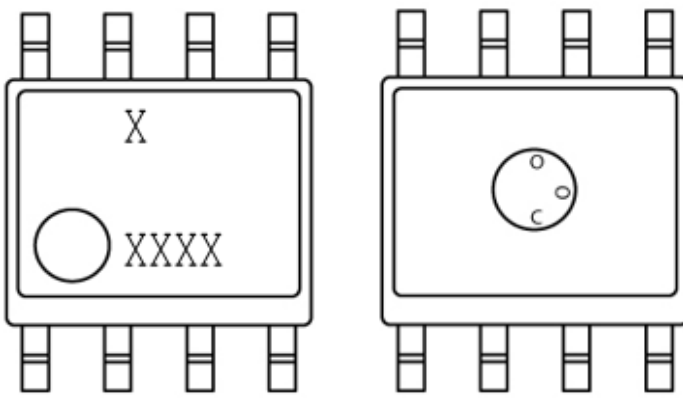
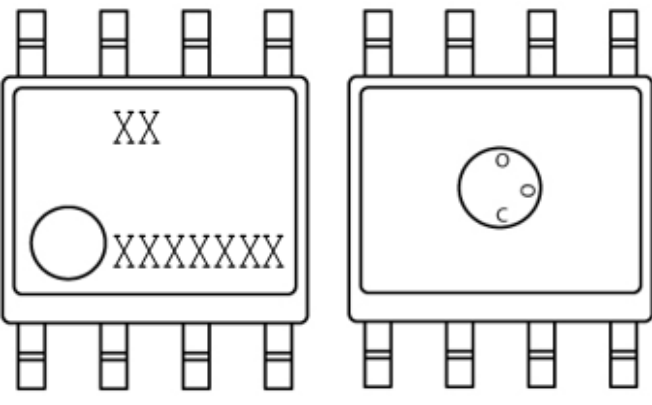
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8	SOIJ		<p>Top Mark Line 1= ATMEL, Date Code Line 2 = Device Name Line 3 = Device Information O = Pin 1 indicator</p> <p>Bottom Mark Lot Number Country of Origin in the injector mold</p>		<p>Top Mark Line 1= ATML, Class Code, Date Code Line 2 = Truncation Code, Country of Origin Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
			<p>Top Mark Line 1= ATML, Class Code, Date Code Line 2 = Truncation Code, Voltage Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>		<p>Top Mark Line 1= ATML, Class Code, Date Code Line 2 = Truncation Code, Country of Origin Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
			<p>Top Mark Line 1 = ATMEL, Date Code, MRL (if shown in ABI) Line 2 = Device Name Line 3 = Device Information O ▲ = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1 = ATMEL, Device Information Line 2 = Device Name Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>



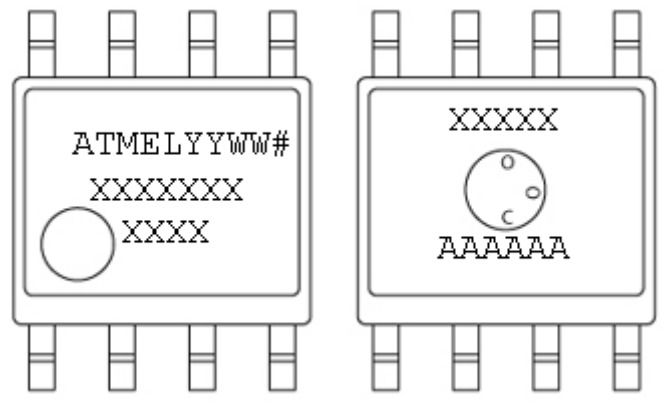
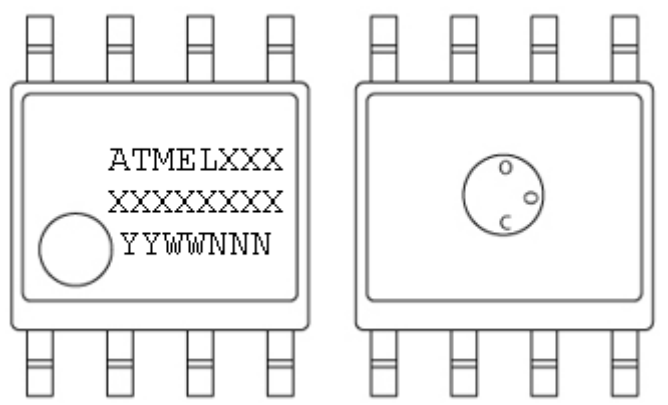
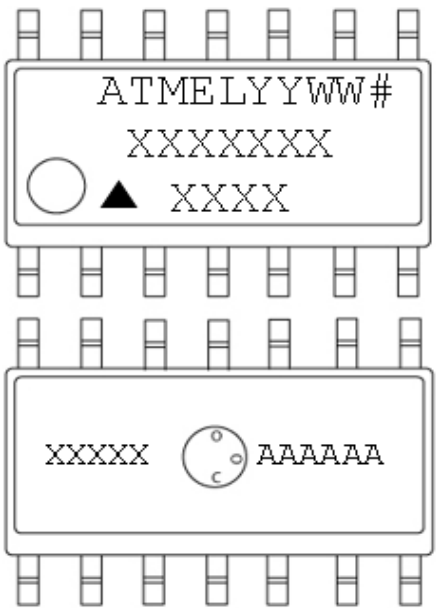
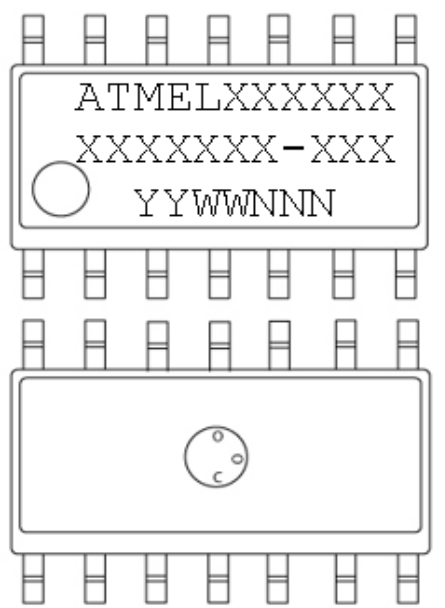
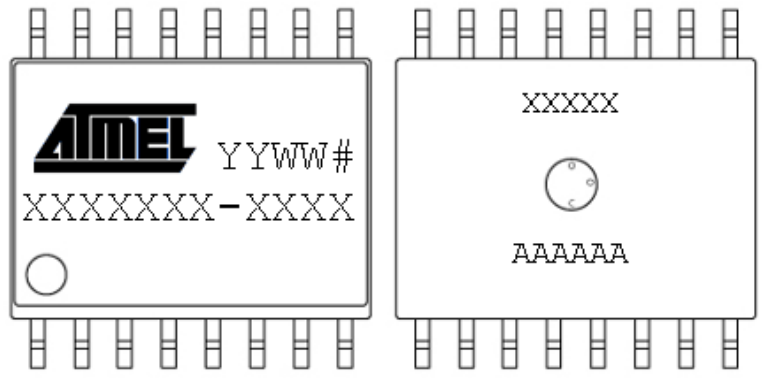
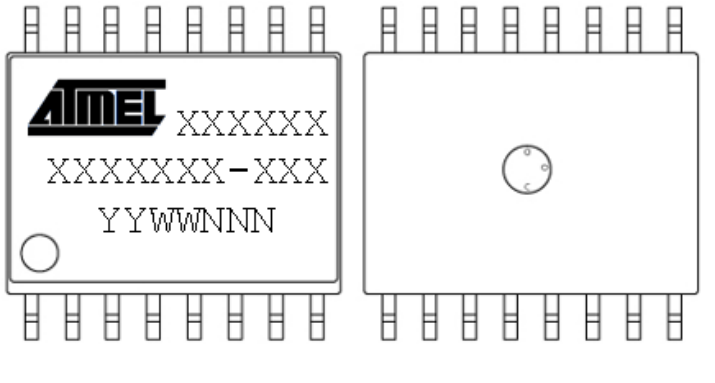
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8	SOIC		<p>Top Mark Line 1= Device Name Line 2 = Device Information Line 3 = Class Code, Date Code or Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Lot Number Country of Origin in injector mold</p>		<p>Top Mark Line 1= Device Name, Class Code Line 2 = Device Information Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
			<p>Top Mark Line 1= ATMEL, Date Code Line 2 = Device Name Line 3 = Device Information O = Pin 1 indicator</p> <p>OR Line 1= ATML, Class Code, Date Code Line 2 = Truncation Code Line 3 = Lot Traceability</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>		<p>Top Mark Line 1= ATML, Class Code, Date Code Line 2 = Device Information Line 3 = Lot Traceability</p> <p>OR Line 1= ATML, Class Code, Date Code Line 2 = Truncation Code, Country of Origin Line 3 = Lot Traceability</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
			<p>Top Mark Line 1= Subcon Code Line 2 = Date Code O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>		<p>Top Mark Line 1= Country of Origin Line 2 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>



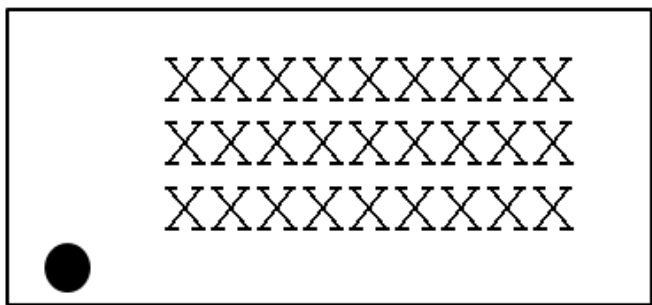
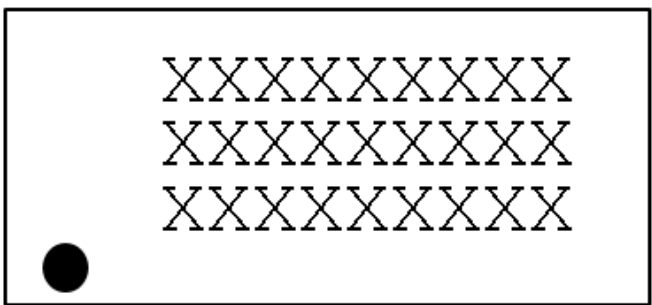
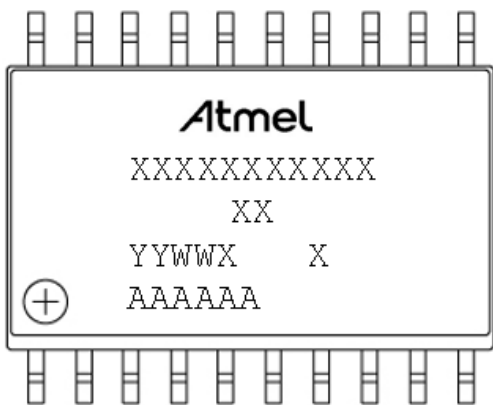
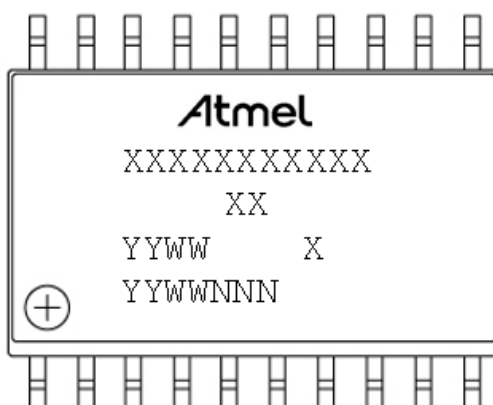
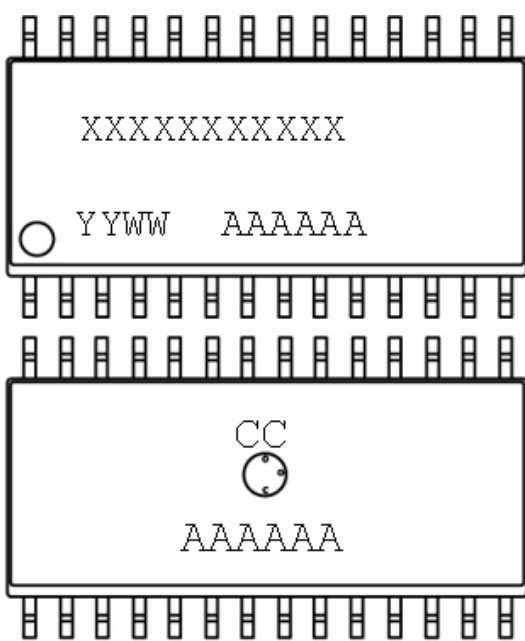
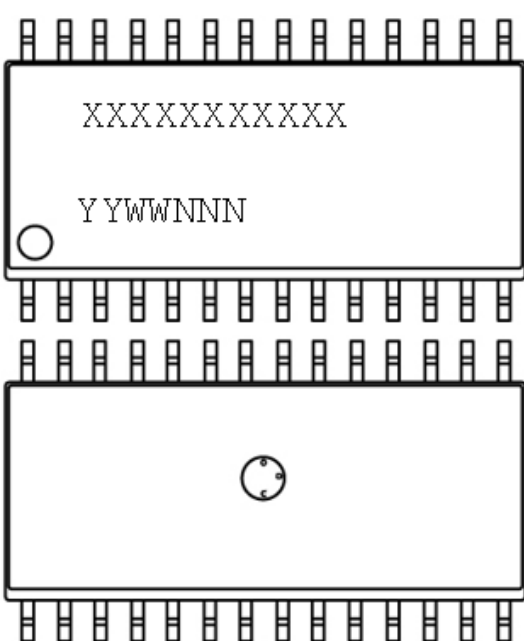
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			<p>Top Mark Line 1 = ATMEL, Date Code, MRL (if shown in ABI) Line 2 = Device Name Line 3 = Device Information O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Country of Origin in injector mold Line 2 = AAAAAAA = Lot Traceability</p>		<p>Top Mark Line 1 = ATMEL, Device Information Line 2 = Device Name Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
14	SOIC 150 MIL		<p>Top Mark Line 1 = ATMEL, Date Code, MRL (if shown in ABI) Line 2 = Device Name Line 3 = Device Information O ▲ = Pin 1 indicator</p> <p>Bottom Mark XXXXX = Country of Origin if not in injector mold Country of Origin in injector mold AAAAAAA = Lot Traceability</p>		<p>Top Mark Line 1 = ATMEL, Die ID, Revision Line 2 = Device Name, Device Information Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
16 / 20 / 24 / 28	SOIC 300 MIL		<p>Top Mark Line 1 = Atmel Logo, Date Code, MRL (if shown in ABI) Line 2 = Device Name, Device Information O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = XXXXX Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1 = Atmel Logo, Die ID, Revision Line 2 = Device Name, Device Information Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>OR</p> <p>Top Mark Line 1 = Atmel Logo, Country of Origin Line 2 = Device Name, Device Information Line 3 = Lot Traceability</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>



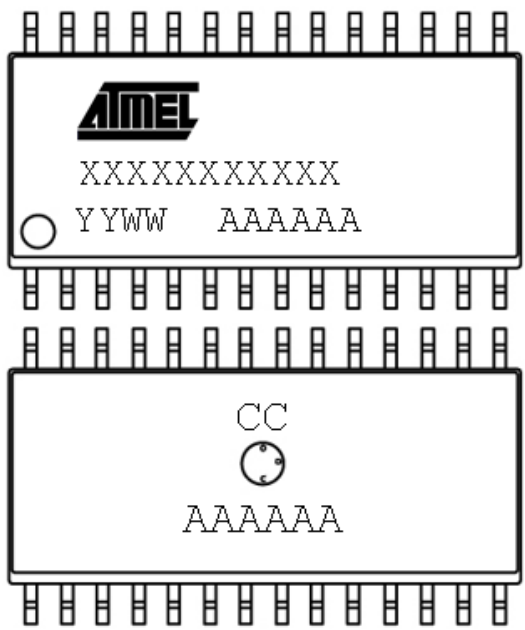
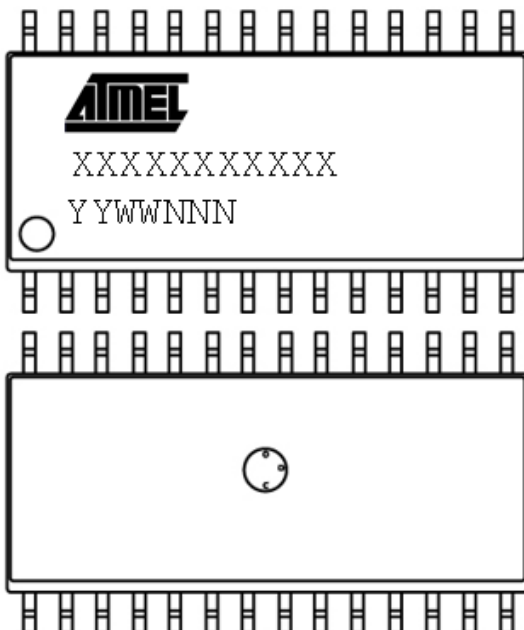
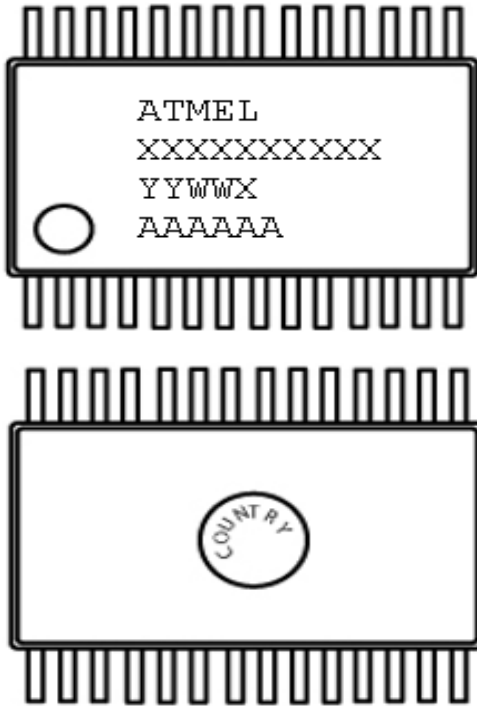
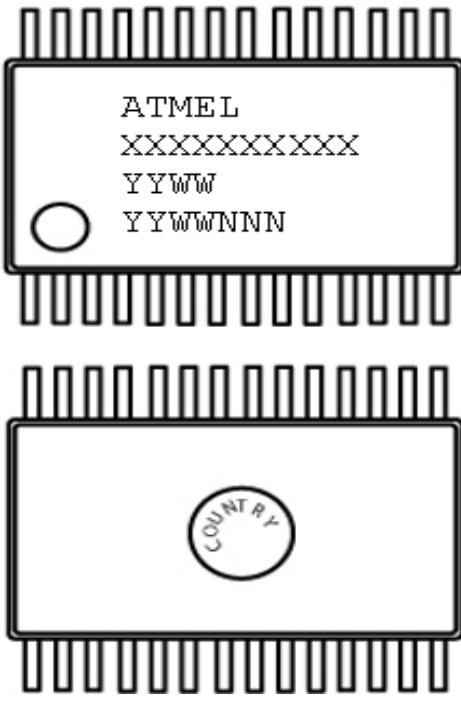
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/ Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			Top Mark Line 1= Device Name Line 2 = Lot Traceability Line 3 = Device Information, Date Code ● = Pin 1 indicator		Top Mark Line 1= Device Name Line 2 = Lot Traceability Line 3 = Device Information, Date Code, Country of Origin ● = Pin 1 indicator
			Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability O = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability O = Pin 1 indicator
			Top Mark Line 1= Device Name Line 2 = Date Code, Lot Traceability O = Pin 1 indicator Bottom Mark Line 1 = Country Code Country of Origin in injector mold Line 2 = Lot Traceability		Top Mark Line 1= Device Name Line 2 = Lot Traceability O = Pin 1 indicator Bottom Mark No bottom mark Country of Origin in injector mold

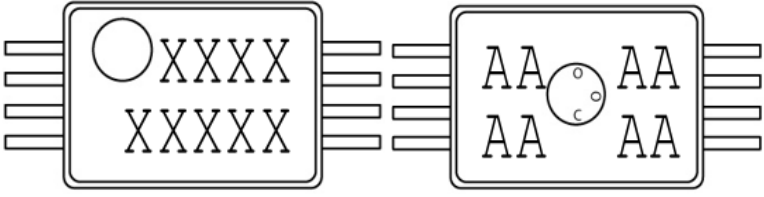
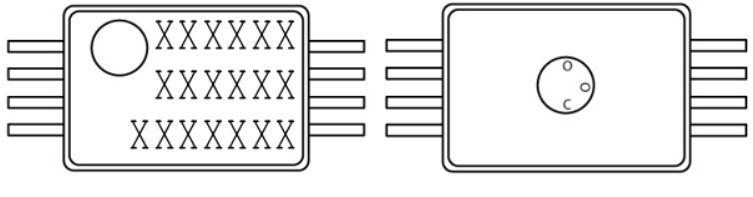
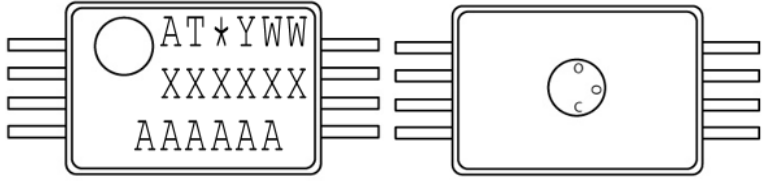
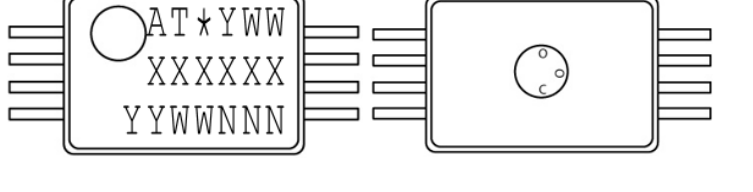
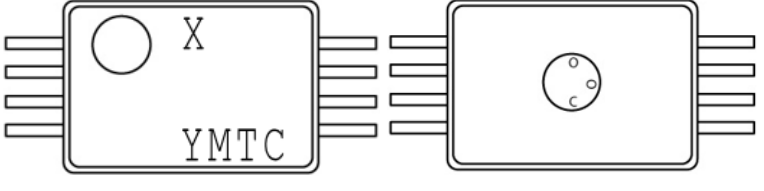
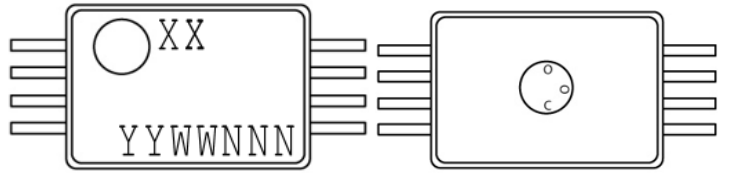
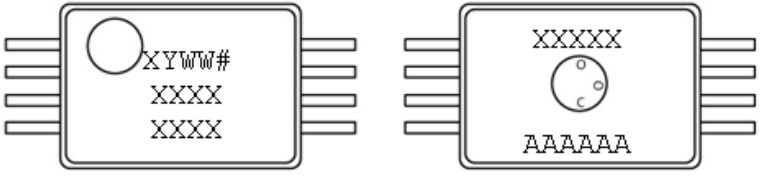
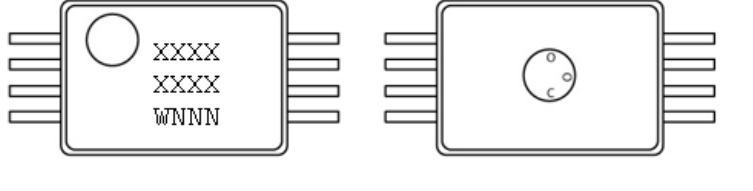
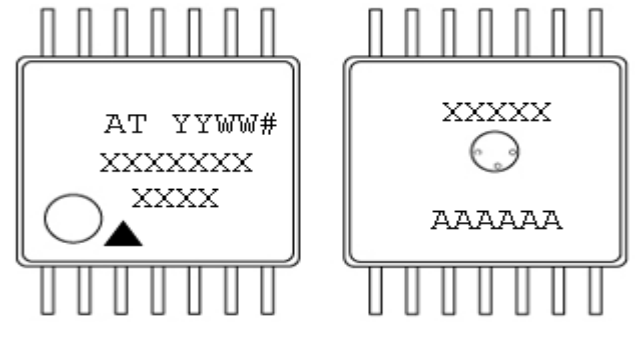
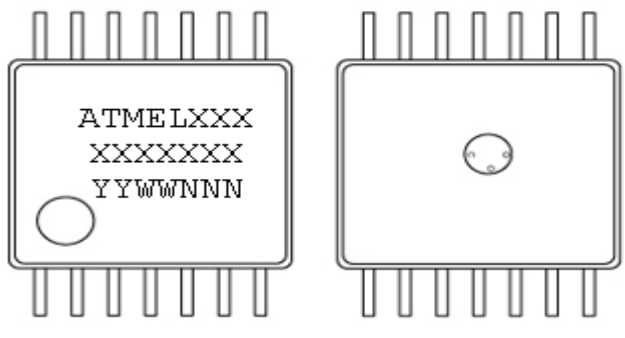
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
28	SSOP 208 MIL		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Date Code, Subcon Code Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Date Code Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>



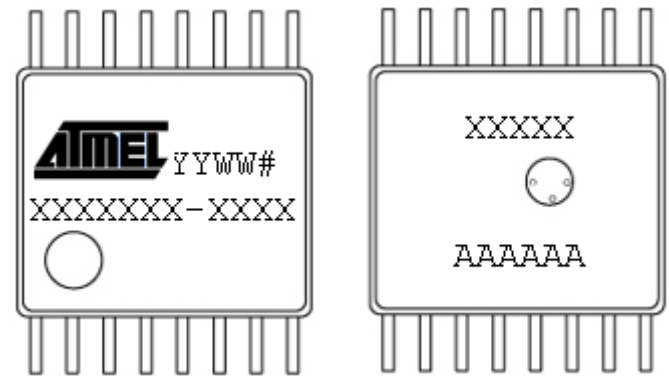
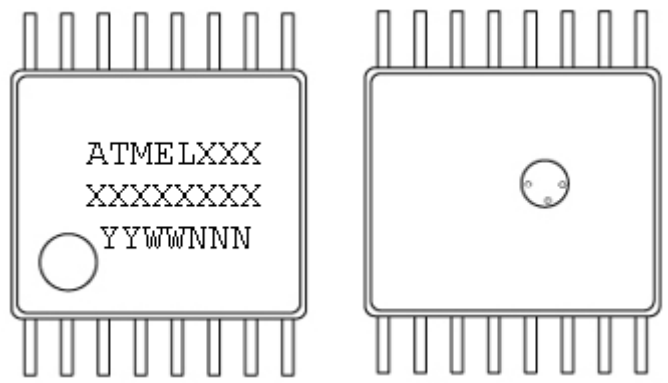
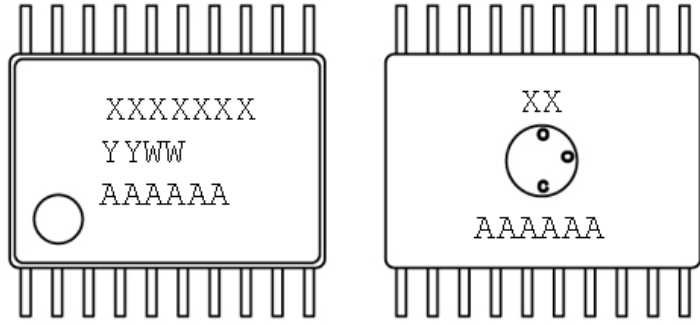
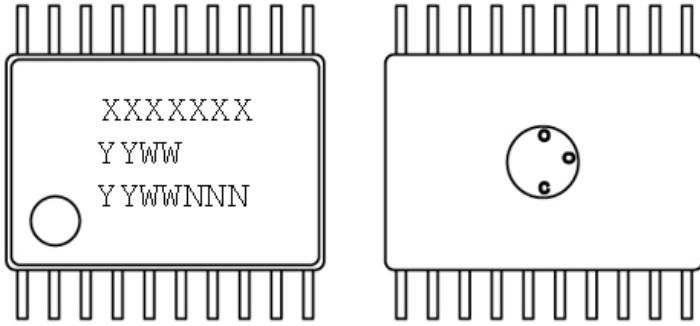
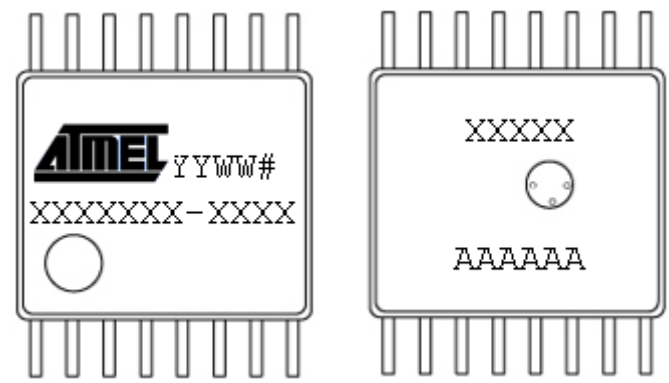
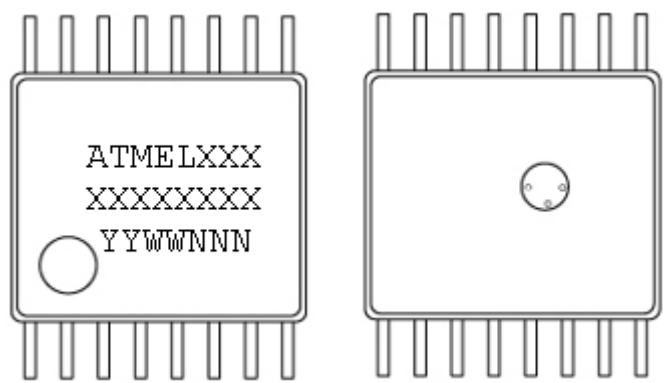
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

		Atmel®		MICROCHIP	
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
8	TSSOP		<p>Top Mark Line 1= Class Code, Date Code Line 2 = Truncation Code O = Pin 1 indicator</p> <p>OR Line 1= Truncation Code Line 2 = Device Name</p> <p>Bottom Mark Lot Traceability Country of Origin in injector mold</p>		<p>Top Mark Line 1= AT, Class Code, Date Code Line 2 = Truncation Code, Country of Origin Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
			<p>Top Mark Line 1= AT, Class Code, Date Code Line 2 = Truncation Code Line 3 = Lot Traceability</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>		<p>Top Mark Line 1= AT, Class Code, Date Code Line 2 = Truncation Code, Country of Origin Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
			<p>Top Mark Line 1= Subcon Code Line 2 = Lot Traceability</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>		<p>Top Mark Line 1= Country of Origin Line 2 = Lot Traceability</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
			<p>Top Mark Line 1= A, Date Code, MRL (if shown in ABI) Line 2 = Device Name (shortened) Line 3 = Device Information</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>		<p>Top Mark Line 1= Device Information Line 2 =Device Name (shortened) Line 3 = Lot Traceability</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
14	TSSOP		<p>Top Mark Line 1 = AT, Date Code, MRL (if shown in ABI) Line 2 = Device Name Line 3 = Device Information O = Pin 1 indicator ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1 = ATMEL, Device Information Line 2 = Device Name Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>



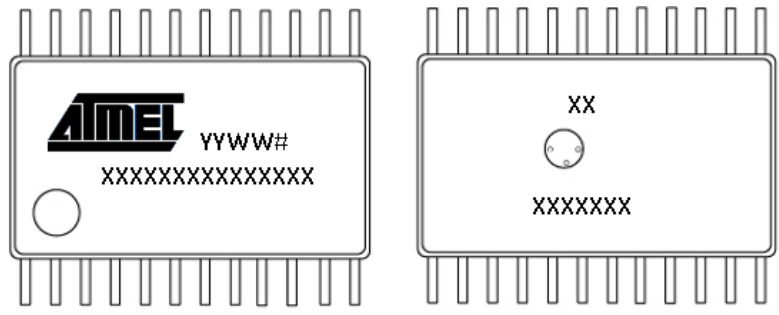
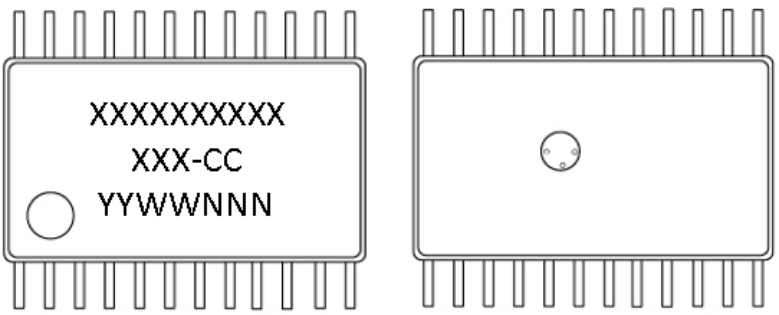
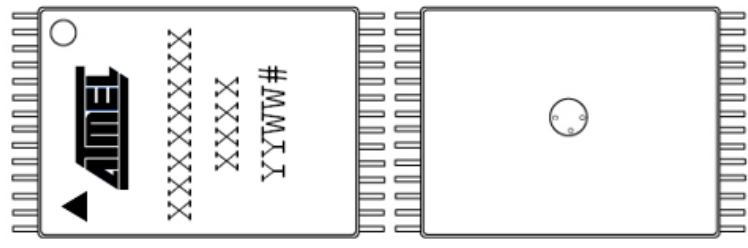
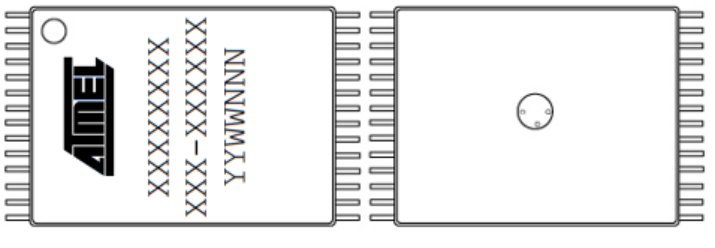
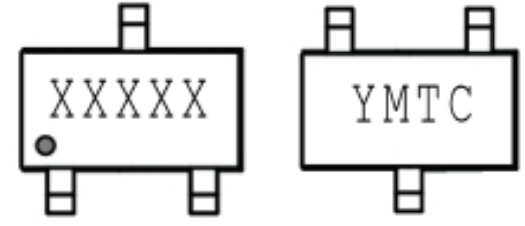
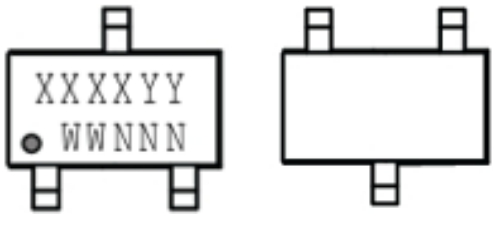
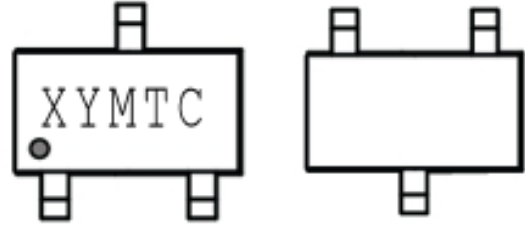
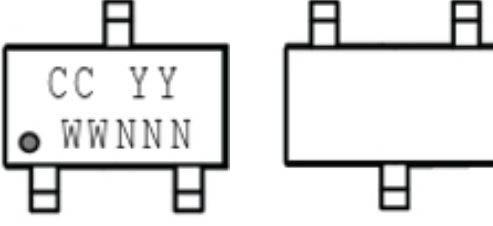
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
16	TSSOP		<p>Top Mark Line 1 = ATMEL Logo, Date Code, MRL (if shown in ABI) Line 2 = Device Name, Device Information O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1 = ATMEL, Device Information Line 2 = Device Name Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
20	TSSOP		<p>Top Mark Line 1 = Device Name Line 2 = Date Code Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1 = Device Name Line 2 = Date Code Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Country of Origin in injector mold</p>
			<p>Top Mark Line 1 = ATMEL Logo, Date Code, MRL (if shown in ABI) Line 2 = Device Name, Device Information O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1 = ATMEL, Device Information Line 2 = Device Name Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>

PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
20 / 24	TSSOP		<p>Top Mark Line 1 = ATMEL Logo, Date Code, MRL (if shown in ABI) Line 2 = Device Name, Device Information O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1 = Device Name Line 2 = Device Information, Country Code Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
28 / 32	TSOP		<p>Top Mark Line 1 = ATMEL Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, MRL (if shown in ABI) O = Pin 1 indicator ▲ = Pin 1 location</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>		<p>Top Mark Line 1 = ATMEL Logo Line 2 = Device Name Line 3 = Device Information, Die ID, Revision Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
	SOT-23		<p>Top Mark Line 1= Truncation Code, Class Code ● = Pin 1 indicator</p> <p>Bottom Mark Lot Traceability (Year, Month, Trace Code)</p>		<p>Top Mark Line 1= Truncation Code, Subcon Code, Year Line 2 = Lot Traceability ● = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
	SOT-23		<p>Top Mark Line 1= Lot Traceability (Year, Month, Trace Code)</p> <p>Bottom Mark No bottom mark</p>		<p>Top Mark Line 1= Country Code, Year Line 2 = Lot Traceability</p> <p>Bottom Mark No bottom mark</p>



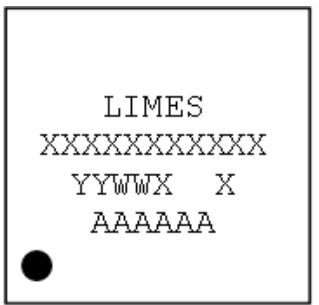
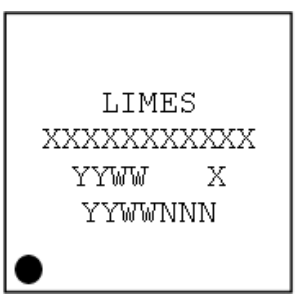
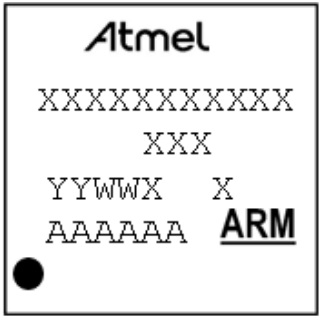
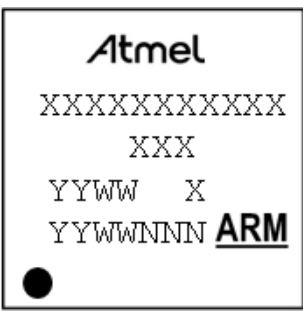
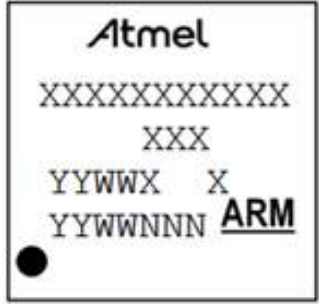
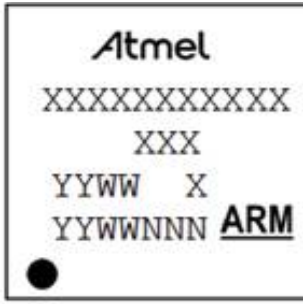

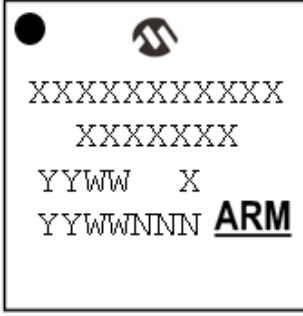
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

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		Atmel®		MICROCHIP	
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
5	SOT-23		<p>Top Mark Line 1= Truncation Code, Device Information ● = Pin 1 indicator</p> <p>Bottom Mark Lot Traceability (Year, Month, Trace Code)</p>		<p>Top Mark Line 1= Truncation Code, Device Information, Year Line 2 = Lot Traceability (Workweek, Lot Number) ● = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
			<p>Top Mark Line 1= Truncation Code, Device Information Line 2 = Lot Traceability (Year, Month, Trace Code)</p> <p>Bottom Mark No bottom mark</p>		<p>Top Mark Line 1= Truncation Code, Lot Traceability ● = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
6	SOT-23		<p>Top Mark Line 1= Device Name (shortened) o = Pin 1 indicator</p> <p>Bottom Mark Lot Traceability</p>		<p>Top Mark Line 1= Device Name (shortened) Line 2 = Lot Traceability o = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
8	VFBGA		<p>Top Mark Line 1= Truncation Code, Device Information Line 2 = Lot Traceability ● = Pin 1 indicator</p>		<p>Top Mark Line 1= Truncation Code, Device Information, Year Line 2 = Lot Traceability ● = Pin 1 indicator</p>
49	VFBGA 5X5 MM		<p>Top Mark Line 1= AT, Date Code, Die Revision, MRL (if shown in ABI) Line 2 = Device Name Line 3 = Device Information Line 4 = Lot Traceability ● = Pin 1 indicator</p>		<p>Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Die ID, Revision Line 5 = Lot Traceability ● = Pin 1 indicator</p>



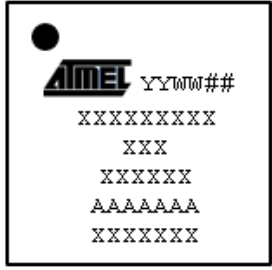
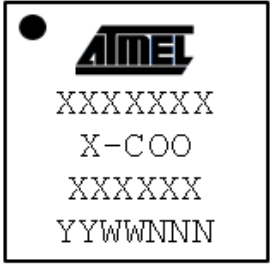
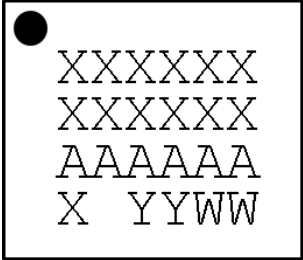
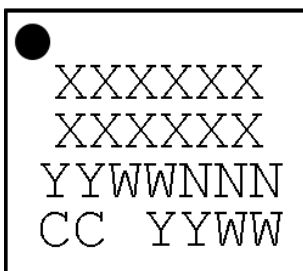
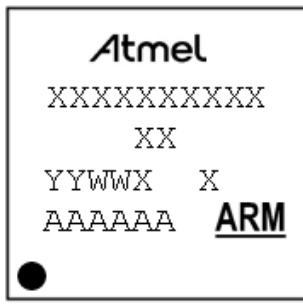
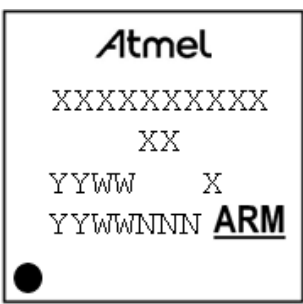
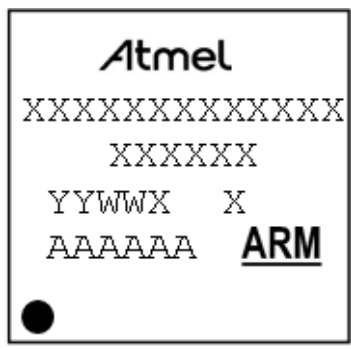
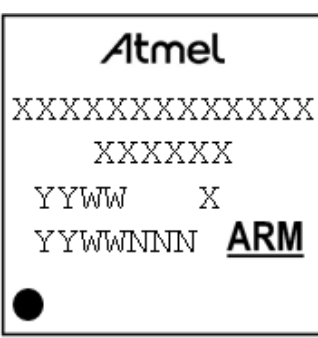
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

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Lead/Pin/ Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
96	VFBGA 6X6 MM		Top Mark Line 1= LINES Line 2 = Device Name Line 3 = Date Code, Subcon Code, Design Revision Line 4 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= LINES Line 2 = Device Name Line 3 = Date Code, Design Revision Line 4 = Lot Traceability ● = Pin 1 indicator
100	VFBGA 7X7 MM		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
			Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
			Top Mark Line 1= Microchip Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Microchip Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator



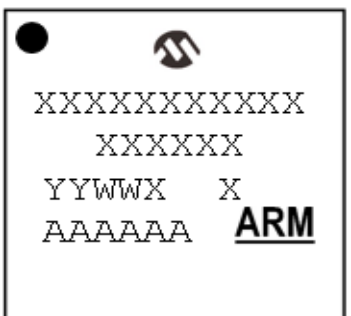
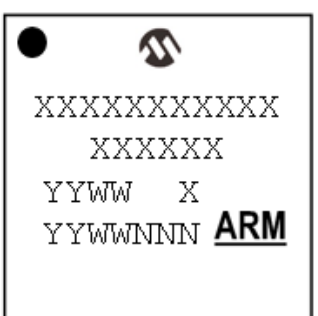
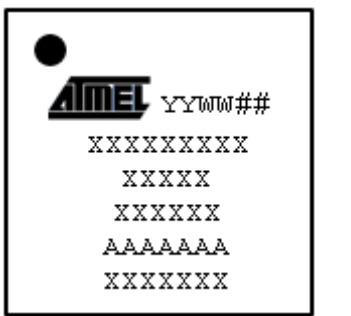

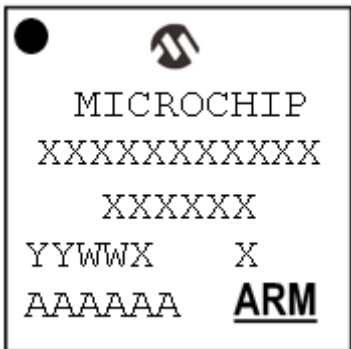
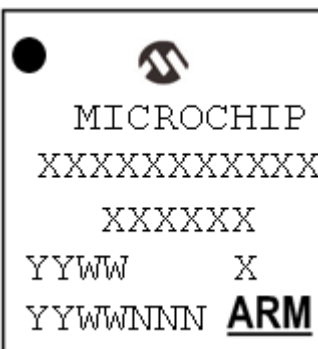
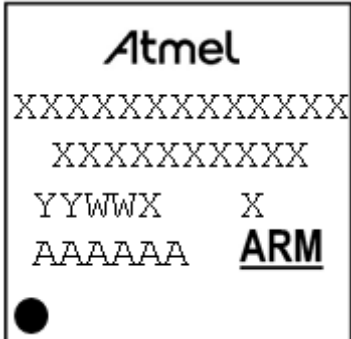
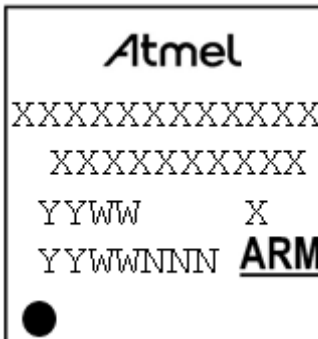
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/ Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			Top Mark Line 1= Atmel Logo, Date Code, MRL (if shown in ABI) Line 2 = Device Name Line 3 = Device Information Line 3 = Country of Assembly Line 4 = Lot Traceability Line 5 = Die ID, Revision ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Die ID, Revision Line 5 = Lot Traceability ● = Pin 1 indicator
40	TFBGA 4X4 MM		Top Mark Line 1= Device Name Line 2 = Device Information Line 3 = Lot Traceability Line 4 = Subcon Code, Date Code ● = Pin 1 indicator		Top Mark Line 1= Device Name Line 2 = Lot Traceability Line 3 = Country of Origin, Date Code ● = Pin 1 indicator
256	TFBGA 8X8 MM		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
100	TFBGA 9X9 MM		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator



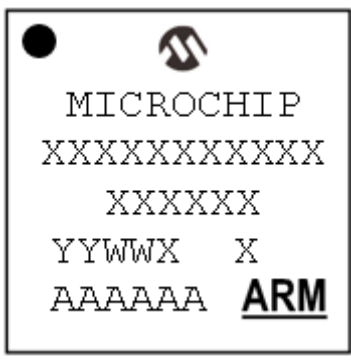
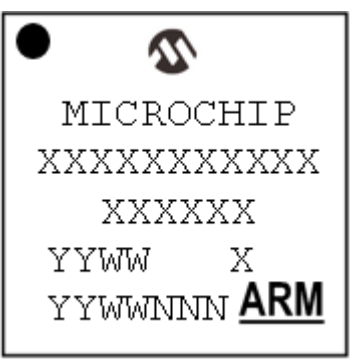
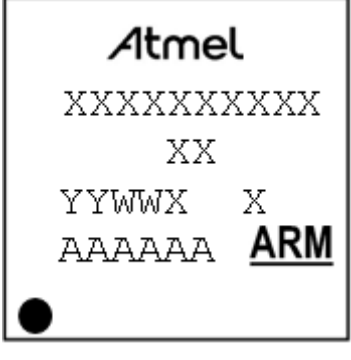
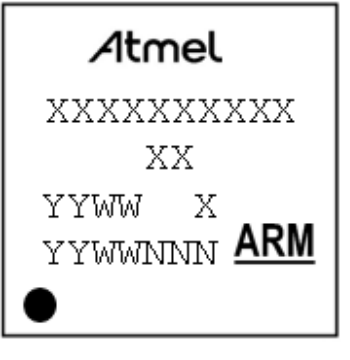
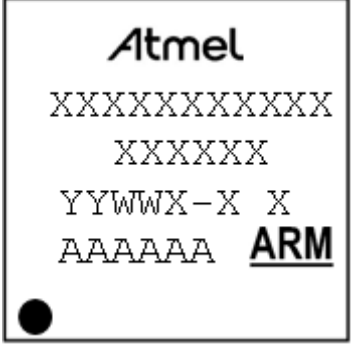
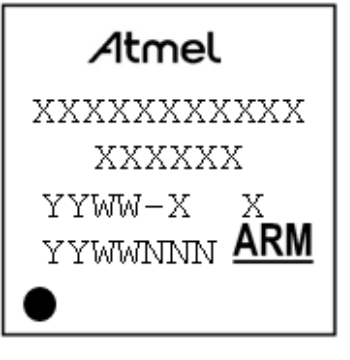
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			Top Mark Line 1= Microchip Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Microchip Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
			Top Mark Line 1= Atmel Logo, Date Code, MRL (if shown in ABI) Line 2 = Device Name Line 3 = Device Information Line 3 = Country of Assembly Line 4 = Lot Traceability Line 5 = Die ID, Revision ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Die ID, Revision Line 5 = Lot Traceability ● = Pin 1 indicator
144	TFBGA 10X10 MM		Top Mark Line 1= Microchip Logo Line 2 = Microchip Line 3 = Device Name Line 4 = Device Information Line 5 = Date Code, Subcon Code, Design Revision Line 6 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Microchip Logo Line 2 = Microchip Line 3 = Device Name Line 4 = Device Information Line 5 = Date Code, Design Revision Line 6 = Lot Traceability, ARM ● = Pin 1 indicator
			Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator



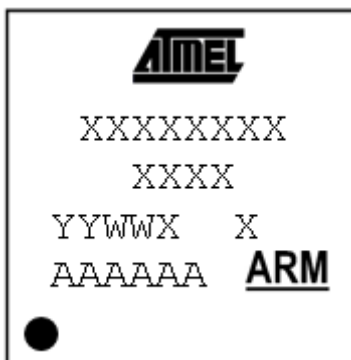
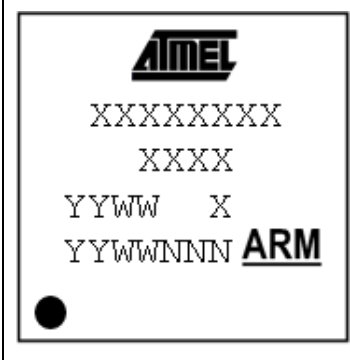
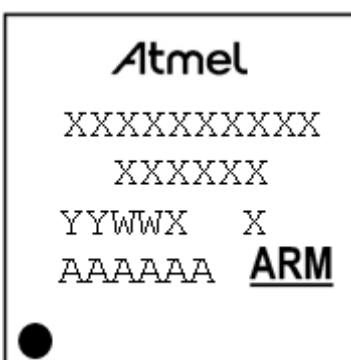
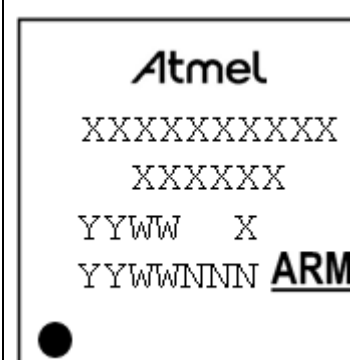
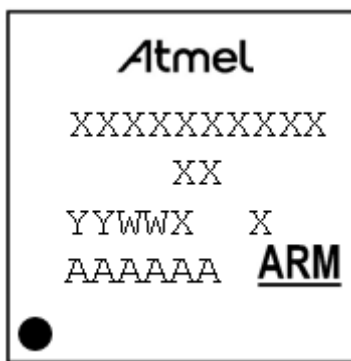
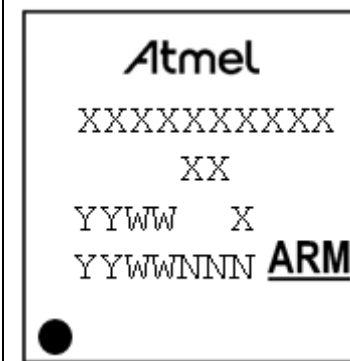
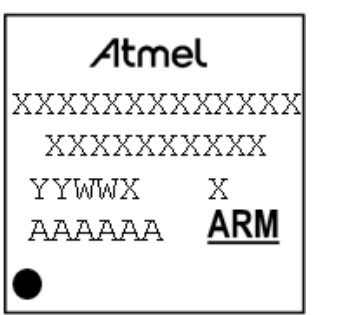
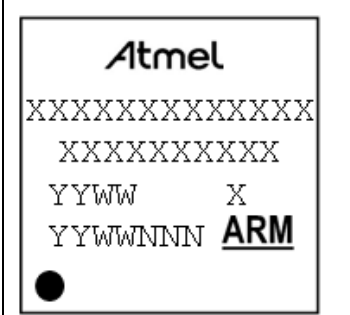
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/ Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
176	TFBGA 11X11 MM		Top Mark Line 1= Microchip Logo Line 2 = Microchip Line 3 = Device Name Line 4 = Device Information Line 5 = Date Code, Subcon Code, Design Revision Line 6 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Microchip Logo Line 2 = Microchip Line 3 = Device Name Line 4 = Device Information Line 5 = Date Code, Design Revision Line 6 = Lot Traceability, ARM ● = Pin 1 indicator
196	TFBGA 11X11 MM		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
			Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator



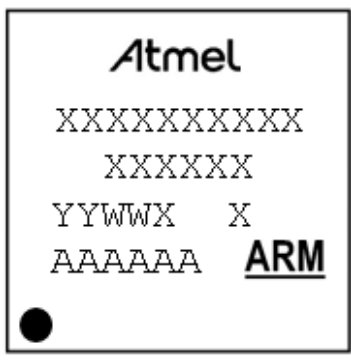
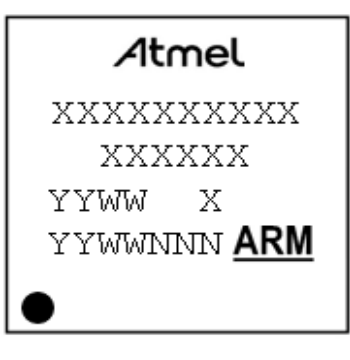
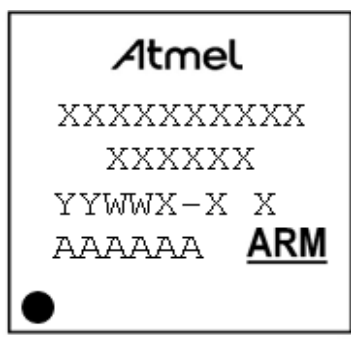
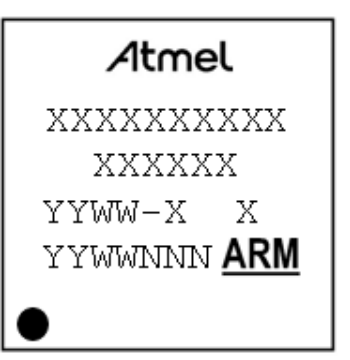
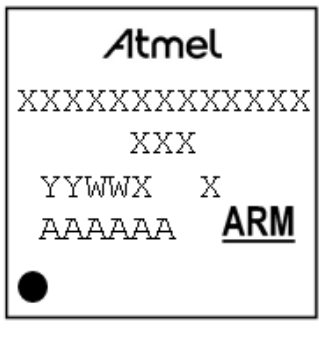
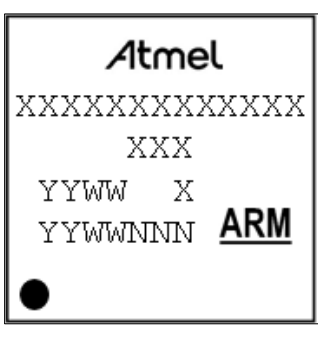
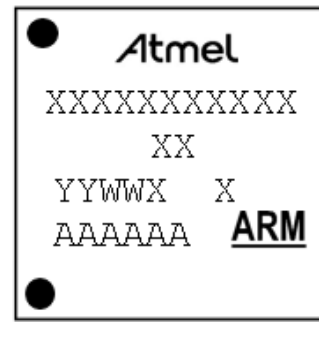
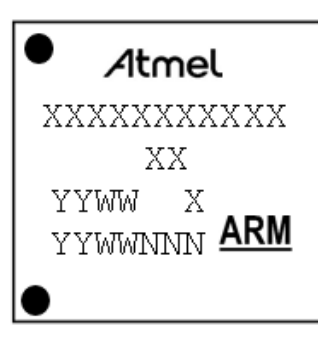
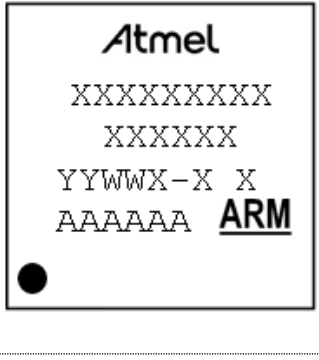
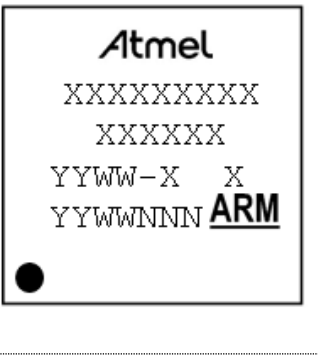
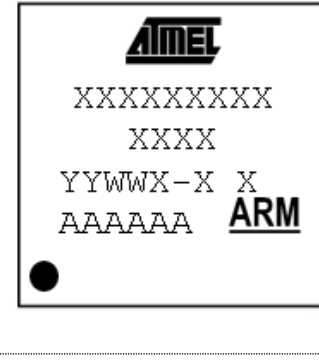
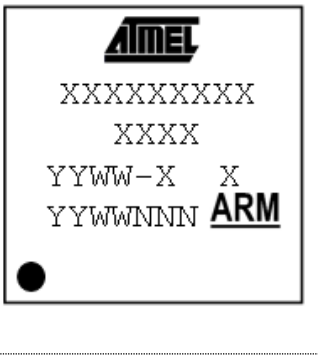
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/ Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
144	TFBGA 12X12 MM		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
324	TFBGA 12X12 MM		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
361	TFBGA 16X16 MM		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
144	LFBGA 10X10 MM		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator



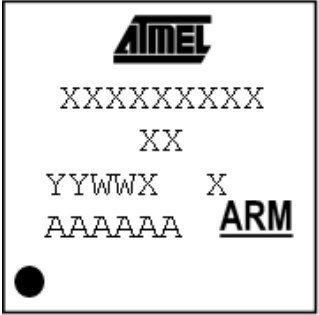
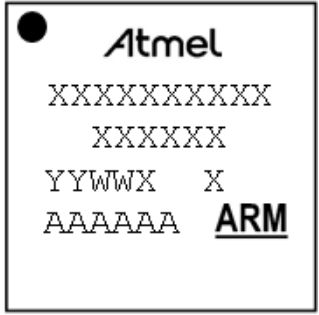
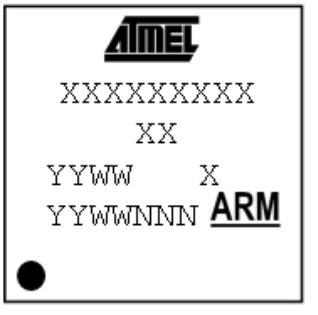
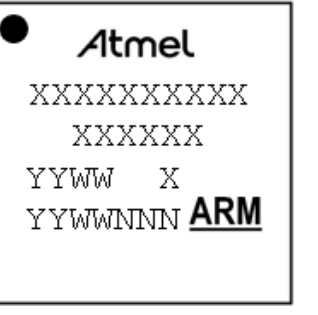
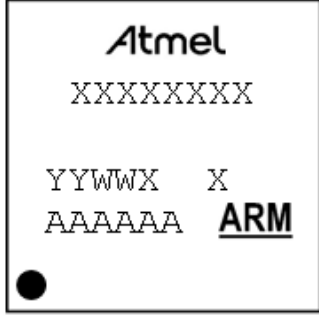
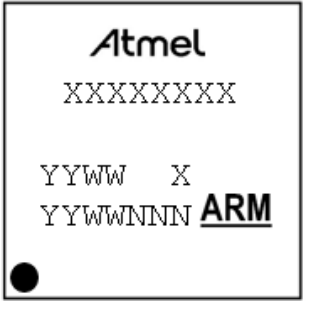
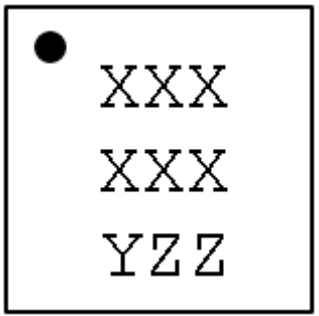
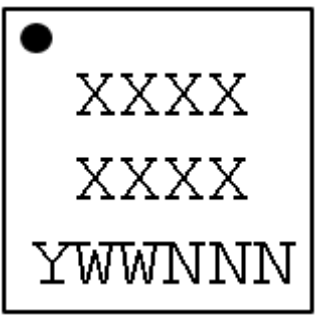
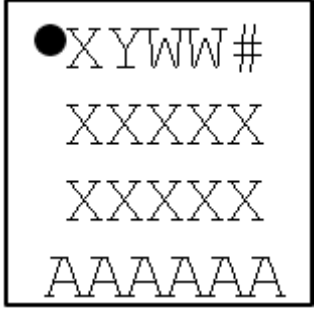
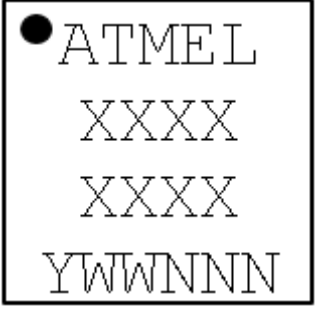
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/ Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
289	LFBGA 14X14 MM		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
			Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
217	LFBGA 15X15 MM		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
		OR 		OR 	
256	LFBGA 15X15 MM		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
		OR 		OR 	




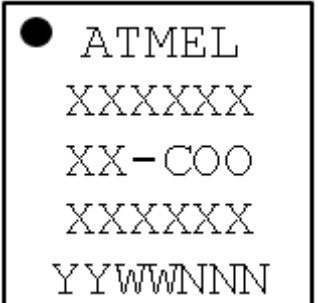
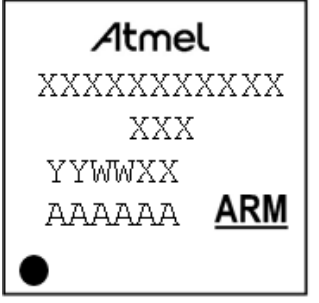
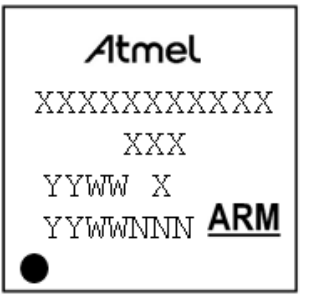

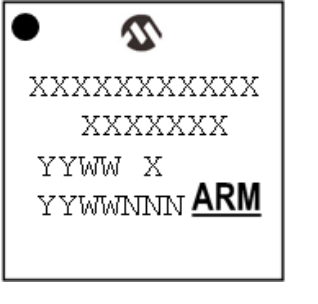
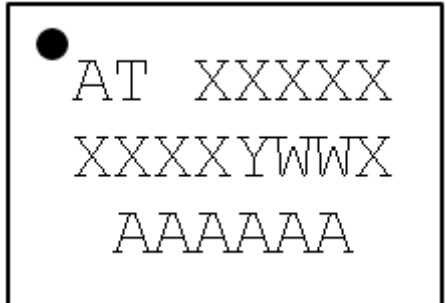
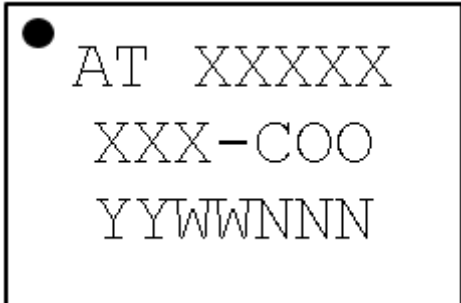
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

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Lead/Pin/ Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
		  <p style="text-align: center;">OR</p>	<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator</p>	  <p style="text-align: center;">OR</p>	<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator</p>
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Date Code, Subcon Code, Design Revision Line 4 = Lot Traceability, ARM ● = Pin 1 indicator</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Date Code, Design Revision Line 4 = Lot Traceability, ARM ● = Pin 1 indicator</p>
15	UFBGA 3X3 MM		<p>Top Mark Line 1= Device Name (shortened) Line 2 = Device Type Code Line 3 = Lot Traceability ● = Pin 1 indicator</p>		<p>Top Mark Line 1= Device Name (shortened) Line 2 = Device Information, Country of Origin Line 3 = Lot Traceability ● = Pin 1 indicator</p>
32	UFBGA 4X4 MM		<p>Top Mark Line 1= A, Date Code, MRL (if shown in ABI) Line 2 = Device Name Line 3 = Device Information, Die Revision Line 4 = Lot Traceability ● = Pin 1 indicator</p>		<p>Top Mark Line 1= Atmel Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Lot Traceability ● = Pin 1 indicator</p>

PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

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Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
49	UFBGA 5X5 MM		Top Mark Line 1= AT, Date Code, Die Revision, MRL (if shown in ABI) Line 2 = Device Name Line 3 = Device Information Line 4 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Die ID, Revision Line 5 = Lot Traceability ● = Pin 1 indicator
144	UFBGA 6X6 MM		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
			Top Mark Line 1= Microchip Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Microchip Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
36	WLGA 6.5X3.5 MM		Top Mark Line 1= AT, Device Name (shortened) Line 2 = Device Information, Date Code, Die Revision Line 3 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= AT, Device Name (shortened) Line 2 = Device Information, Country of Origin Line 3 = Lot Traceability ● = Pin 1 indicator



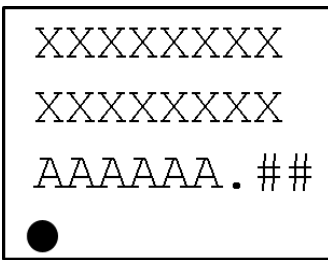
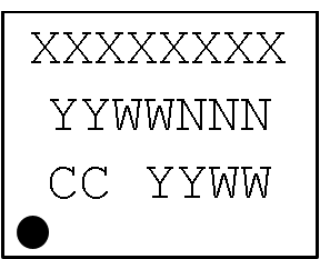
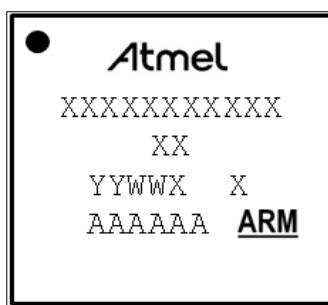
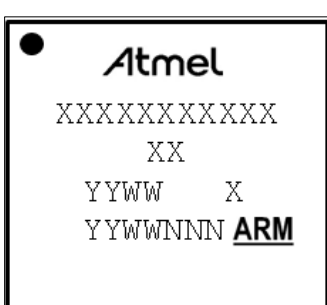
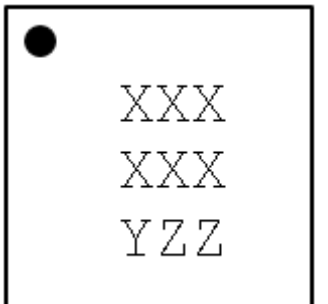
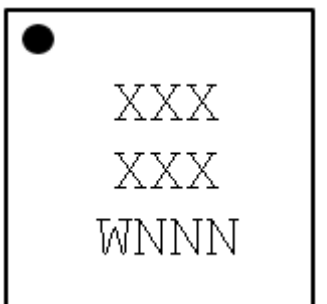
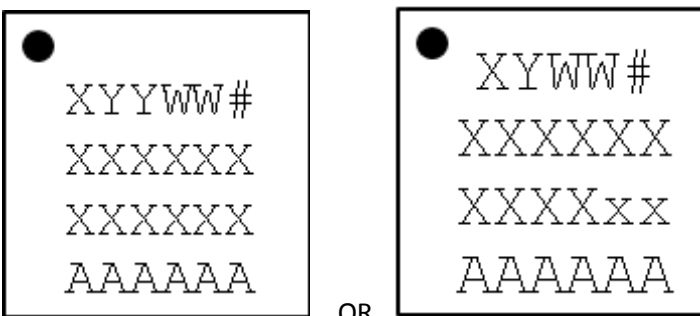
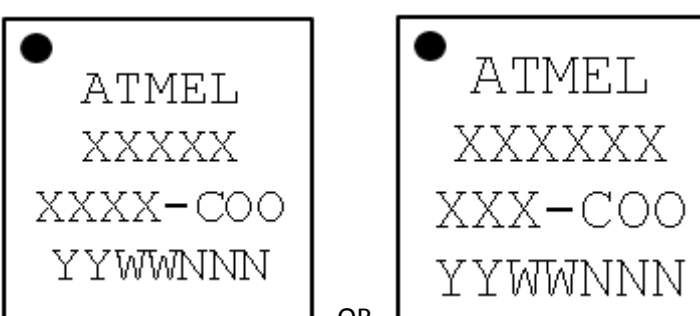
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

Atmel®			MICROCHIP		
Lead/Pin/ Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
4	WLCSP		Top Mark Line 1= Subcon Code Line 2 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= Workweek Line 2 = Lot Traceability ● = Pin 1 indicator
8	WLCSP		Top Mark Line 1= Device Information Line 2 = Truncation Code Line 3 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= Country of Origin Line 2 = Truncation Code Line 3 = Lot Traceability ● = Pin 1 indicator
			Top Mark Line 1= ATML, Class Code, Date Code Line 2 = Truncation Code Line 3 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= ATML, Class Code, Date Code Line 2 = Truncation Code, Country of Origin Line 3 = Lot Traceability ● = Pin 1 indicator
31	WLCSP		Top Mark Line 1= Device Name Line 2 = Device Information Line 3 = Lot Traceability Line 4 = Date Code ● = Pin 1 indicator		Top Mark Line 1= Device Name Line 2 = Device Information Line 3 = Lot Traceability Line 4 = Country of Origin, Date Code ● = Pin 1 indicator



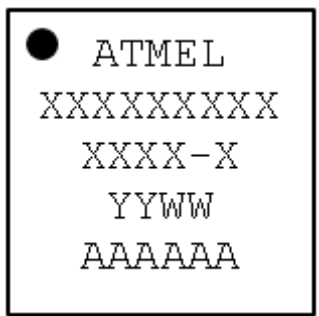
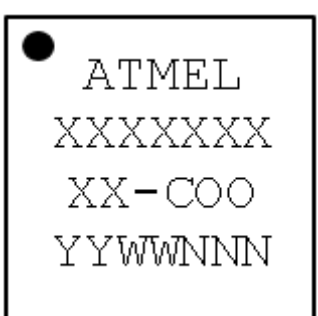
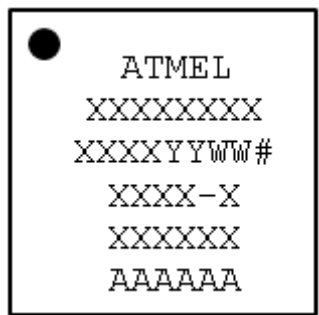

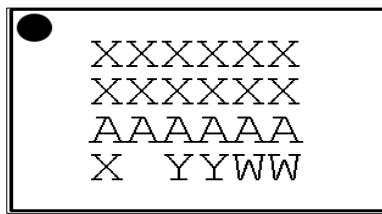
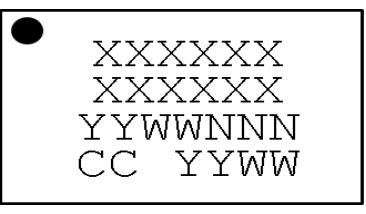
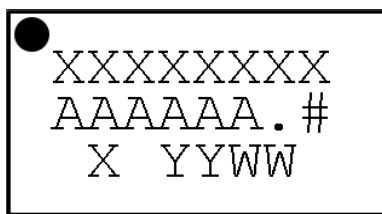
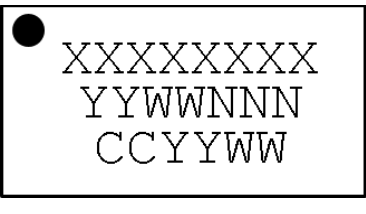
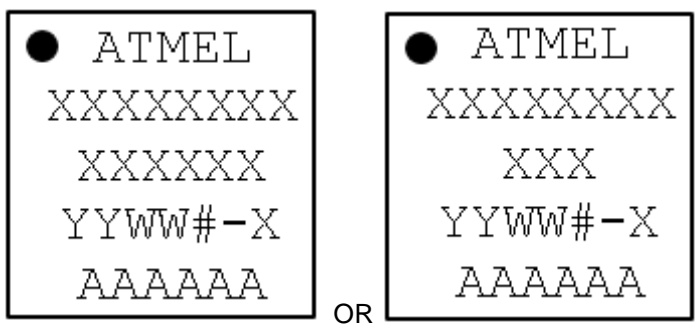
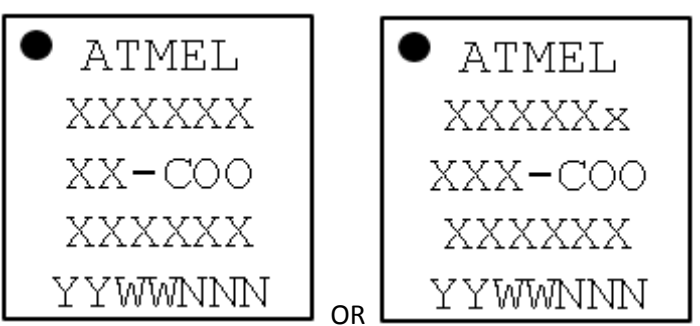
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/ Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
54	WLCSP		Top Mark Line 1= Device Name Line 2 = Date Code Line 3 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= Device Name Line 2 = Lot Traceability Line 3 = Country of Origin, Date Code ● = Pin 1 indicator
49 / 64	WLCSP		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
20	VQFN 3X3 MM		Top Mark Line 1= Device Name (shortened) Line 2 = Device type code / Class code / Die Revision / Assembly location code Line 3 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= Device Name (shortened) Line 2 = Device type code / Class code, Die Revision / Assembly location code Line 3 = Lot Traceability ● = Pin 1 indicator Bottom Mark No bottom mark
24 / 28	VQFN 4X4 MM		Top Mark Line 1= A, Date Code, MRL (if shown in ABI) Line 2 = Device Name Line 3 = Device Information Line 4 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Lot Traceability ● = Pin 1 indicator



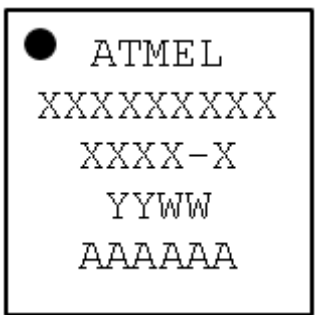
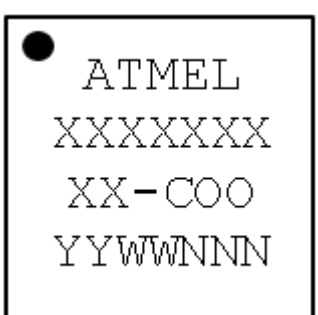
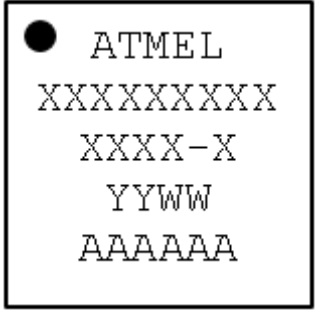
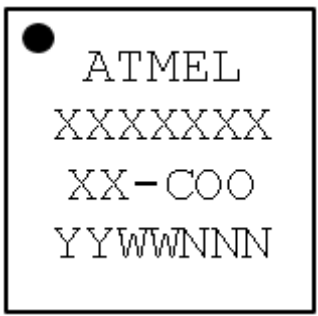
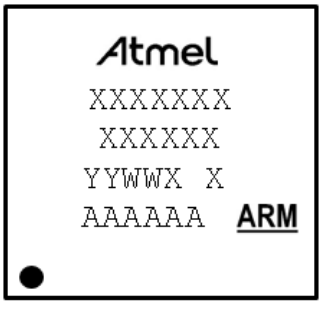
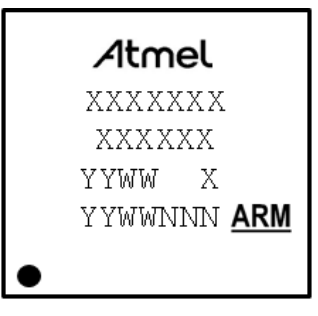
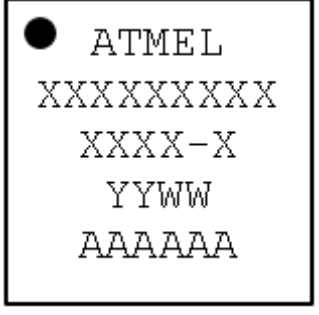
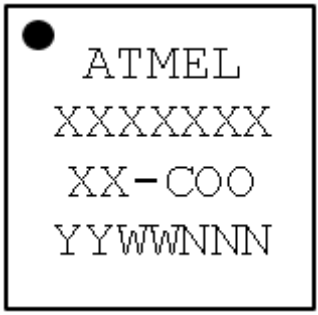
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/ Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Class, Die Revision, Assembly Location Code Line 4 = Date Code Line 5 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Class, Die Revision, Country of Origin Line 4 = Lot Traceability ● = Pin 1 indicator
			Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Device Information, Date Code, MRL (if shown in ABI) Line 4 = Class, Assembly Location Code Line 5 = Die ID, Revision Line 6 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Die ID, Revision Line 5 = Lot Traceability ● = Pin 1 indicator
24 / 32 / 40 / 44 / 48	VQFN 5X5 MM		Top Mark Line 1= Device Name Line 2 = Device Information Line 3 = Lot Traceability Line 4 = Subcon Code, Date Code ● = Pin 1 indicator		Top Mark Line 1= Device Name Line 2 = Device Information Line 3 = Lot Traceability Line 4 = Country of Origin, Date Code ● = Pin 1 indicator
			Top Mark Line 1= Device Name Line 2 = Lot Traceability Line 3 = Date Code		Top Mark Line 1= Device Name Line 2 = Lot Traceability Line 3 = Country of Origin, Date Code
			Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, MRL (if shown in ABI) Line 5 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Die ID, Revision Line 5 = Lot Traceability ● = Pin 1 indicator



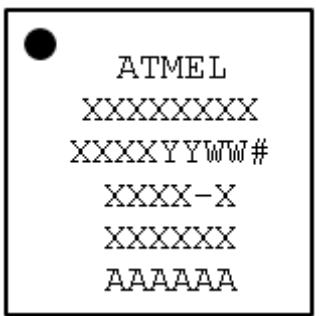
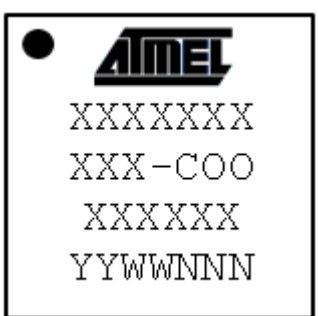
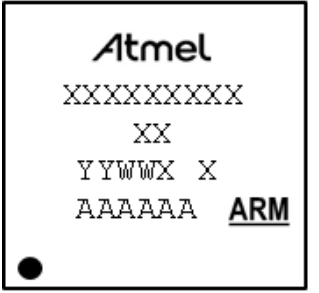
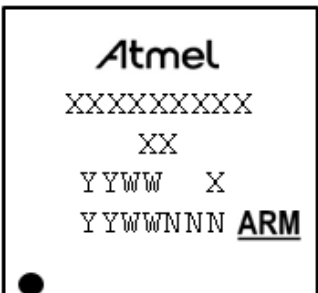
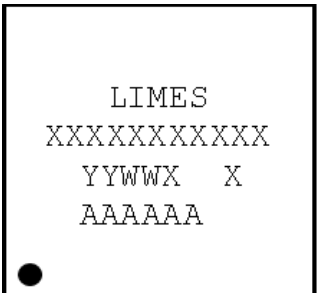
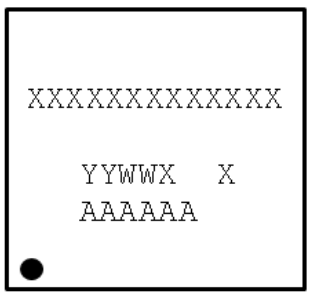
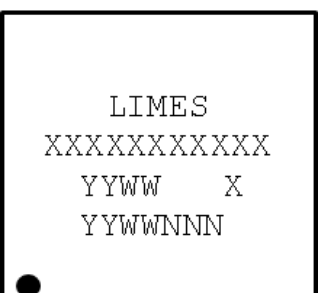
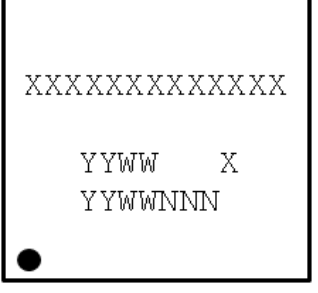
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/ Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Device Information, MRL (if shown in ABI) Line 4 = Date Code Line 5 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Lot Traceability ● = Pin 1 indicator
40	VQFN 6X6 MM		Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code Line 5 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Lot Traceability ● = Pin 1 indicator
48	VQFN 6X6 MM		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
			Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code Line 5 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Lot Traceability ● = Pin 1 indicator



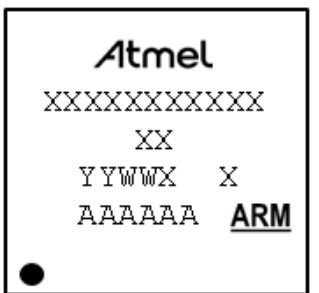
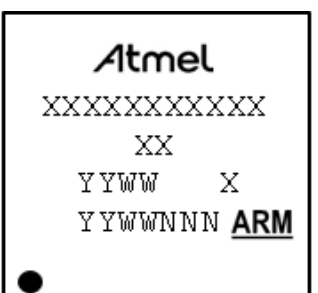
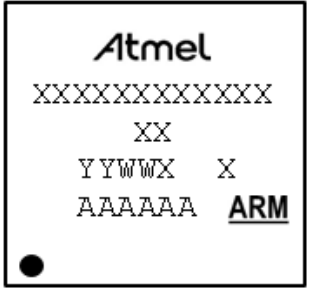
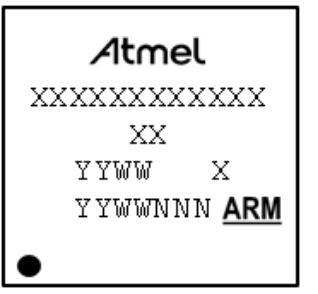
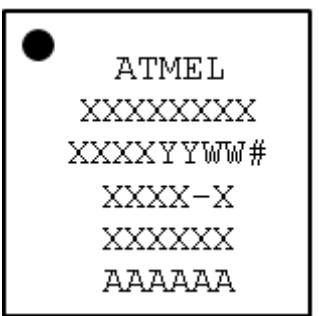
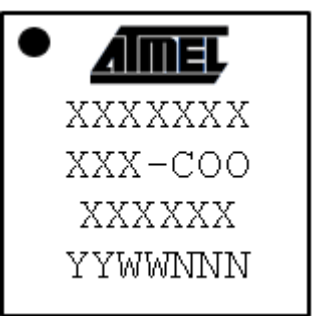
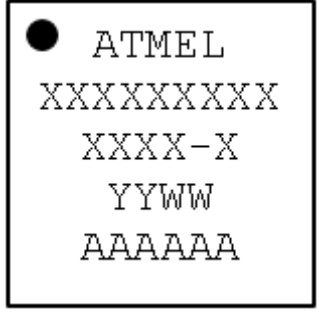
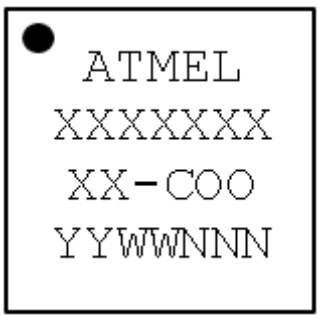
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
44 / 64	VQFN 7X7 MM		Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Device Information, Date Code, MRL (if shown in ABI) Line 4 = Country of Assembly, Assembly Location Code Line 5 = Die ID, Revision Line 6 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Die ID, Revision Line 5 = Lot Traceability ● = Pin 1 indicator Bottom Mark No bottom mark
48	VQFN 7X7 MM		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
64	VQFN 7.5X7.5 MM	 	Top Mark Line 1= LINES Line 2 = Device Name Line 3 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability ● = Pin 1 indicator Top Mark Line 1= Device Name Line 2 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability ● = Pin 1 indicator	 	Top Mark Line 1= LINES Line 2 = Device Name Line 3 = Date Code, Design Revision Line 5 = Lot Traceability ● = Pin 1 indicator Top Mark Line 1= Device Name Line 2 = Date Code, Design Revision Line 5 = Lot Traceability ● = Pin 1 indicator



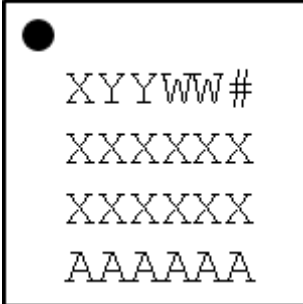
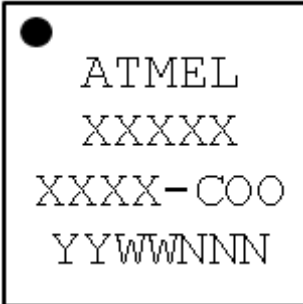
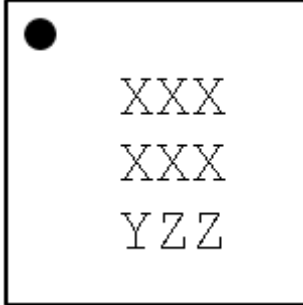
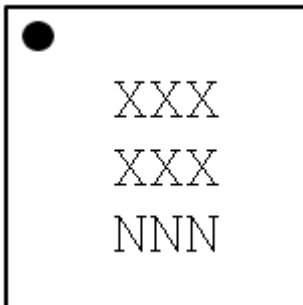
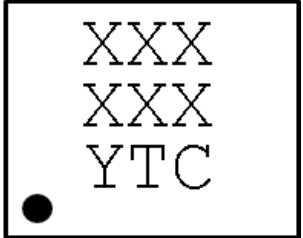
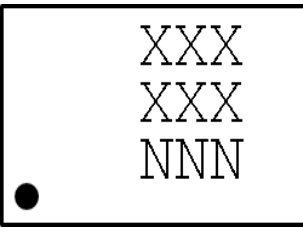
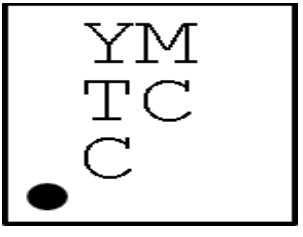
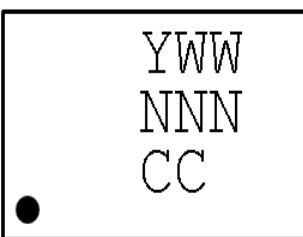
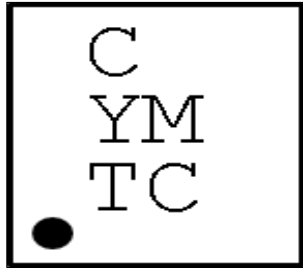
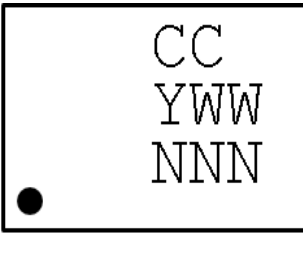
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
64	VQFN 9X9 MM		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM ● = Pin 1 indicator
			Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Device Information, Date Code, MRL (if shown in ABI) Line 4 = Country of Assembly, Assembly Location Code Line 5 = Die ID, Revision Line 6 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Die ID, Revision Line 5 = Lot Traceability ● = Pin 1 indicator Bottom Mark No bottom mark
			Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Class, Die Revision, Assembly Location Code Line 4 = Date Code Line 5 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= ATMEL Line 2 = Device Name Line 3 = Class, Die Revision, Country of Origin Line 4 = Lot Traceability ● = Pin 1 indicator

PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
20	WQFN 4X4 MM		Top Mark Line 1= A, Date Code, MRL (if shown in ABI) Line 2 = Device Name (shortened) Line 3 = Device Information Line 4 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= ATMEL Line 2 = Device Name (shortened) Line 3 = Device Information, Country of Origin Line 4 = Lot Traceability ● = Pin 1 indicator
8	UDQFN 2X2 MM		Top Mark Line 1= Device Name (shortened) Line 2 = Device type code / Class code / Die Revision / Assembly location code Line 3 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= Device Name (shortened) Line 2 = Device type code / Class code, Die Revision / Assembly location code Line 3 = Lot Traceability ● = Pin 1 indicator
8	UDFN 2X3 MM		Top Mark Line 1= Truncation Code Line 2 = Device Information Line 3 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= Truncation Code Line 2 = Device Information Line 3 = Lot Traceability ● = Pin 1 indicator
			Top Mark Line 1= Year, Month Line 2 = Lot Traceability Line 3 = Subcon Code		Top Mark Line 1= Date Code Line 2 = Lot Traceability Line 3 = Country Code
8	XDFN / UDFN		Top Mark Line 1= Subcon Code Line 2 = Year, Month Line 3 = Lot Traceability		Top Mark Line 1= Country of Origin Line 2 = Date Code Line 3 = Lot Traceability



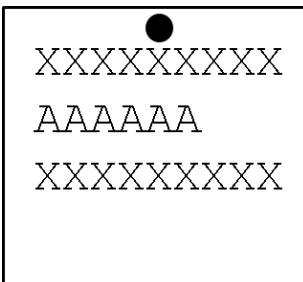
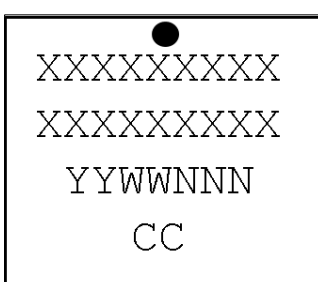
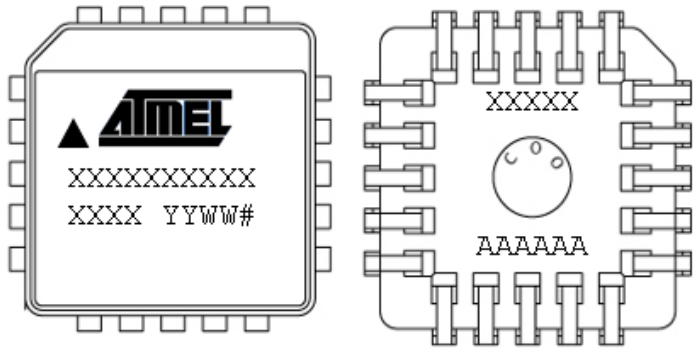
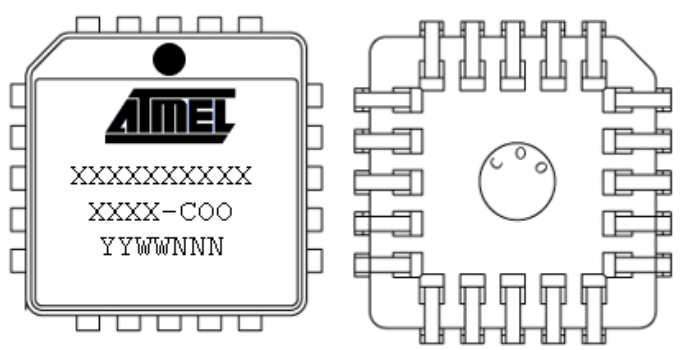
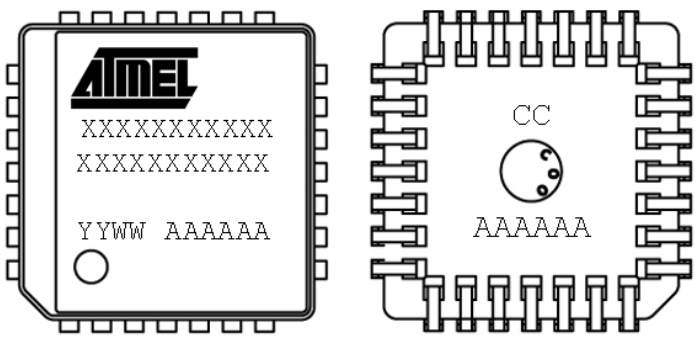
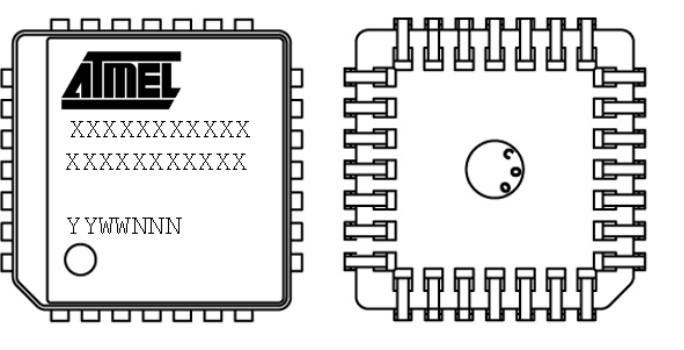
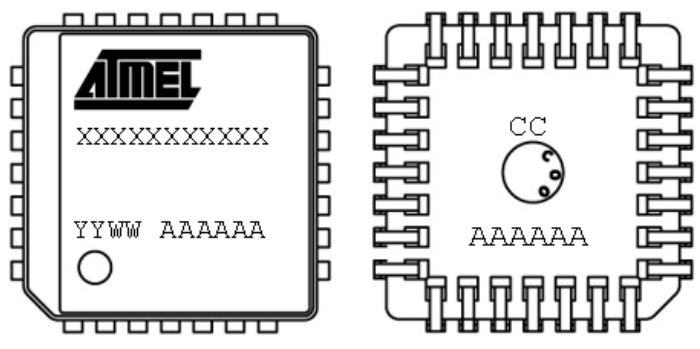
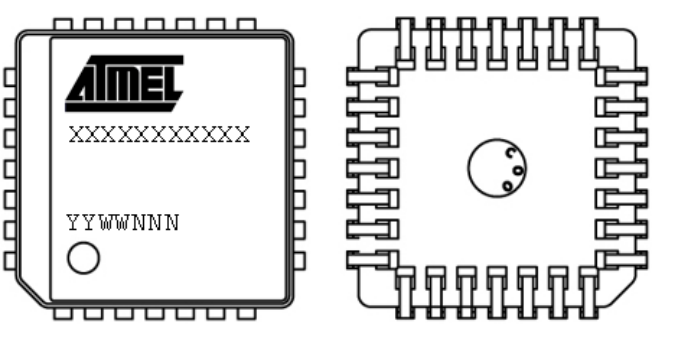
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

		Atmel®		MICROCHIP	
Lead/Pin/ Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
8	UDFN 6X5 MM		Top Mark Line 1= ATML, Class Code, Date Code Line 2 = Truncation Code Line 3 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= ATML, Class Code, Date Code Line 2 = Truncation Code Line 3 = Lot Traceability ● = Pin 1 indicator
10	VDFN 3X3 MM		Top Mark Line 1= Device Name (shortened) Line 2 = Device type code / Class code / Die Revision / Assembly location code Line 3 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= Device Name (shortened) Line 2 = Device type code / Class code, Die Revision / Assembly location code Line 3 = Lot Traceability ● = Pin 1 indicator Bottom Mark No bottom mark
2	XSFN (DFN 5X3.5 MM)		Top Mark Line 1= ATML Line 2 = Truncation Code, Device Information Line 3 = Lot Traceability ● = Pin 1 indicator		Top Mark Line 1= ATML Line 2 = Truncation Code, Device Information, Year Line 3 = Lot Traceability ● = Pin 1 indicator
3	LAB (DFN 2.5X6.5 MM)		Top Mark Line 1= Subcon Code Line 2 = Year, Month Line 3 = Trace Code ● = Pin 1 indicator		Top Mark Line 1= Country of Origin Line 2 = Date Code Line 3 = Lot Traceability ● = Pin 1 indicator



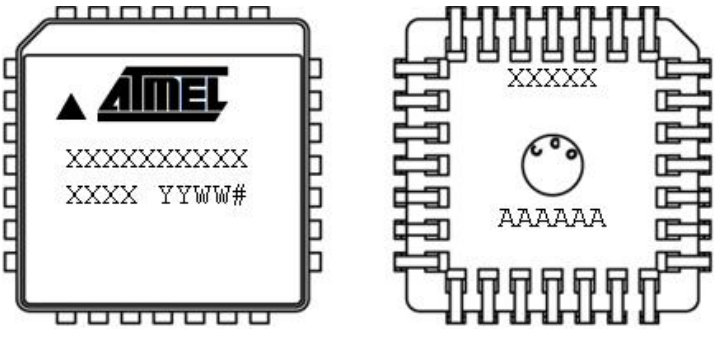
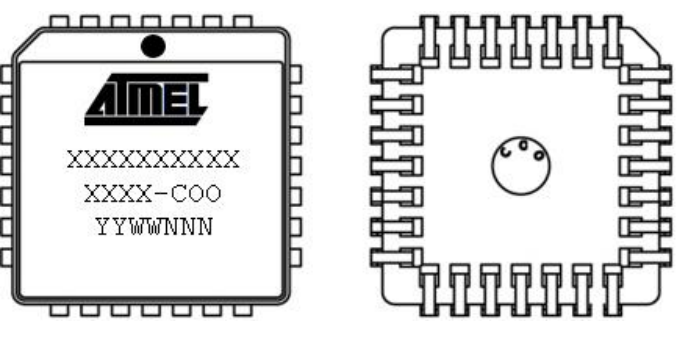
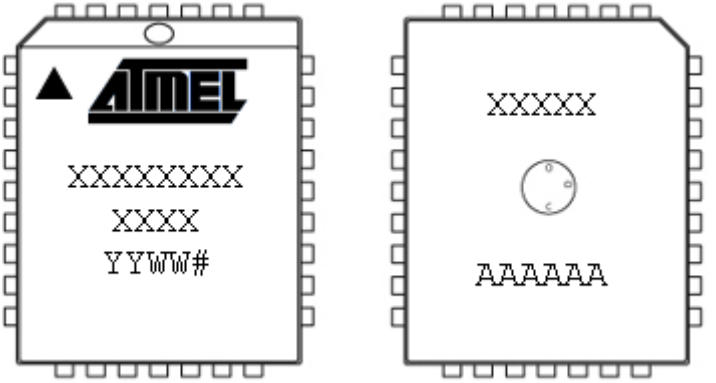
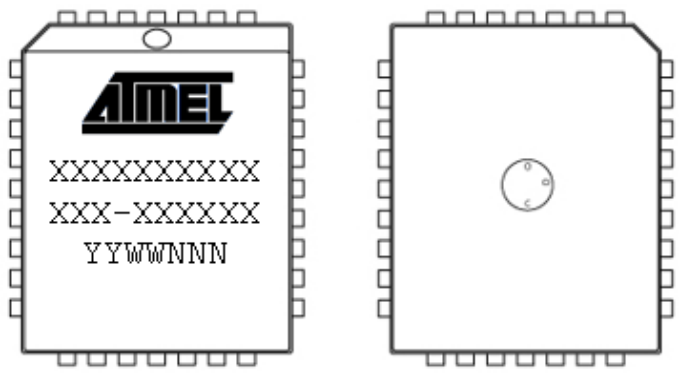
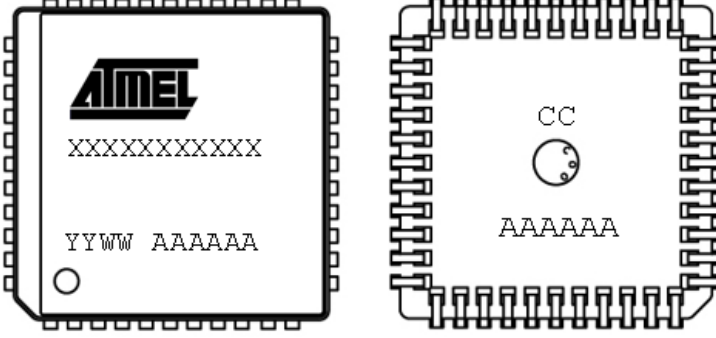
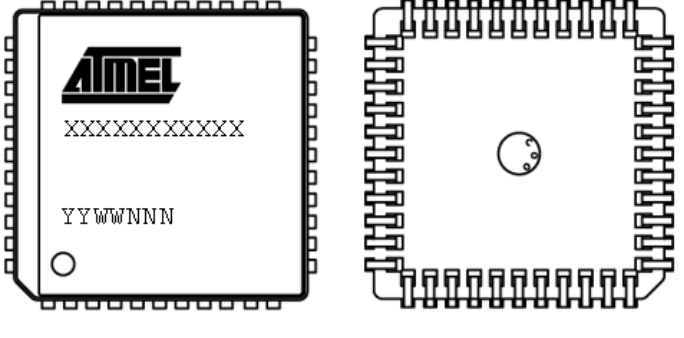
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
20	PLCC		Top Mark Line 1= Device Name Line 2 = Lot Traceability Line 3 = Device Information, Date Code ● = Pin 1 indicator		Top Mark Line 1= Device Name Line 2 = Device Information, Date Code Line 3 = Lot Traceability Line 4 = Country of Origin ● = Pin 1 indicator
			Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Date Code, MRL (if shown in ABI) ▲ = Pin 1 location Bottom Mark Line 1 = Country of Origin if not in injector mold Country of Origin in injector mold Line 2 = Lot Traceability		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Lot Traceability ● = Pin 1 indicator Bottom Mark No bottom mark Country of Origin in injector mold
28	PLCC		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Lot Traceability O = Pin 1 indicator Bottom Mark Line 1 = Country Code Country of Origin in injector mold Line 2 = Lot Traceability		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Lot Traceability O = Pin 1 indicator Bottom Mark No bottom mark Country of Origin in injector mold
			Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Date Code, Lot Traceability O = Pin 1 indicator Bottom Mark Line 1 = Country Code Country of Origin in injector mold Line 2 = Lot Traceability		Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Lot Traceability O = Pin 1 indicator Bottom Mark No bottom mark Country of Origin in injector mold



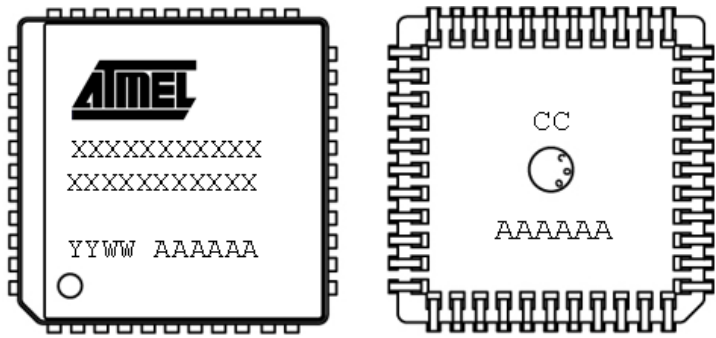
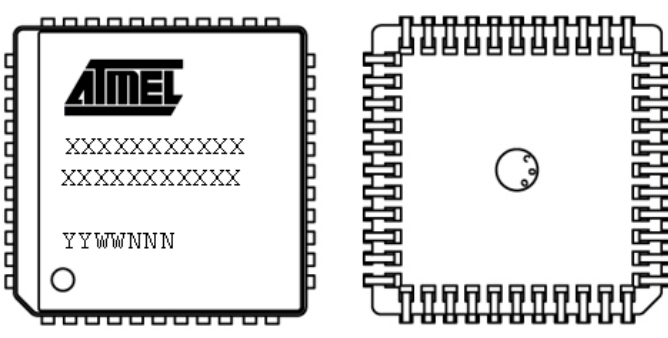
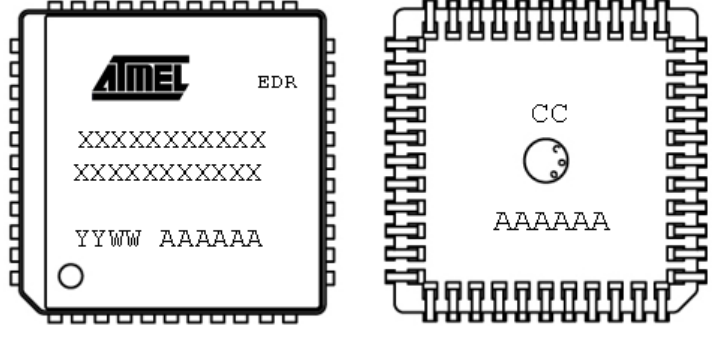
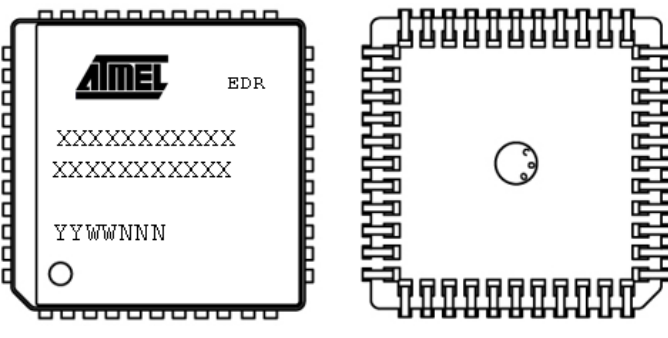
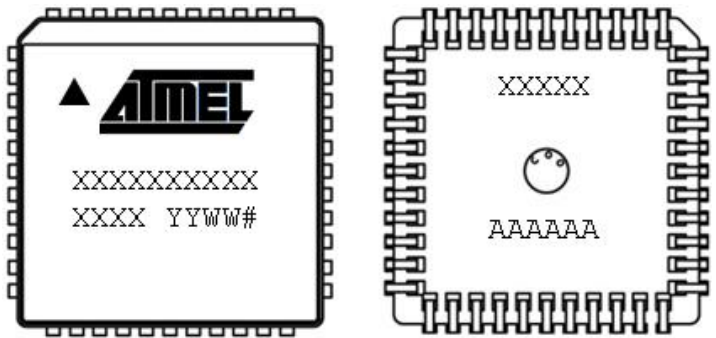
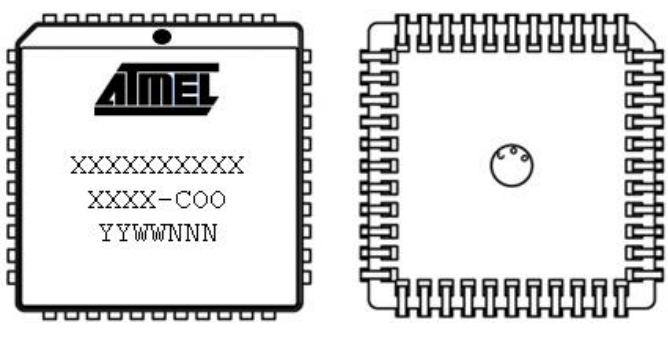
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Lot Traceability ● = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
32	PLCC		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Die ID, Revision Line 4 = Lot Traceability ● = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
44	PLCC		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>



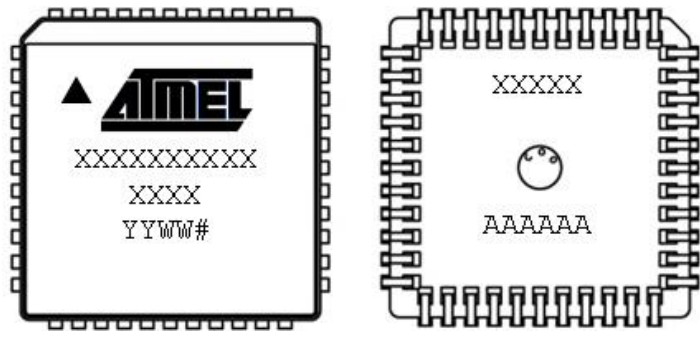
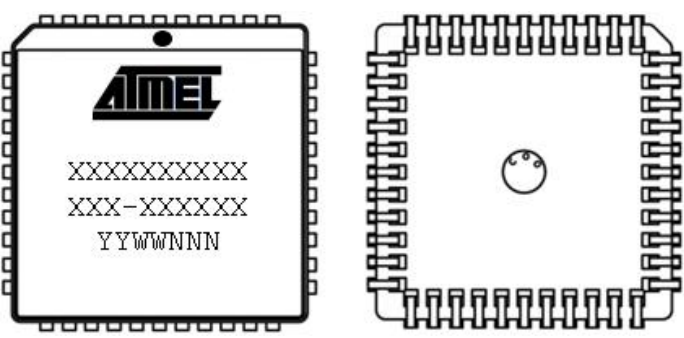
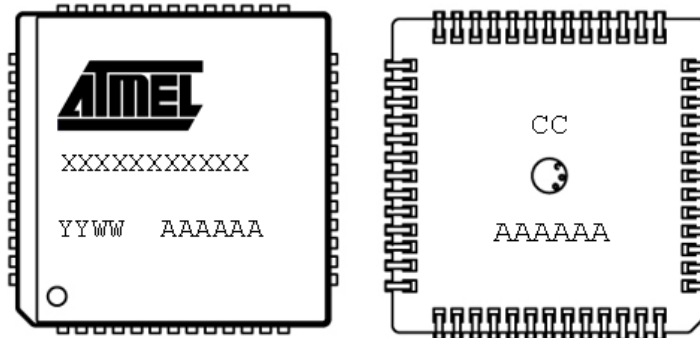
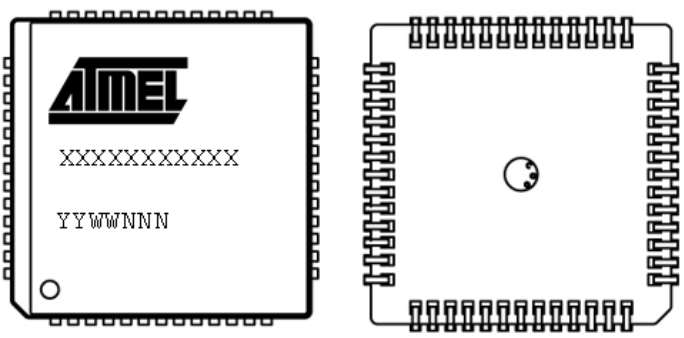
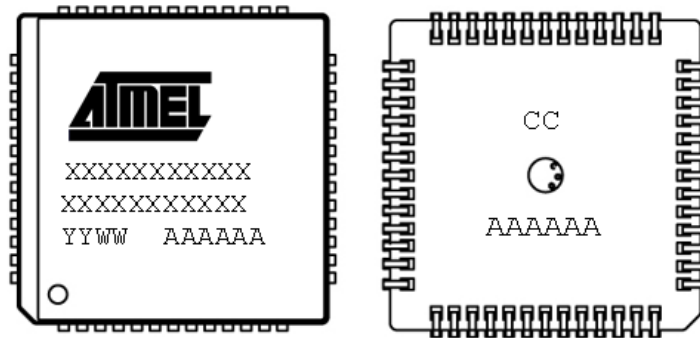
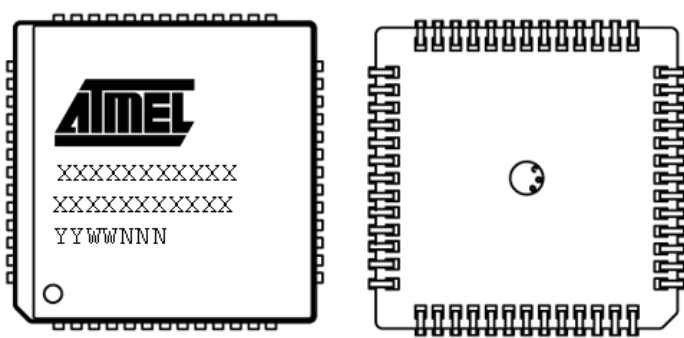
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/ Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
			<p>Top Mark Line 1= Atmel Logo, EDR Line 2 = Silicon Revision Line 3 = Device Name Line 4 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo, EDR Line 2 = Silicon Revision Line 3 = Device Name Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country or Origin Line 4 = Lot Traceability ● = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>



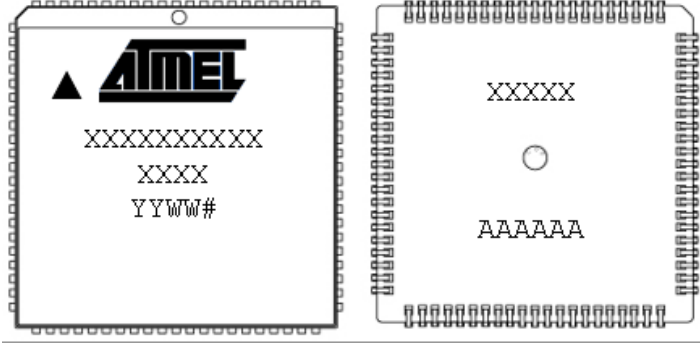
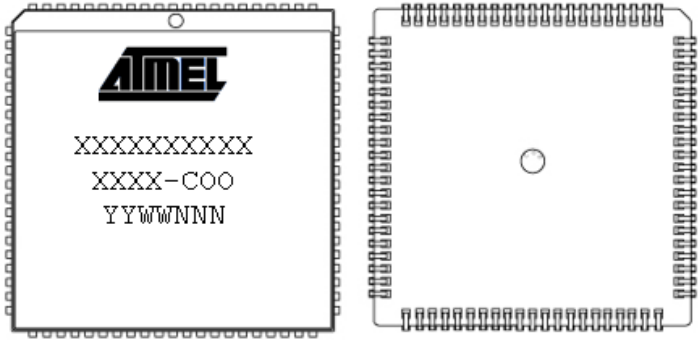
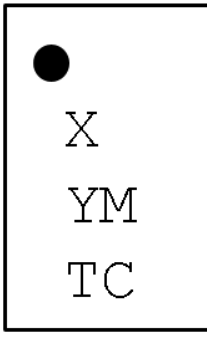
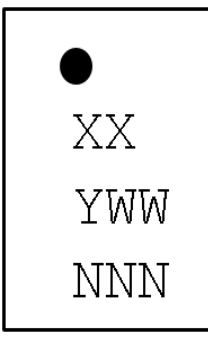
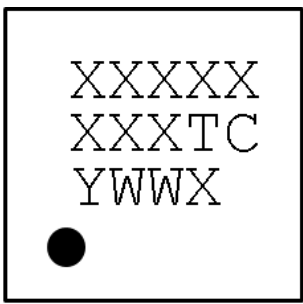
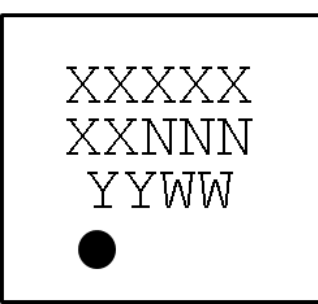
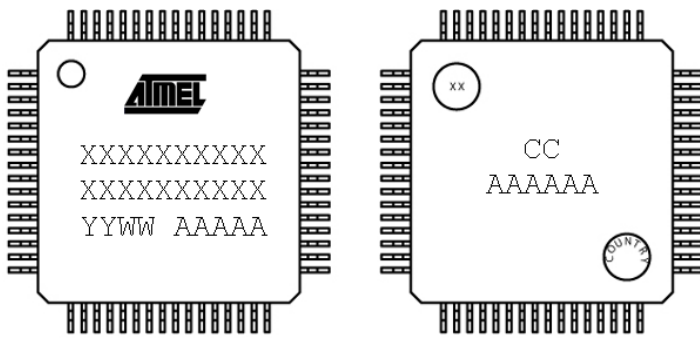
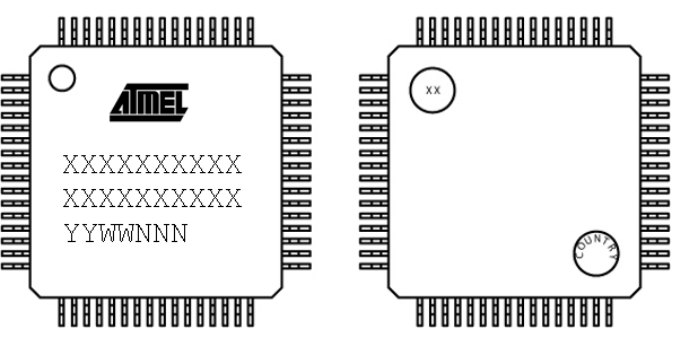
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Die ID, Revision Line 4 = Lot Traceability ● = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
52	PLCC		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>



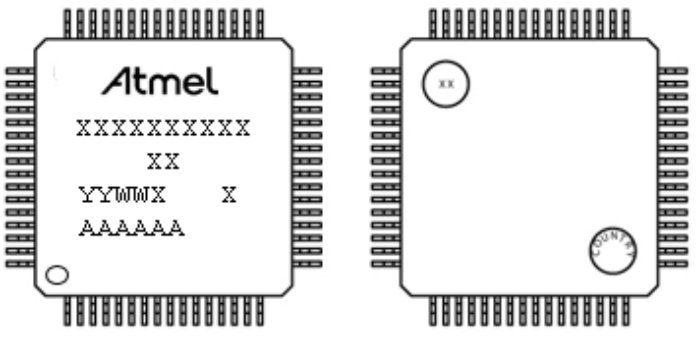
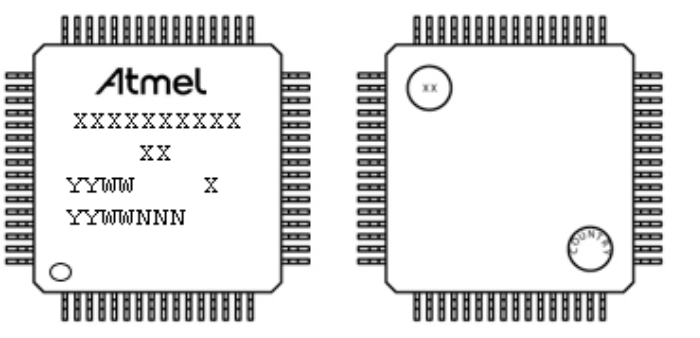
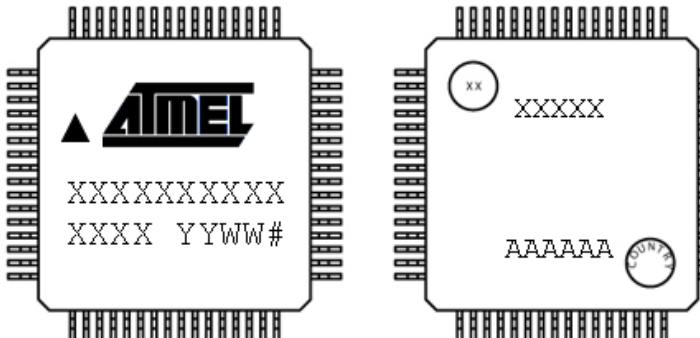
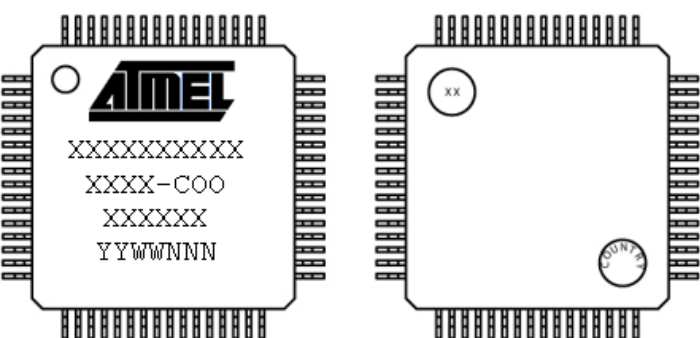
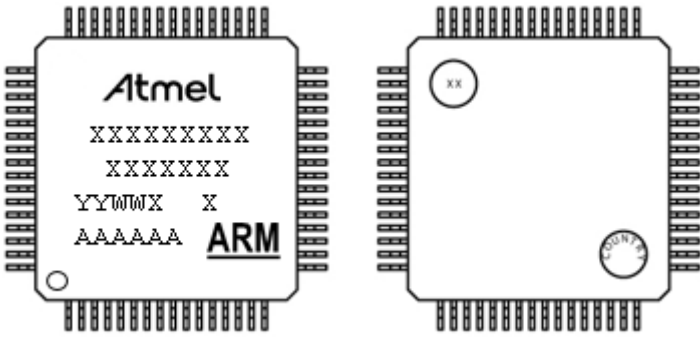
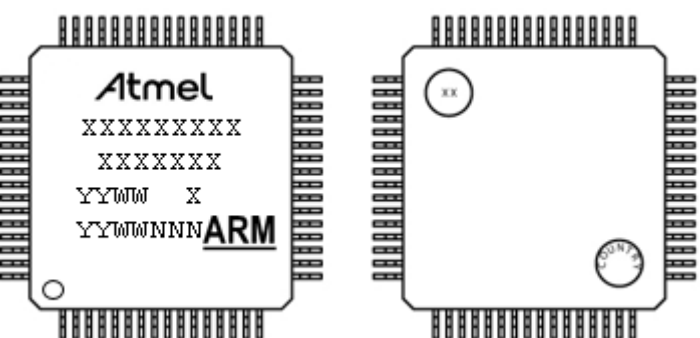
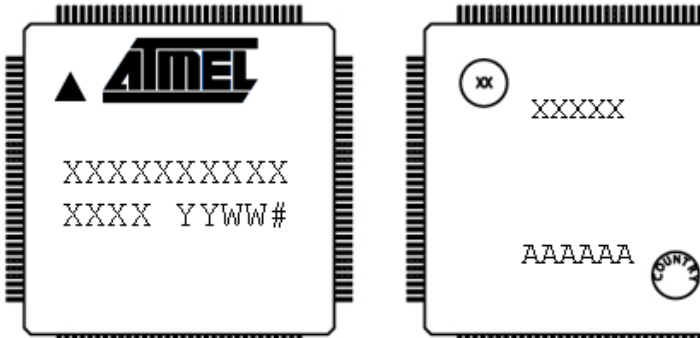
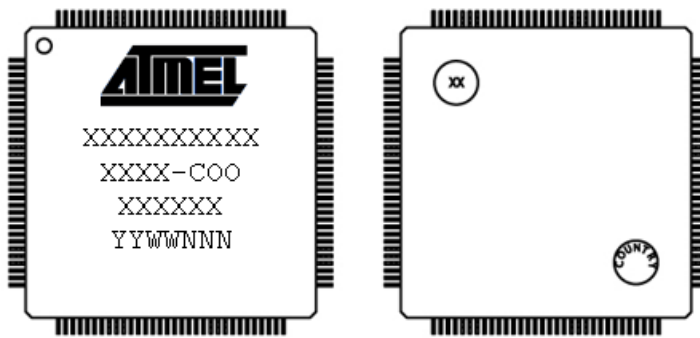
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
84	PLCC		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country or Origin Line 4 = Lot Traceability</p> <p>Bottom Mark No bottom mark Country of Origin in injector mold</p>
2	CONTACT (SIP MODULE - 4x2 MM)		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Country of Origin in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1=Country of Origin Line 2 = Date Code Line 3 = Lot Traceability ● = Pin 1 indicator</p>
8	MSOP - 3x3 MM		<p>Top Mark Line 1= Truncation Code Line 2 = Class Code, Trace Code Line 3 = Lot Traceability ● = Pin 1 indicator</p>		<p>Top Mark Line 1= Truncation Code Line 2 = Class Code, Lot Traceability Line 3 = Date Code ● = Pin 1 indicator</p>
32 / 48	LQFP		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>



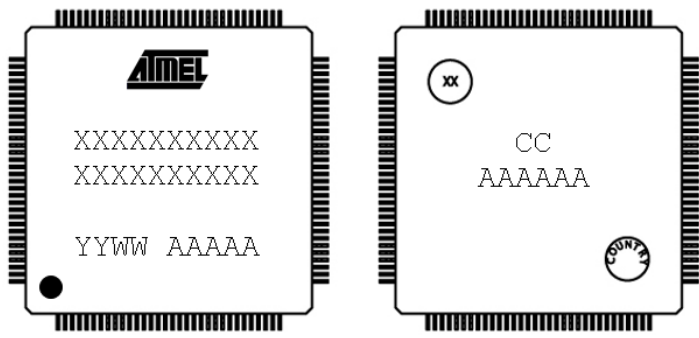
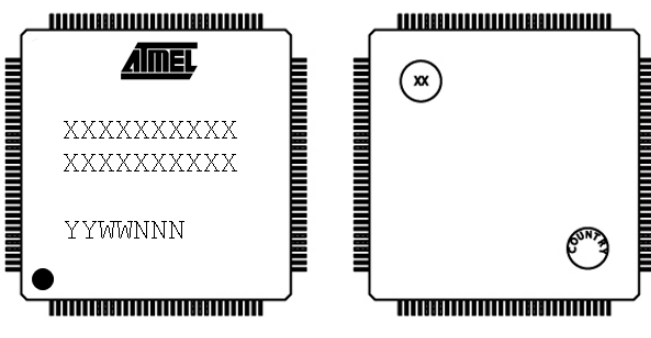
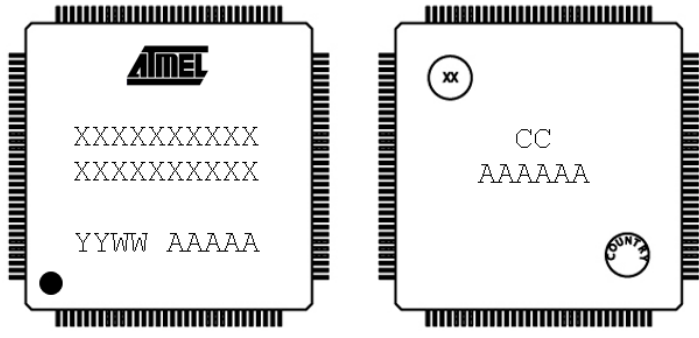
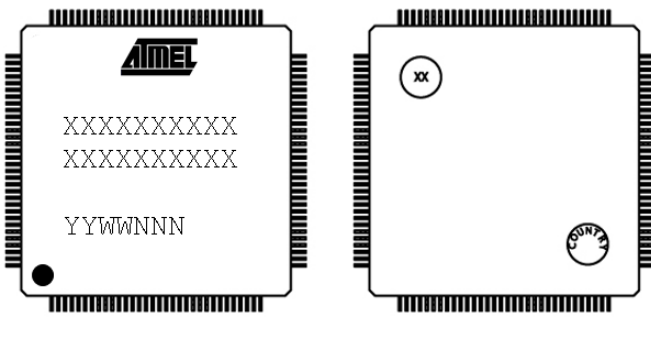
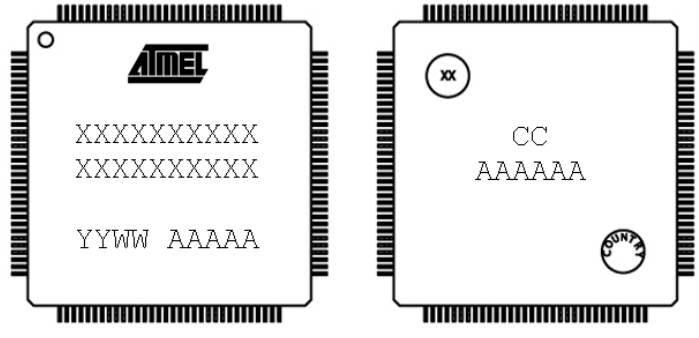
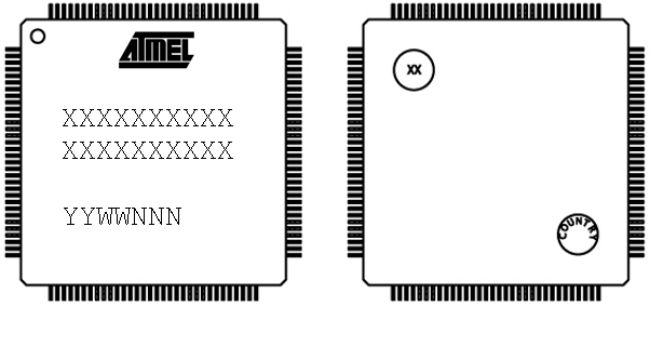
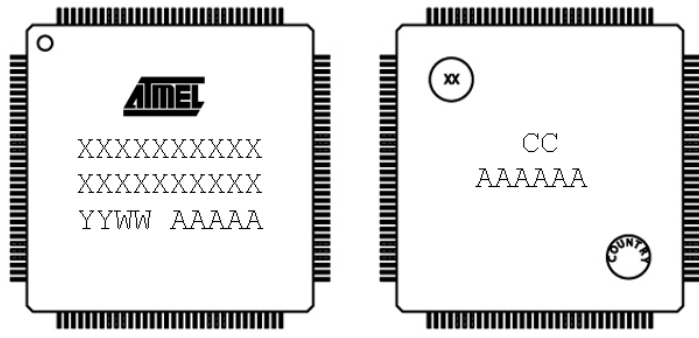
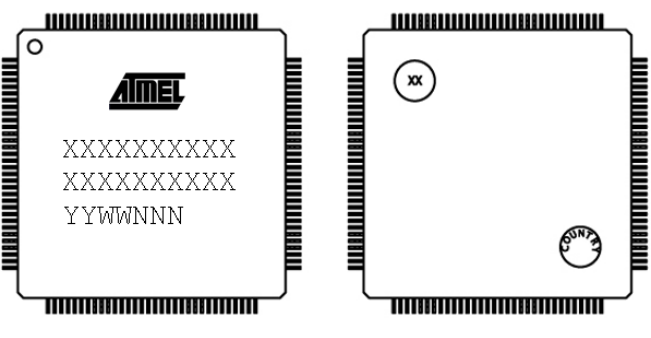
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
48	LQFP 7X7 MM		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Die ID, Revision Line 5 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
44	LQFP 10X10 MM		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Die ID, Revision Line 5 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>



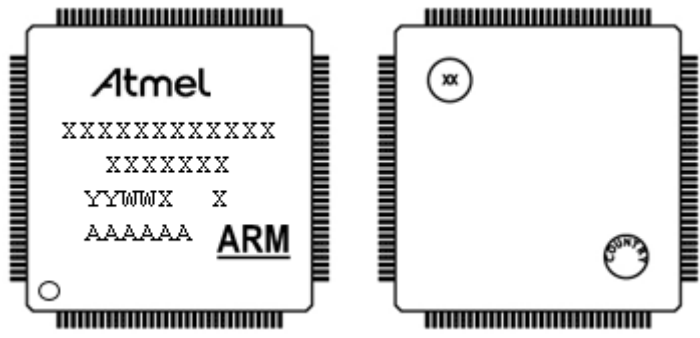
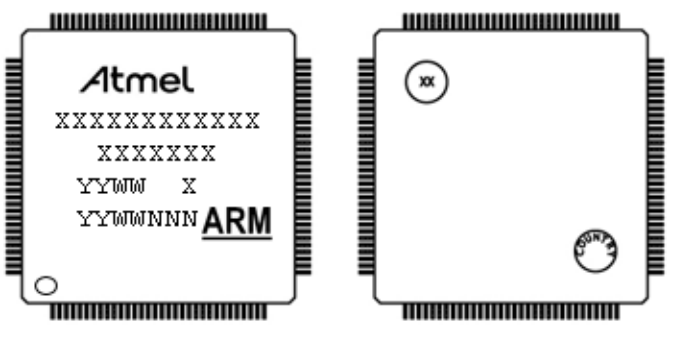
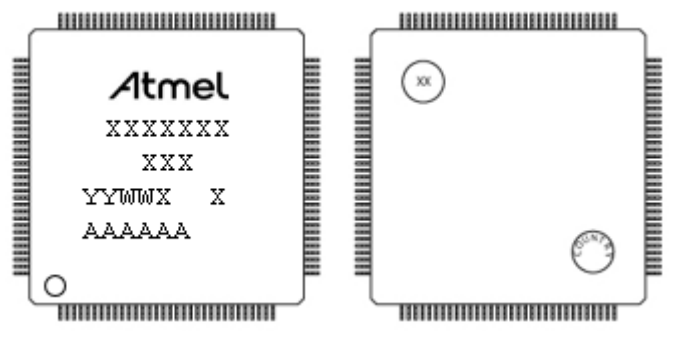
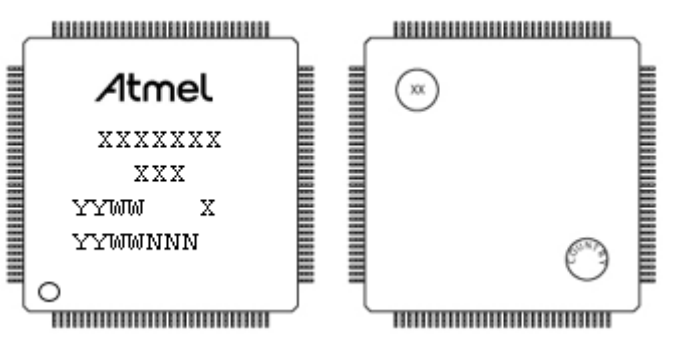
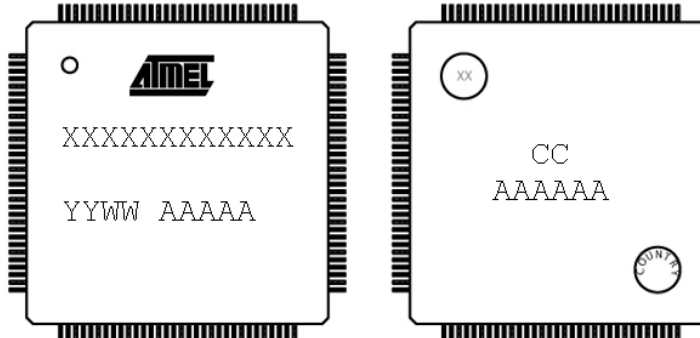
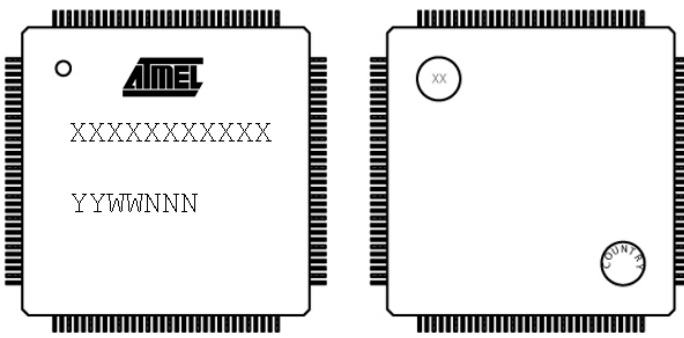
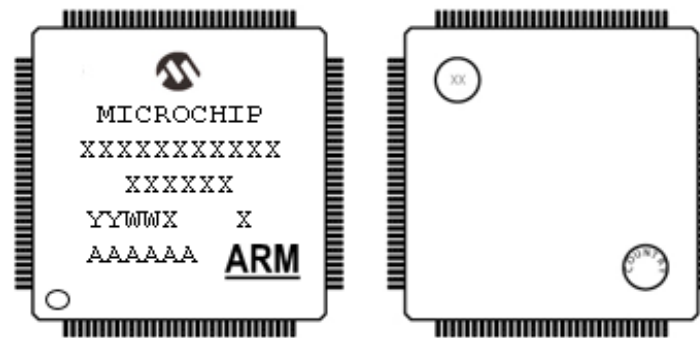
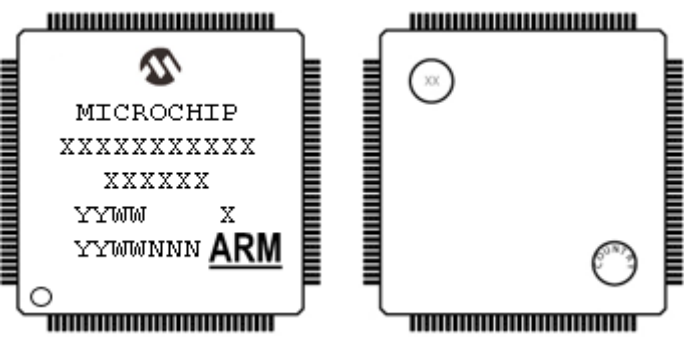
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/ Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Lot Traceability ● = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Lot Traceability ●O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
64	LQFP 10X10 MM		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Lot Traceability ● = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Lot Traceability ●O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>



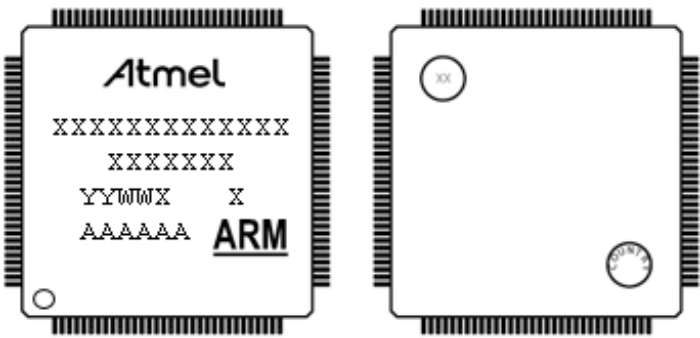
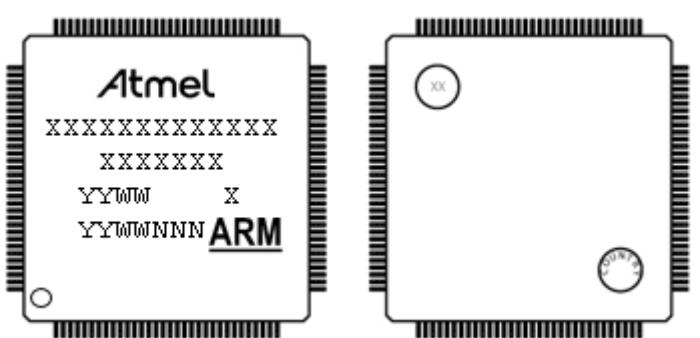
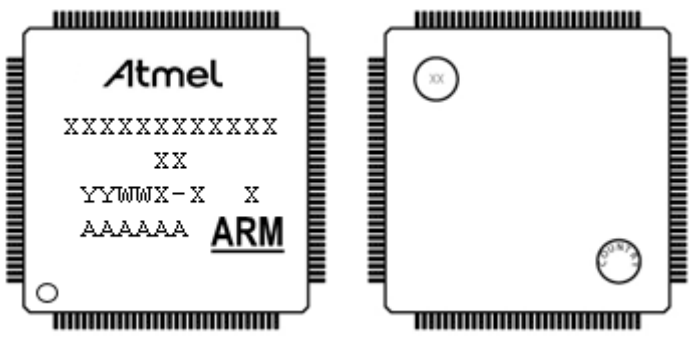
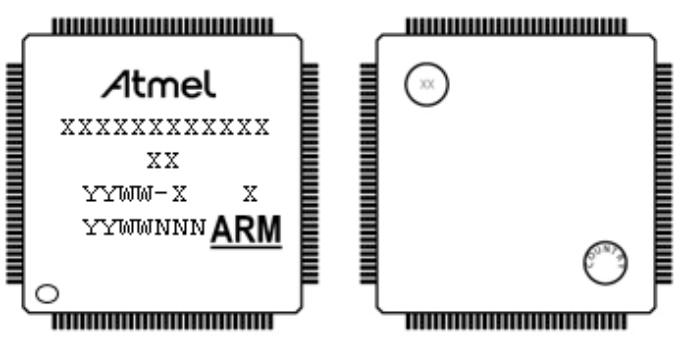
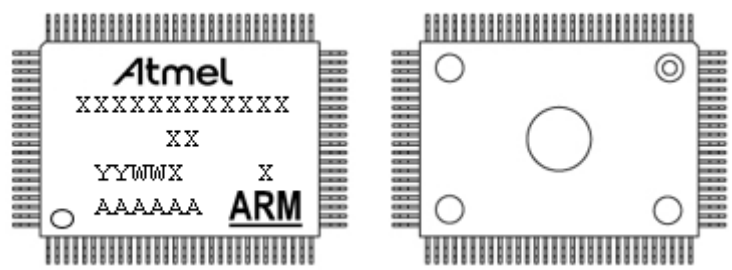
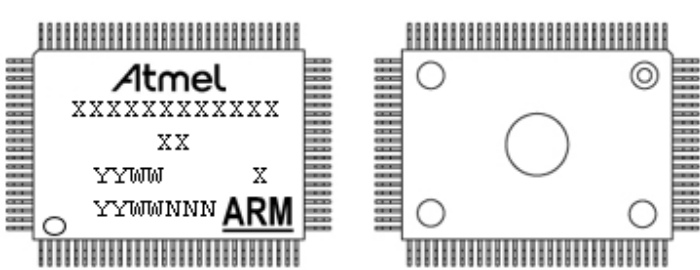
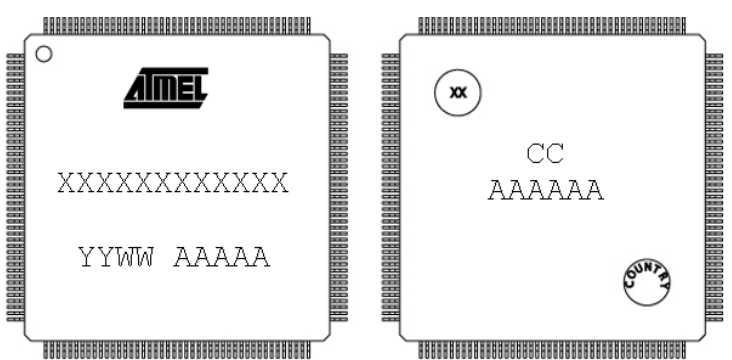
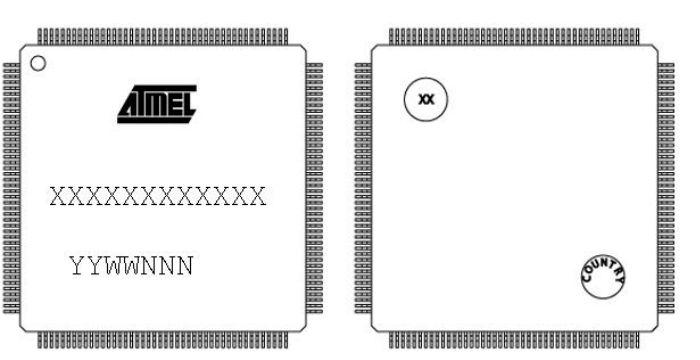
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

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Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
80	LQFP 12X12 MM		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
100	LQFP 14X14 MM		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
			<p>Top Mark Line 1= Microchip Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>		<p>Top Mark Line 1= Microchip Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>



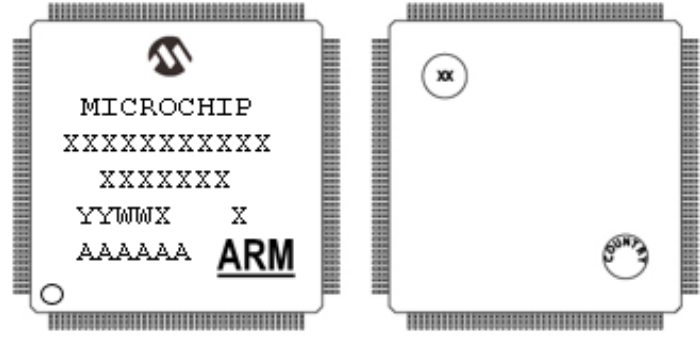
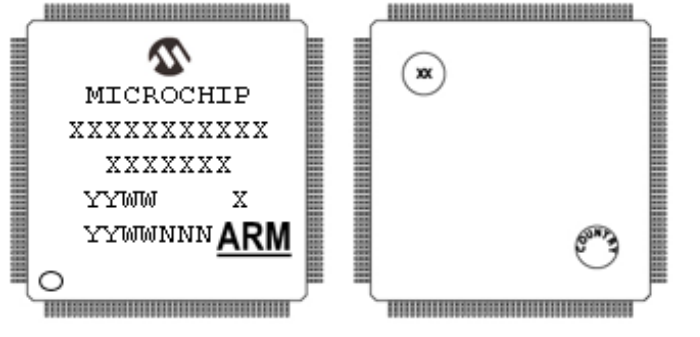
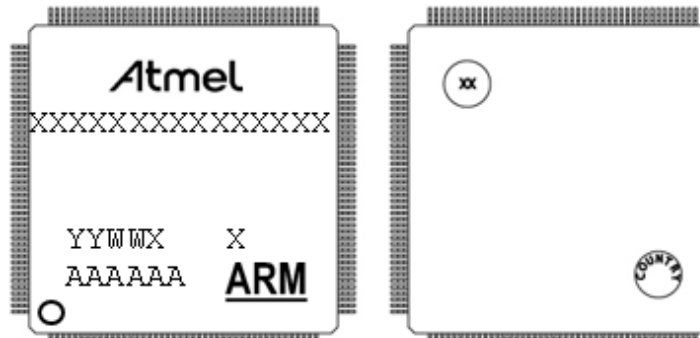
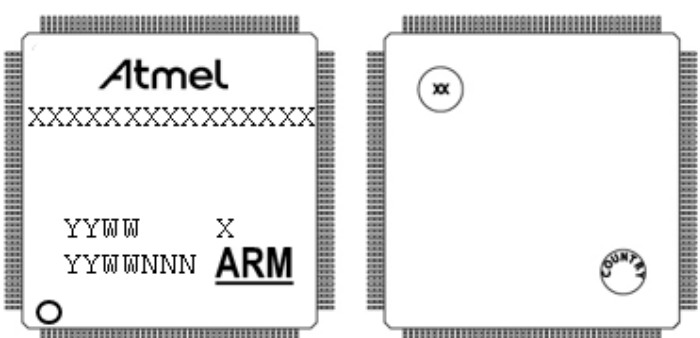
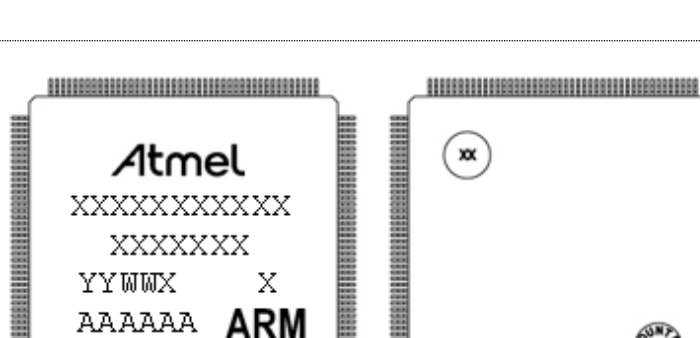
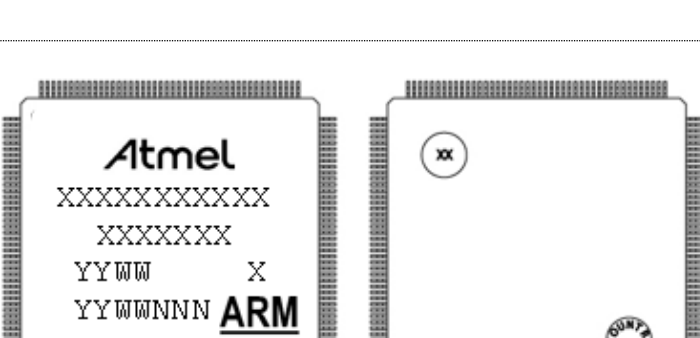
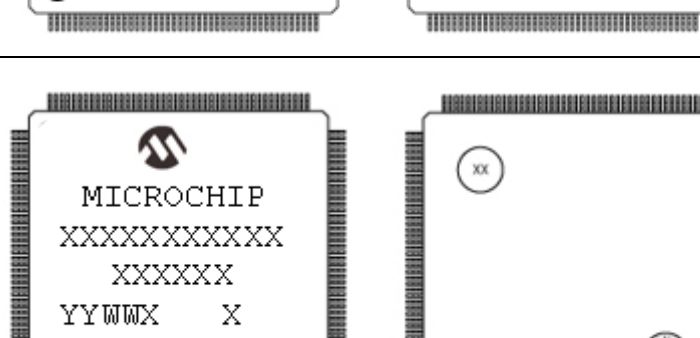
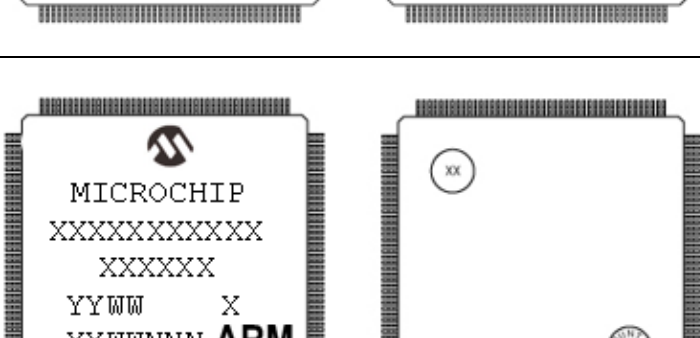
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

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Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			<p>Top Mark Line 1= Microchip Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision</p> <p>Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>		<p>Top Mark Line 1= Microchip Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
			<p>OR</p> <p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision</p> <p>Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>		<p>OR</p> <p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
128	LQFP 14X20 MM		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision</p> <p>Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
144	LQFP 20X20 MM		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>



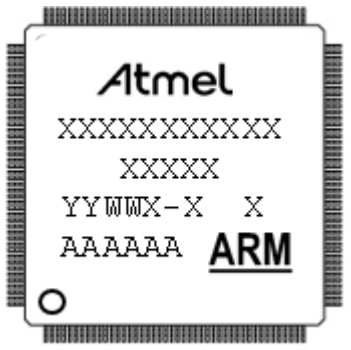
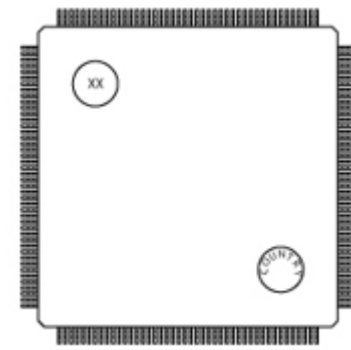
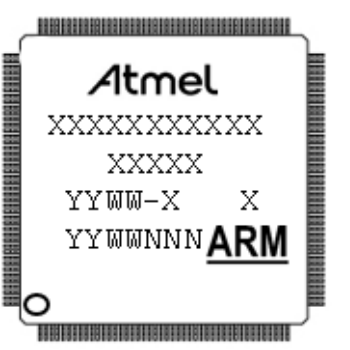
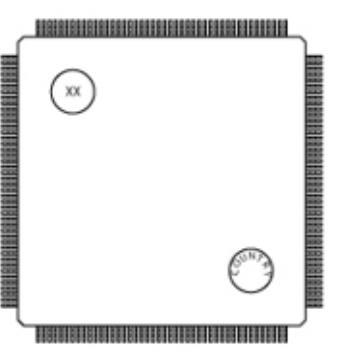
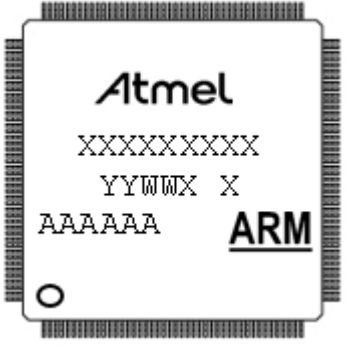
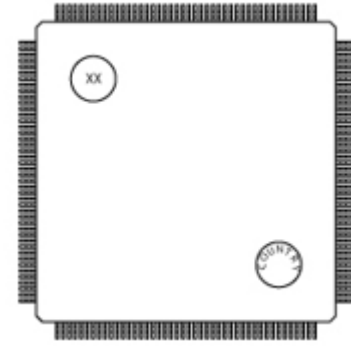
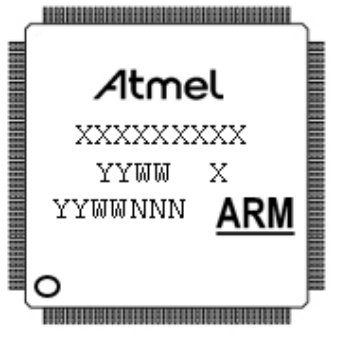
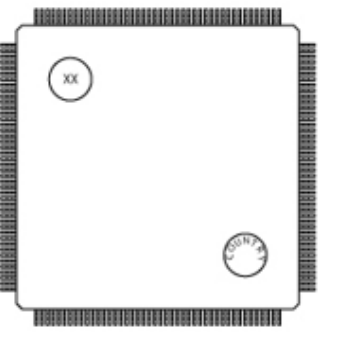
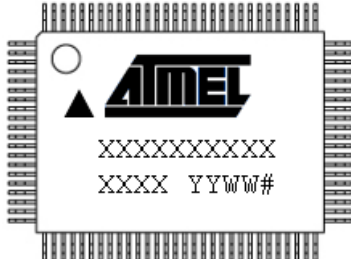
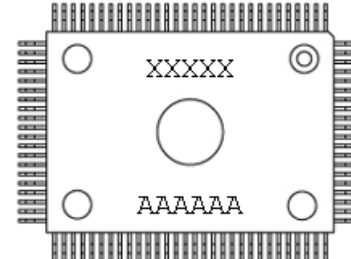
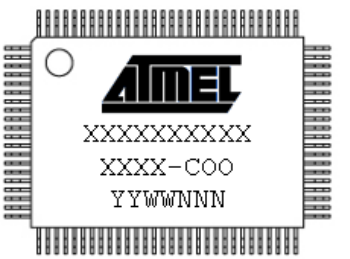
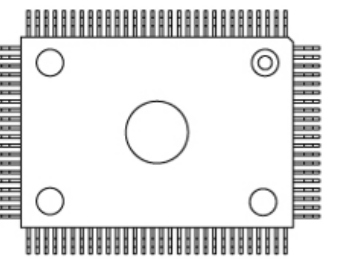
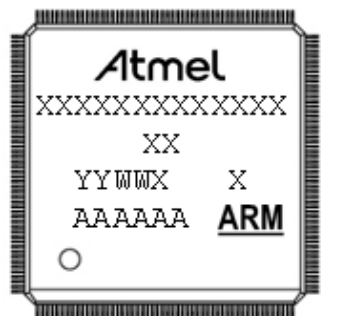
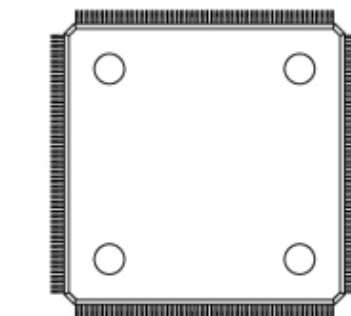
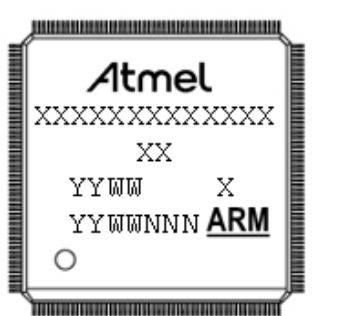
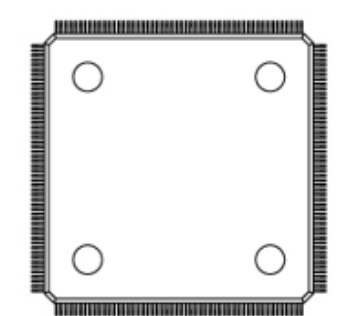
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

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			<p>Top Mark Line 1= Microchip Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision</p> <p>Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>		<p>Top Mark Line 1= Microchip Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
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			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision</p> <p>Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
176	LQFP 24X24 MM		<p>Top Mark Line 1= Microchip Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision</p> <p>Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>		<p>Top Mark Line 1= Microchip Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>



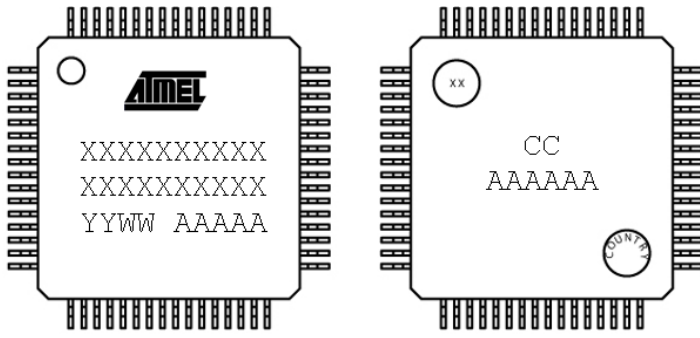
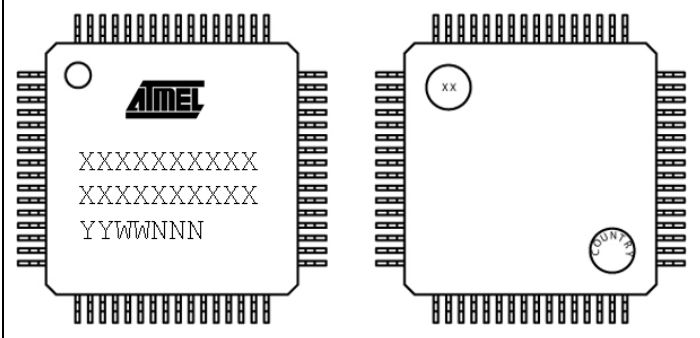
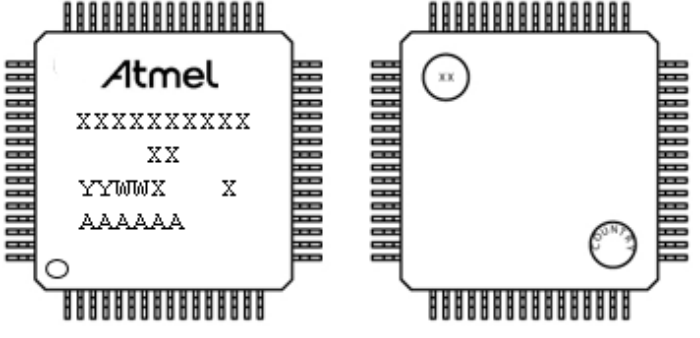
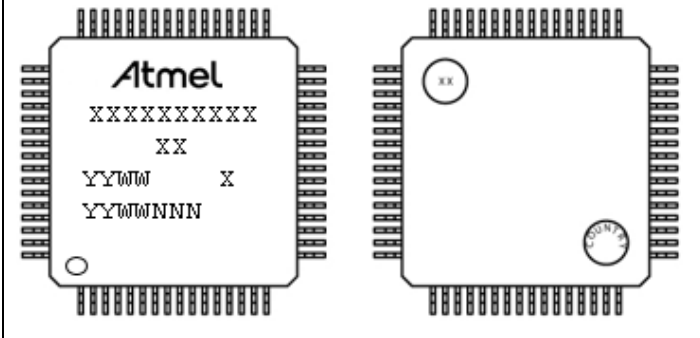
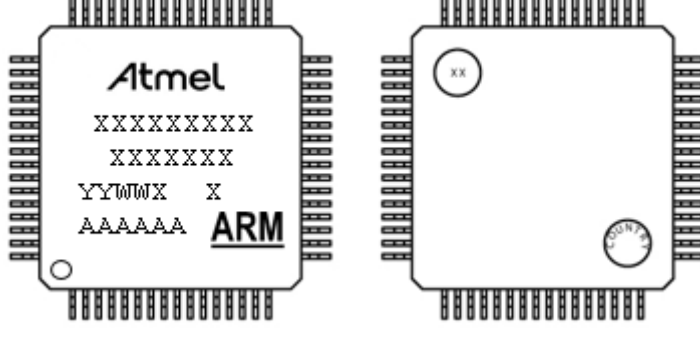
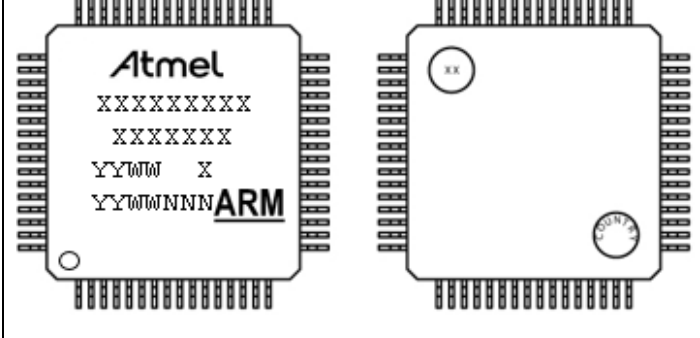
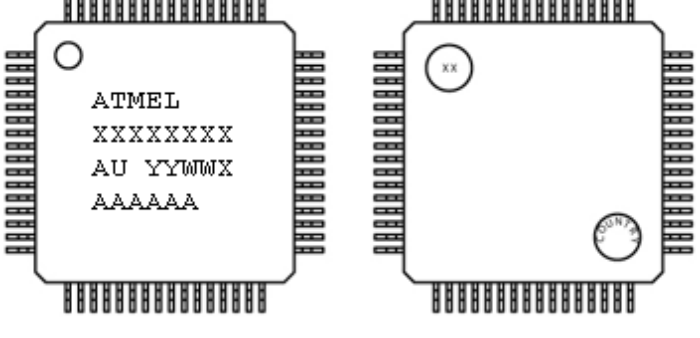
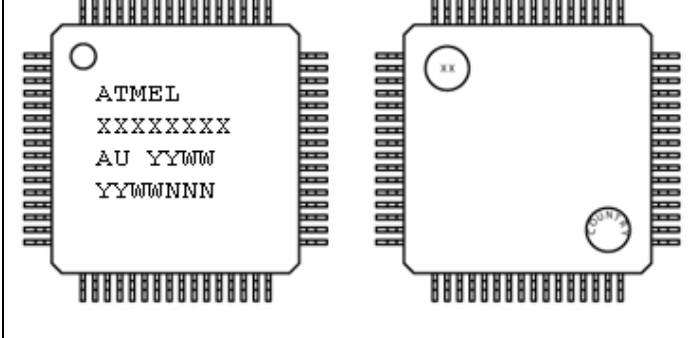
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

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100	PQFP 14X20 MM			Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Date Code, MRL (if shown in ABI) O = Pin 1 indicator ▲ = Pin 1 location Bottom Mark Line 1 = Country of Origin if not in injector mold Line 2 = Lot Traceability			Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Lot Traceability O = Pin 1 indicator Bottom Mark No bottom mark
208	PQFP 28X28 MM			Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 3 = Date Code, Subcon Code, Design Revision Line 4 = Lot Traceability, ARM O = Pin 1 indicator Bottom Mark No bottom mark			Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 3 = Date Code, Design Revision Line 4 = Lot Traceability, ARM O = Pin 1 indicator Bottom Mark No bottom mark



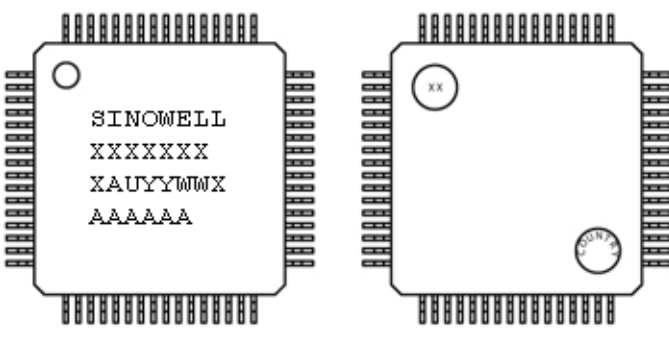
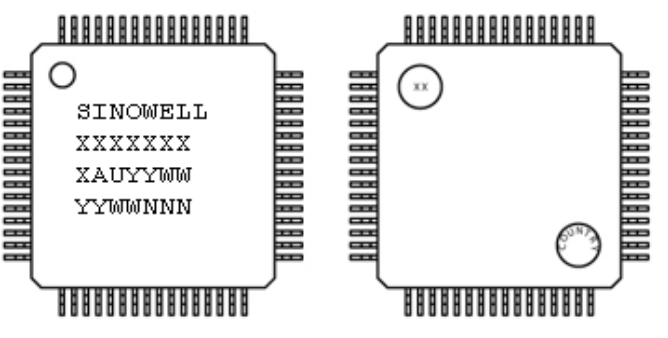
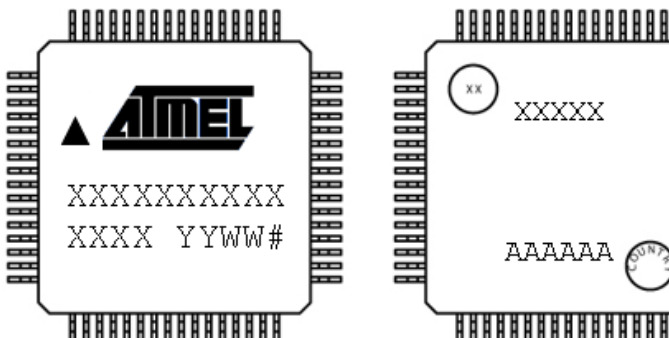
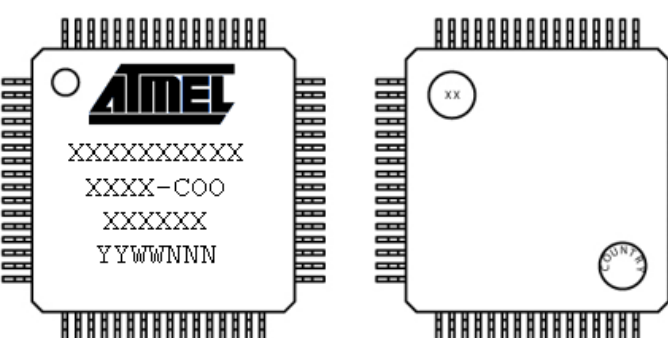
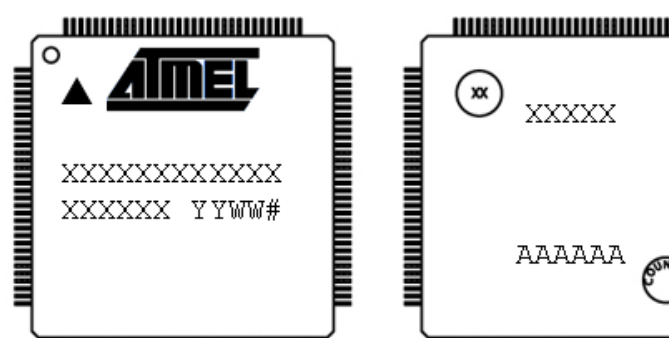
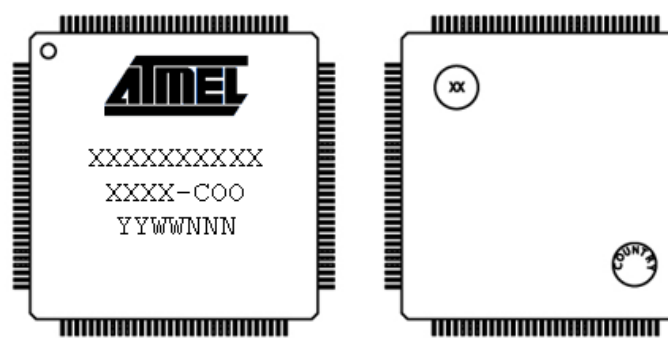
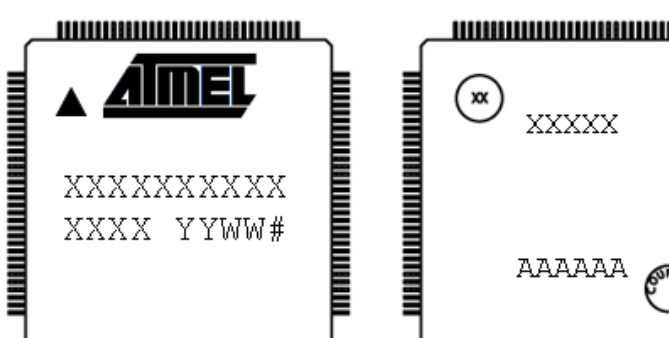
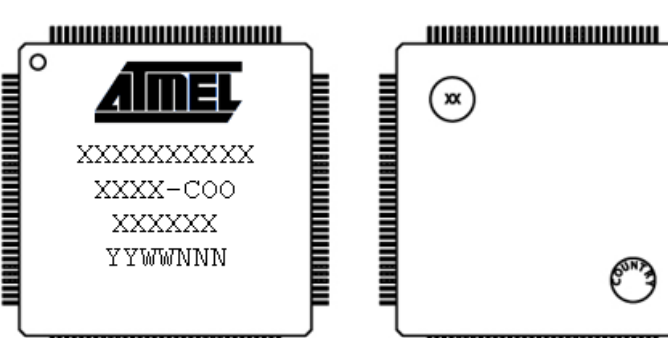
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
32 / 48	TQFP 7X7 MM		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Subcon Code, Design Revision Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Design Revision Line 5 = Lot Traceability, ARM O = Pin 1 indicator</p> <p>Bottom Mark No bottom mar</p>
			<p>Top Mark Line 1= Atmel Line 2 = Device Name Line 3 = Device Information, Date Code, Subcon Code Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>		<p>Top Mark Line 1= Atmel Line 2 = Device Name Line 3 = Device Information, Date Code Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>



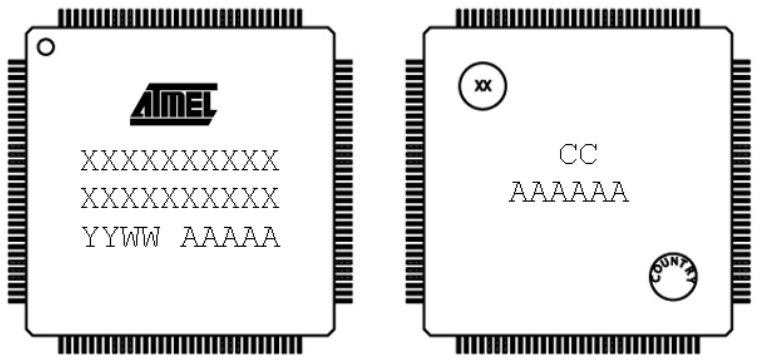
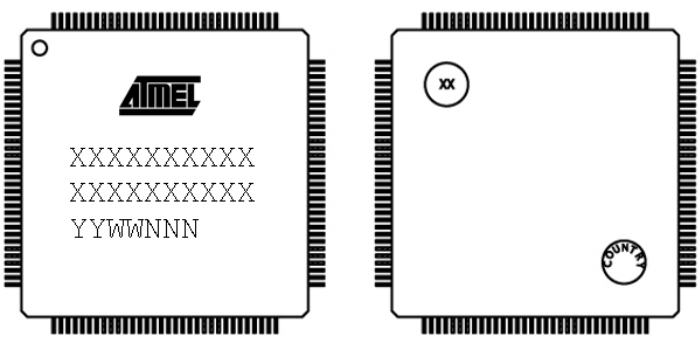
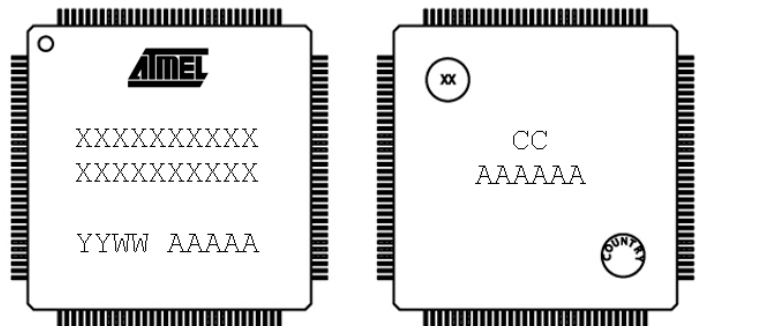
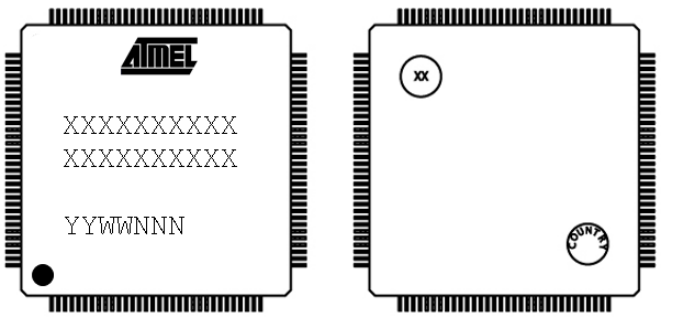
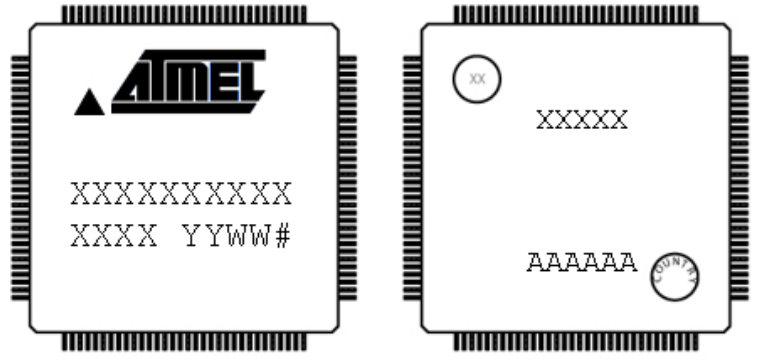
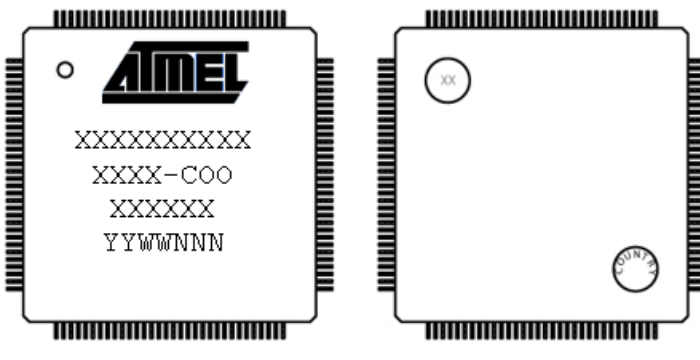
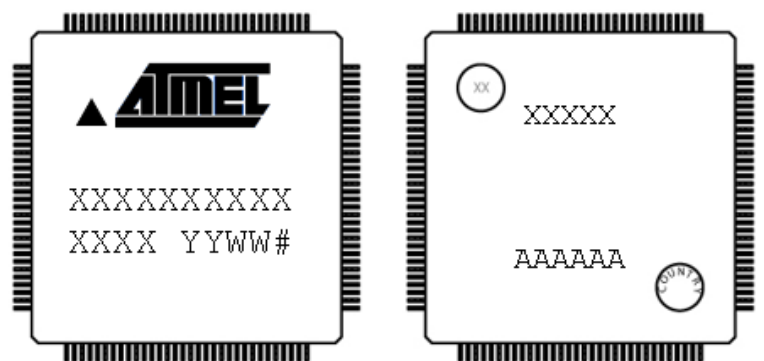
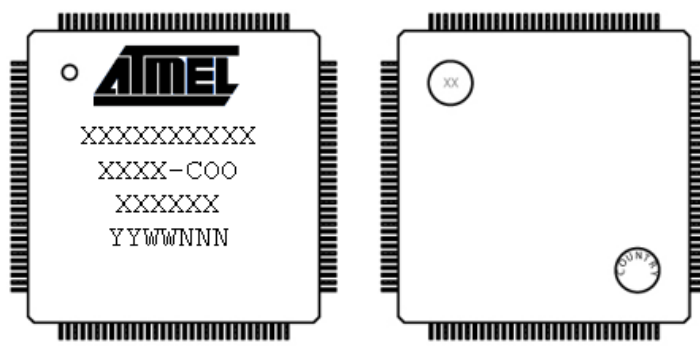
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

This chart is to be used as a general guidelines only and does not include custom marking. It does not contain actual part marking on any specific product.

					
Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			<p>Top Mark Line 1= SINOWEL Line 2 = Device Name Line 3 = Device Information, Date Code, Subcon Code Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>		<p>Top Mark Line 1= SINOWEL Line 2 = Device Name Line 3 = Device Information, Date Code Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Die ID, Revision Line 5 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
44	TQFP 10X10 MM		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Date Code, MRL (if shown in ABI) O = Pin 1 indicator ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
			<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Die ID, Revision Line 5 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>



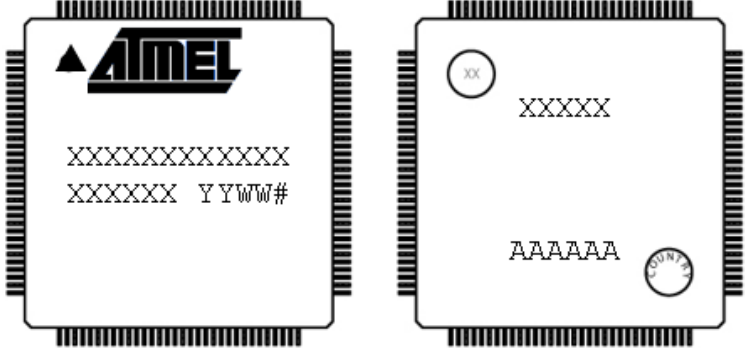
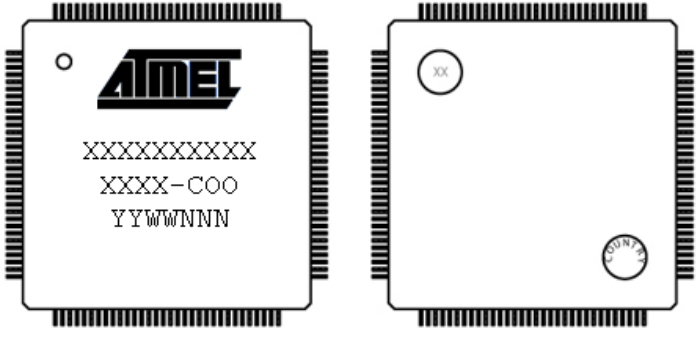
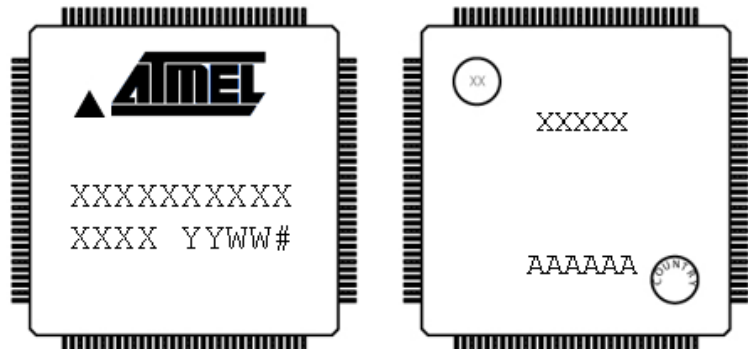
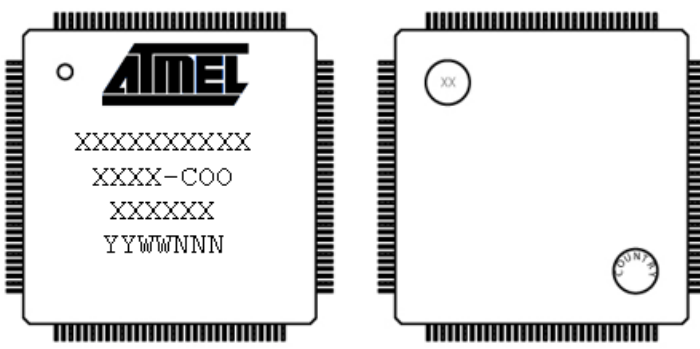
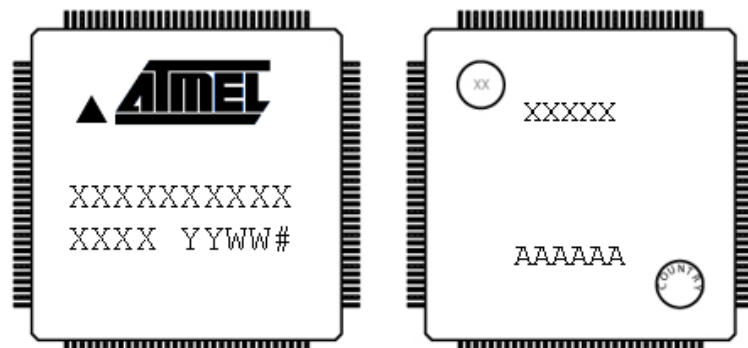
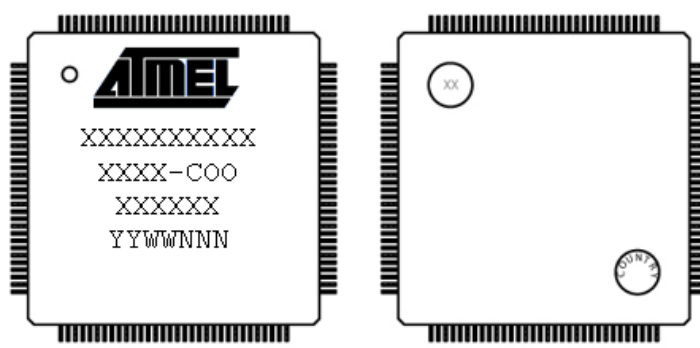
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

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Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			<p>Top Mark Line 1 = Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Line 2 = Lot Traceability</p>		<p>Top Mark Line 1 = Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
64	TQFP 10X10 MM		<p>Top Mark Line 1 = Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Date Code, Lot Traceability ● = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Line 2 = Lot Traceability</p>		<p>Top Mark Line 1 = Atmel Logo Line 2 = Device Name Line 3 = Device Information Line 4 = Lot Traceability ● = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
64	TQFP 14X14 MM		<p>Top Mark Line 1 = Atmel Logo Line 2 = Device Name Line 3 = Device Information, Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1 = Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Die ID, Revision Line 5 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
			<p>Top Mark Line 1 = Atmel Logo Line 2 = Device Name Line 3 = Device Information, Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1 = Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Die ID, Revision Line 5 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>



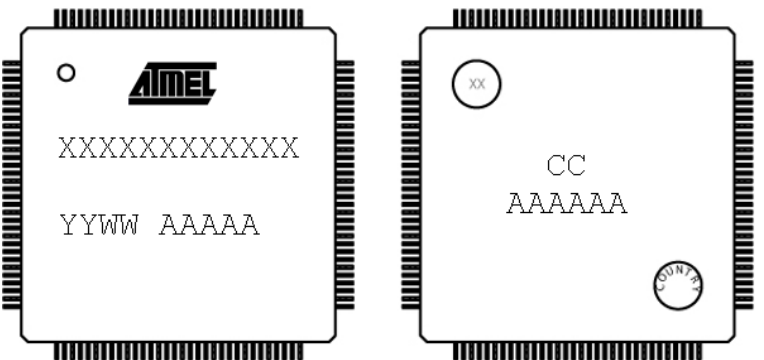
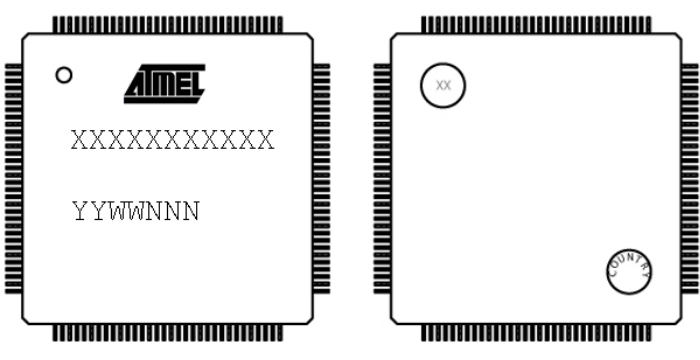
PART MARKING GUIDELINE (Supplement to PCN# GBNG-15KQFZ896 ADDENDUM)

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Lead/Pin/Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
100	TQFP 14X14 MM		<p>Top Mark Line 1 = Atmel Logo Line 2 = Device Name Line 3 = Device Information, Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1 = Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
			<p>Top Mark Line 1 = Atmel Logo Line 2 = Device Name Line 3 = Device Information, Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1 = Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Die ID, Revision Line 5 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>
			<p>Top Mark Line 1 = Atmel Logo Line 2 = Device Name Line 3 = Device Information, Date Code, MRL (if shown in ABI) ▲ = Pin 1 location</p> <p>Bottom Mark Line 1 = Country of Origin if not in injector mold Line 2 = Lot Traceability</p>		<p>Top Mark Line 1 = Atmel Logo Line 2 = Device Name Line 3 = Device Information, Country of Origin Line 4 = Die ID, Revision Line 5 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark No bottom mark</p>

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Lead/Pin/ Bump Count	Package Description	Pre-Change_Marking Diagram (Atmel)	Pre-Change_Marking Guidelines (Atmel)	Post-Change_Marking Diagram (Microchip)	Post-Change_Marking Guidelines (Microchip)
			<p>Top Mark Line 1= Atmel Logo Line 2 = Date Code, Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Line 2 = Lot Traceability</p>		<p>Top Mark Line 1= Atmel Logo Line 2 = Lot Traceability O = Pin 1 indicator</p> <p>Bottom Mark Line 1 = Country Code Line 2 = Lot Traceability</p>