## Product Change Notification - BCDSI00009 - JAON-24FRFP480

Date:07 Aug 2016Product Category:Supertex

**Notification subject:** CCB 1737.01 Final Notice: Release to production of listed Micrel Driver product type

manufactured with the BCDSI process technology to Microchip Fab.

Notification text: PCN Status: Final Notification

Note: This final PCN only pertains to the products listed in this PCN. Additional final PCNs may be issued for this combination of product type and process technology.

#### Microchip Parts Affected:

Please open the attachments found in the attachments field below labeled as PCN # Affected CPN.

NOTE: For your convenience Microchip includes identical files in two formats (.pdf and .xls).

#### **Description of Change:**

Release to production of listed Micrel Driver product type manufactured with the BCDSI process technology to Microchip Fab.

NOTE: Please review the FAQ linked here for additional information about this change.

#### Pre Change:

Fabricated at Micrel fabrication site (San Jose, CA, USA) using 6 inch wafers.

#### Post Change:

Fabricated at Microchip fabrication site using 8 inch wafers.

#### Pre and Post Change Summary:

	Pre Change	Post Change
	Micrel fabrication site	
Fab Site		Microchip fabrication site
	(San Jose, CA, USA)	
Wafer Size	6 inch wafers	8 inch wafers

#### Impacts to Data Sheet:

None

#### Reason for Change:

To improve productivity with the closure of the Micrel fab as part of the integration of Micrel and Microchip.

#### **Change Implementation Status:**

In Progress

#### **Estimated First Ship Date:**

As identified for each CPN listed in the attached parts list.

#### **Summary Time Table of notable events to date:**

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WW	36	37	38	39	40		09	10	11	12	13		31	32	33	34	35	36	37	38	39
Initial PCN Issue Date																					
CYER- 31JLEX869	Х																				
Qual Report Available																					
JAON- 24FRFP480							X														
Final PCN Issue Date																					
JAON- 24FRFP480- BCDS100009													Х								
Estimated First Ship Date																tac	ted hed list.	par			

#### Markings to Distinguish Revised from Unrevised Devices:

Traceability code and shipping label see FAQ.

#### **Revision History:**

**September 1, 2015:** Issued initial notification as PCN number CYER-31JLEX869 **March 3, 2016:** Issued intermediate notification as PCN number JAON-24FRFP480. Attached the Qualification Report. Revised the estimated first ship date (EFSD) to inform that the EFSD will be revealed in the final PCN.

**August 7, 2016:** Issued final notification as PCN number JAON-24FRFP480-BCDSI00009 for listed Micrel's Driver products manufactured with the BCDSI process technology. Provided estimated first ship date (EFSD) for each CPN listed in the attached parts list.

The change described in this PCN does not alter Micrel's or Microchip's current regulatory compliance regarding the material content of the applicable products.

Attachment(s):

PCN\_JAON-24FRFP480\_Qual Report.pdf

PCN\_JAON-24FRFP480-BCDSI00009\_Affected\_CPN.pdf PCN\_JAON-24FRFP480-BCDSI00009\_Affected\_CPN.xls

Please contact your local Microchip sales office with questions or concerns regarding this notification.

#### **Terms and Conditions:**

If you wish to change your product/process change notification (PCN) profile please log on to our website at <a href="http://www.microchip.com/PCN">http://www.microchip.com/PCN</a> sign into myMICROCHIP to open the myMICROCHIP home page, then select a profile option from the left navigation bar.

To opt out of future offer or information emails (other than product change notification emails), click here to go to microchipDIRECT and login, then click on the "My account" link, click on "Update profile" and un-check the box that states "Future offers or

JAON-24FRFP480-BCDSI00009 - CCB 1737.01 Final Notice: Release to production of listed Micrel Driver product type manufactured with the BCDSI process technology to Microchip Fab.

Affected Catalog Part Numbers (CPN)

PCN_JAON-2	24FRFP480-BCDSI00009
CATALOG_PART_NBR	Estimated First Ship Date (EFSD)
MIC5891YN	August 19, 2016
MIC5891YWM	September 16, 2016
MIC5891YWM-TR	September 16, 2016



# QUALIFICATION REPORT RELIABILITY LABORATORY

PCN #: JAON-24FRFP480

Date January 22, 2016

Qualification of Microchip 8 inch Fabrication site for Micrel products manufactured with the BCDSi Process Technology.

PART NUMBER	PACKAGE TYPE	ASSEMBLY LOCATION	Wafer Size	PROCESS NAME
MIC2199YML	DFN44-12L	UNISEM IPOH MALAYSIA	8"	BCDSI

## **DIE QUALIFICATION RESULTS**

TEST DESCRIPTION	METHOD/CONDITIONS	DATE CODE	LOT ID.	168 HR Rej/ss	1000 HR Rej/ss	COMMENTS
HTOL	JESD22, Method 108	1540	V416.201815.210MQA	0/77	0/77	
High Temperature Operating Life Test	TA= + 125°C	1541	V416.201815.200MQA	0/77	0/77	
	VCC = +16V	1542	V416.201815.3000MQA	0/77	0/77	

### ELECTROSTATIC DISCHARGE

TEST DESCRIPTION	METHOD/CONDITIONS	DATE CODE	LOT ID.	STRESS	RESULT Rej/ss	COMMENTS
ESD-HBM	R= 1500 Ohms	1540	V416.201815.210MQA	+/-2000V	0/3	
Human Body Model ATE Test @ Room +25C	C= 100 pF 1X +/- Voltage					Note: ESD ratings are device specific. All Products qualified on the 8" Micrel process technologies will have the same or better
ESD-CDM  Charged Device Model  ATE Test @ Room +25C	JESD22-C101 1X +/- Voltage	1540	V416.201815.210MQA	+/-1500V	0/3	ESD and Latch-up performance as the 6" San Jose Products.
TEST DESCRIPTION	METHOD/CONDITIONS	DATE CODE	LOT ID.	STRESS	RESULT Rej/ss	COMMENTS
LATCH-UP	JESD-78	1540	V416.201815.210MQA	I/O LU O/V LU	0/6 0/6	
	TA = +25°C					

TEST DESCRIPTION	METHOD/CONDITIONS	DATE CODE	LOT ID.	Rej/ss	COMMENTS
Level 1 Pre-conditioning Flow	JESD22-A113	1540	V416.201815.210MQA	0/323	
		1541	V416.201815.200MQA	0/143	
		1542	V416.201815.3000MQA	0/145	
TEST DESCRIPTION	METHOD/CONDITIONS	DATE CODE	LOT ID.	<b>96 HR</b> Rej/ss	COMMENTS
PRESSURE POT	JESD22-A102	1540	V416.201815.210MQA	0/45	
With Level 1 Pre-	Ta = +121°C/100%RH	1541	V416.201815.200MQA	0/45	
Tpeak + 260°C 3X Reflow	15 PSIG	1542	V416.201815.3000MQA	0/45	
TEST DESCRIPTION	METHOD/CONDITIONS	DATE CODE	LOT ID.	<b>96 HR</b> Rej/ss	COMMENTS
HAST	JESD22-A110 (BIASED)	1540	V416.201815.210MQA	0/45	
With Level 1 Pre- conditioning	Ta= +130°C/85%RH	1541	V416.201815.200MQA	0/45	
Tpeak + 260°C 3X Reflow		1542	V416.201815.3000MQA	0/45	
		DATE		500 CY	
TEST DESCRIPTION	METHOD/CONDITIONS	DATE CODE	LOT ID.	Rej/ss	COMMENTS
	METHOD/CONDITIONS  JESD22-A104		LOT ID.  V416.201815.210MQA		COMMENTS
TEST DESCRIPTION  TEMP CYCLE  With Level 1 Pre- conditioning		CODE		Rej/ss	COMMENTS
TEMP CYCLE With Level 1 Pre- conditioning	JESD22-A104	1540	V416.201815.210MQA	Rej/ss 0/45	COMMENTS
TEMP CYCLE With Level 1 Pre- conditioning Tpeak + 260°C 3X Reflow	JESD22-A104	1540 1541	V416.201815.210MQA V416.201815.200MQA	Rej/ss 0/45 0/45	COMMENTS
TEMP CYCLE With Level 1 Pre-	JESD22-A104 Ta = -65°C / +150°C	1540 1541 1542 DATE	V416.201815.210MQA V416.201815.200MQA V416.201815.3000MQA	Rej/ss 0/45 0/45 0/45 1000 HR	