Product Change Notification - JAON-21DZUH494

Date: 30 Jul 2015

Product Category: Analog (Linear & Mixed Signal) AND Interface; 8-bit Microcontrollers; Analog (Thermal, Power Management &

Safety)

Notification subject: CCB 1708 Initial Notice: Qualification of CuPdAu bond wire and G700LS molding compound in selected

products of the 150K and 160K wafer technologies available in 28L SSOP package at ANAP assembly site.

Notification text: PCN Status:

Initial notification

Microchip Parts Affected:

Please open the attachments found in the attachments field below labeled as PCN_#_Affected_CPN.

NOTE: For your convenience Microchip includes identical files in two formats (.pdf and .xls).

Description of Change:

Qualification of palladium coated copper with gold flash (CuPdAu) bond wire and G700LS molding compound in selected products of the 150K and 160K wafer technologies available in 28L SSOP package at ANAP assembly site.

Pre Change:

Gold wire (Au) and G600 molding compound.

Post Change:

Palladium coated copper wire with gold flash (CuPdAu) and G700LS molding

compound.

Impacts to Data Sheet:

None

Reason for Change:

To improve manufacturability and qualify palladium coated copper with gold flash (CuPdAu) bond wire.

Change Implementation Status:

In Progress

Estimated First Ship Date:

November 15, 2015 (date code: 1546)

NOTE: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Markings to Distinguish Revised from Unrevised Devices:

Traceability code

Revision History:

July 30, 2015: Issued initial notification.

The change described in this PcN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

Attachment(s):

PCN JAON-21DZUH494 Qual Plan.pdf PCN JAON-21DZUH494 Affected CPN.pdf PCN JAON-21DZUH494 Affected CPN.xls

Please contact your local Microchip sales office with questions or concerns regarding this notification.

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To opt out of future offer or information emails (other than product change notification emails), click here to go to <u>microchipDIRECT</u> and login, then click on the "My account" link, click on "Update profile" and un-check the box that states "Future offers or information about Microchip's products or services."

PCN_JAON-21DZUH494
CATALOG_PART_NBR
HA1930-I/SS
HA1930T-I/SS
HA2030-I/SS
HA2030T-I/SS
HA4314-I/SS021
HA4315T-I/SS
HA4315T-I/SS022
MCP23016-I/SS
MCP23016T-I/SS
MCP23017-E/SS
MCP23017T-E/SS
MCP23S17-E/SS
MCP23S17T-E/SS
PIC16F570-E/SS
PIC16F570-I/SS
PIC16F570T-I/SS
PIC16F57-E/SS
PIC16F57-I/SS
PIC16F57T-E/SS
PIC16F57T-I/SS
PIC16F57T-I/SS024
PIC16F57T-I/SS025
PIC16F57T-I/SS027
PIC16F72-E/SS
PIC16F72-I/SS
PIC16F72-I/SSG
PIC16F72T-E/SS
PIC16F72T-I/SS
PIC16F737-I/SS
PIC16F737T-I/SS
PIC16F73-E/SS
PIC16F73-I/SS
PIC16F73T-E/SS
PIC16F73T-I/SS
PIC16F73T-I/SS059
PIC16F73T-I/SS155
PIC16F73T-I/SSC15
PIC16F767-E/SS
PIC16F767-I/SS
PIC16F767T-I/SS
PIC16F76-E/SS
PIC16F76-I/SS
PIC16F76-I/SS098
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PIC16F873AT-I/SS

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PIC16F876AT-I/SSG
PIC16F882-E/SS
PIC16F882-I/SS
PIC16F882T-E/SS
PIC16F882T-I/SS
PIC16F882T-I/SS026
PIC16F882T-I/SS029
PIC16F883-E/SS
PIC16F883-I/SS
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PIC16F883-I/SS021
PIC16F883-I/SS023
PIC16F883-I/SS032
PIC16F883-I/SS035
PIC16F883-I/SS037
PIC16F883-I/SS038
PIC16F883-I/SS045
PIC16F883-I/SS046
PIC16F883-I/SS062
PIC16F883-I/SS063
PIC16F883-I/SS064
PIC16F883-I/SSC02
PIC16F883T-E/SS
PIC16F883T-I/SS
PIC16F883T-I/SS022
PIC16F883T-I/SS032
PIC16F883T-I/SS062
PIC16F883T-I/SS063
PIC16F883T-I/SS064
PIC16F883T-I/SS070
PIC16F883T-I/SS071
PIC16F883T-I/SSC02
PIC16F886-E/SS
PIC16F886-I/SS
PIC16F886T-E/SS
PIC16F886T-I/SS
PIC16F886T-I/SS026
PIC16F886T-I/SS052
PIC16F913-E/SS
PIC16F913-I/SS
PIC16F913T-E/SS
PIC16F913T-I/SS
PIC16F913T-I/SS025
PIC16F913T-I/SS027
PIC16F913T-I/SS032



QUALIFICATION PLAN

PCN #: JAON-21DZUH494

Date: July 15, 2015

Qualification of palladium coated copper with gold flash (CuPdAu) bond wire and G700LS molding compound in selected products of the 150K and 160K wafer technologies available in 28L SSOP package at ANAP assembly site.

Distribution

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Purpose:								
	(CuPdAu) bond wire and G700LS molding compound in							
	selected products of the 150K and 160K wafer technologies available in 28L SSOP package at ANAP assembly site.							
MP code:	DE021TN2X024							
Part No.:	PIC16F57T-I/SS024							
CCB No .	No:BDM-000835B 3 No.:1708							
Package:								
Туре	28L SSOP							
Width or Size	0.209"							
Die thickness:	15							
Die size: 60.5x53.9								
MSL:	1							
Lead frame:								
Paddle size:	154x200							
Material	C194							
Surface	Ring Ag							
Treatment	Rough							
Process	etched							
Leadlock	yes							
Part Number	101383340							
Strip	OMLF							
Wire:								
Material	CuPdAu							
Die Attach Epoxy:								
Part Number	8290							
Conductive	Yes							
Mold Compound:								
Part Number	G700LS							
Lead finish:	matte tin							

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days	Special Instructions
Standard Pb-free Solderability	JESD22B-102E; Perform 8 hour steam aging for Matte tin finish and 1 hour steam aging for NiPdAu finish prior to testing. Standard Pb-free: Matte tin/ NiPdAu finish, SAC solder, wetting temp 245°C for both SMD & through hole packages.	22	5	1	27	> 95% lead coverage	5	Standard Pb-free solderability is the requirement. SnPb solderability (backward solderability- SMD reflow soldering) is required for any plating related changes and highly recommended for other package BOM changes.
Wire Bond Pull - WBP	Mil. Std. 883-2011	5	0	3	24	0 fails after TC	5	30 bonds from a minimum of 5 devices.
Wire Bond Shear - WBS	CDF-AEC-Q100-001	5	0	3	24	0	5	30 bonds from a minimum of 5 devices.
Wire Sweep		5	0	3	15	0		Required for any reduction in wire bond thickness.
Physical Dimensions	Measure per JESD22 B100 and B108	10	0	3	30	0	5	
External Visual	Mil. Std. 883-2009/2010	All devices prior to submission for qualification testing	0	3	ALL	0	5	
HTSL (High Temp Storage Life)	+175 C for 504 hours or 150°C for 1008 hrs. Electrical test pre and post stress at +25°C and hot temp.85oC, (1 lot to be tested at 125°C)	45	5	1	50	0	10	Must be in progress at time of package release to production, but completion is not required for release to production.
Preconditioning - Required for surface mount devices	+150°C Bake for 24 hours, moisture loading requirements per MSL level + 3X reflow at peak reflow temperature per Jedec-STD-020D for package type; Electrical test pre and post stress at +25°C. MSL1 @ 260°C	231	15	3	738	0	15	Spares should be properly identified. 77 parts from each lot to be used for HAST, Autoclave, Temp Cycle test.
HAST	+130°C/85% RH for 96/192 hours. Electrical test pre and post stress at +25°C and hot temp. (1 lot to be tested at 125°C)	77	5	3	246	0	10	Spares should be properly identified. Use the parts which have gone through Preconditioning.

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days	Special Instructions
Unbiased HAST	+130°C/85% RH for 96/192 hrs. Electrical test pre and post stress at +25°C	77	5	3	246	0	10	Spares should be properly identified. Use the parts which have gone through Preconditioning.
Temp Cycle	-65°C to +150°C for 500/1000 cycles. Electrical test pre and post stress at hot temp; 3 gram force WBP, on 5 devices from 1 lot, test following Temp Cycle stress. (1 lot to be tested at 125°C)	77	5	3	246	0	15	Spares should be properly identified. Use the parts which have gone through Preconditioning.