

# Product Change Notification - JAON-22WRZE808

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**Date:** 31 Oct 2014

**Product Category:** Touch Sensing Technologies; 16-bit Microcontrollers and Digital Signal Controllers; Analog (Linear & Mixed Signal) AND Interface; Supertex; Analog (Thermal, Power Management & Safety); SMSC; 8-bit Microcontrollers; 32-bit PIC Microcontrollers; Radio Frequency Devices

**Notification subject:** CCB 1476 Initial Notice: Qualification of G700LTD Mold Compound and 8600 Die Attach in various QFN packages using EFTC64T LF at NSEB (UTL).

**Notification text:**

**PCN Status:**

Initial notification

**Microchip Parts Affected:**

See attachments of affected catalog part numbers (CPN) labeled as...

PCN\_JAON-22WRZE808\_Affected\_CPN.xls

PCN\_JAON-22WRZE808\_Affected\_CPN.pdf

**Description of Change:**

Qualification of G700LTD Mold Compound and 8600 Die Attach in various QFN packages using EFTC64T LF at NSEB (UTL).

**Pre Change:**

G770HCD mold compound and 8006NS or 8200T die attach

**Post Change:**

G700LTD mold compound and 8600 die attach

**Impacts to Data Sheet:**

None

**Reason for Change:**

To Improve Manufacturability by qualifying G700LTD mold compound and 8600 die attach at this assembly site.

**Change Implementation Status:**

In Progress

**Estimated First Ship Date:**

February 28, 2015 (date code: 1509)

NOTE: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

**Markings to Distinguish Revised from Unrevised Devices:**

Traceability code

**Revision History:**

**October 31, 2014:** Issued initial notification.

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

**Attachment(s):**

[PCN\\_JAON-22WRZE808\\_Qual Plan.pdf](#) [PCN\\_JAON-22WRZE808\\_Affected\\_CPN.pdf](#) [PCN\\_JAON-22WRZE808\\_Affected\\_CPN.xlsx](#)

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PCN_JAON-22WRZE808
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PIC32MX764F128HT-V/MR
PIC32MX764F128H-V/MR
PIC32MX775F256H-80I/MR
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PIC32MX795F512HT-80I/MR
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PIC32MZ1024ECG064T-I/MR
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RNG2C547T-I/NQ



**MICROCHIP**

# **QUALIFICATION PLAN**

**PCN#: JAON-22WRZE808**

**Date**  
**October 22, 2014**

**Qualification of G700LTD Mold Compound and 8600 Die  
Attach in various QFN packages using EFTC64T LF at NSEB  
(UTL).**

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**Purpose:** \_\_\_\_\_ Qualification of G700LTD Mold Compound and 8600 Die Attach in various QFN packages using EFTC64T LF at NSEB (UTL).

**MP code:** \_\_\_\_\_ XGA01TY5XA00

**Part No.:** \_\_\_\_\_ RNG2C547T-I/NQ

**BD No:** \_\_\_\_\_ BDM-000664\_A

**PSI mask:** \_\_\_\_\_ XGA1

**Sub-con suggestion:** \_\_\_\_\_ MSL1

**CCB No.:** \_\_\_\_\_ 1476

**Package:**

**Type** \_\_\_\_\_ 72 pin VQFN

**Width or Size** \_\_\_\_\_ 10x10x0.9mm

**Die thickness:** \_\_\_\_\_ 11

**Die size:** \_\_\_\_\_ 200.2x251.4

**Lead frame:**

**Paddle size:** \_\_\_\_\_ 330x330

**Material** \_\_\_\_\_ Eftek64T

**Surface** \_\_\_\_\_ Double ring

**Process** \_\_\_\_\_ Etched

**Lead Lock** \_\_\_\_\_ Yes

**Part Number** \_\_\_\_\_ FR0715

**Treatment** \_\_\_\_\_ none

**Wire:**

**Material** \_\_\_\_\_ Au

**Die Attach Material:**

**Part Number** \_\_\_\_\_ 8600

**Conductive** \_\_\_\_\_ yes

**Molding Compound:** \_\_\_\_\_ G700LTD

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days	Special Instructions
Wire Bond Pull - WBP	Mil. Std. 883-2011	5	0	3	24	0 fails after TC	5	30 bonds from a minimum of 5 devices.
Wire Bond Shear - WBS	CDF-AEC-Q100-001	5	0	3	24	0	5	30 bonds from a minimum of 5 devices.
Wire Sweep		5	0	3	15	0		Required for any reduction in wire bond thickness.
Physical Dimensions	Measure per JESD22 B100 and B108	10	0	3	30	0		
External Visual	Mil. Std. 883-2009/2010	All devices prior to submission for qualification testing	0	3	ALL	0	5	
HTSL (High Temp Storage Life)	+175 C for 504 hours or 150°C for 1008 hrs. Electrical test pre and post stress at +25°C and hot temp.85°C	45	5	3	150	0	10	Must be in progress at time of package release to production, but completion is not required for release to production.
Preconditioning - Required for surface mount devices	+150°C Bake for 24 hours, moisture loading requirements per MSL level + 3X reflow at peak reflow temperature per Jedec-STD-020D for package type; Electrical test pre and post stress at at +25°C. Perform SAM analysis using the standard sample size.  MSL1 @ 260°C	231	15	3	738	0	15	Spares should be properly identified. 77 parts from each lot to be used for HAST, Autoclave, Temp Cycle test.
HAST	+130°C/85% RH for 96 hours. Electrical test pre and post stress at at +25°C and hot temp. 85°C	77	5	3	246	0	10	Spares should be properly identified. Use the parts which have gone through Pre-conditioning.
Unbiased HAST	+130°C/85% RH for 96 hrs Electrical Test pre and post stress at +25° C and hot temp. 85°C	77	5	3	246	0	10	Spares should be properly identified. Use the parts which have gone through Pre-conditioning.

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days	Special Instructions
Temp Cycle	-65°C to +150°C for 500 cycles. Electrical test pre and post stress at +25°C and hot temp85°C, 3 gram force WBP, on 5 devices from 1 lot, test following Temp Cycle stress.	77	5	3	246	0	15	Spares should be properly identified. Use the parts which have gone through Pre-conditioning.