Compliant with IEC 62474/ D9.00

| MICROCHIP  Semiconductor Device Type: AT / AV (B8X) 005 TO-220 Matte Tin   |   |  | Termination Base Alloy:<br>Copper Alloy (Cu)   |   |   | Package Homogeneous Materials:<br>8.1 Electronics (e.g. pc boards, displays) |   |  |  | JEDEC 97<br>Product Marking<br>and/or Pkg.<br>Labeling |
|--|---|--|--|---|---|--|---|--|--|--|
| Semiconductor Device   | Type: AT/AV   |  |  |   |   |  |   |  |  | e3   |
| Basis Substance  | CACAlumban  | "Contained In" Sub-Component   | % lotal<br>Weight  |   |   | 526.92   | (mg) Total  | Mold Compound  | % ot Total Weight  | 26.56  |
| Basic Substance  | CAS Number  | •  | 23.373   | mg/part   | ppm   |  |   |  |  | T  |
| Fused Silica Epoxy Resin 1   | 60676-86-0<br>Trade Secret  | Mold Compound  Mold Compound   | 0.863  | 463.693<br>17.125   | 233,728<br>8.632  |  | Fused Silica<br>Epoxy Resin 1                     | 60676-86-0<br>Trade Secret   | 88.00<br>3.25  |  |
| Epoxy Resin 2  | Trade Secret  | Mold Compound  | 0.863  | 15.808  | 7.968   |  | Epoxy Resin 2                                     | Trade Secret   | 3.00   |  |
| Phenol Resin   | Trade Secret  | Mold Compound  | 1.195  | 23.712  | 11,952  |  | Phenol Resin                                      | Trade Secret   | 4.50   |  |
| Carbon Black   | 1333-86-4   | Mold Compound  | 0.066  | 1.317   | 664   |  | Carbon Black                                      | 1333-86-4  | 0.25   |  |
| Misc.  | Trade Secret  | Mold Compound  | 0.266  | 5.269   | 2.656   |  | Undeclared  | Trade Secret   | 1.00   |  |
| Copper   | 7440-50-8   | Lead Frame   | 70.627   | 1401.171  | 706.271   |  | Ondooiarod  | Total  | 100.00   | ı  |
| Tin  | 7440-31-5   | Lead Frame   | 0.119  | 2.361   | 1,190   | 1430.79  | (mg) Total  | Lead Frame   | % of Total Weight  | 72.12  |
| Silver   | 7440-22-4   | Lead Frame   | 1.374  | 27.257  | 13.739  |  | Copper  | 7440-50-8  | 97.93  | 1  |
| Silver (Aa)  | 7440-22-4   | Die Attach   | 0.071  | 1.402   | 707   |  | Tin   | 7440-31-5  | 0.17   |  |
| Proprietary Resin  | Trade Secret  | Die Attach   | 0.017  | 0.330   | 167   |  | Silver  | 7440-22-4  | 1.91   |  |
| Proprietary Curing agent & Hardener  | Trade Secret  | Die Attach   | 0.003  | 0.054   | 27  | '  |   | Total  | 100.00   | <u> 1</u>  |
| Silicon  | 7440-21-3   | Chip (Die)   | 0.620  | 12.300  | 6,200   | 1.79   | (mg) Total  | Die Attach   | % of Total Weight  | 0.09   |
| Gold   | 7440-57-5   | Wire Bond  | 0.040  | 0.794   | 400   | 1.73   | Silver (Ag)                                       | 7440-22-4  | 78.50  | 0.03   |
| Tin  | 7440-31-5   | Plating on external leads (pins) - Matte Tin / annealed at 150°C for 1 hour  | 0.570  | 11.308  | 5.700   |  | Proprietary Resin                                 | Trade Secret   | 18.50  |  |
|  | 7110010   | TOTALS:  | 100.000  | 1,983.900   | 1,000,000   | Proprietan   | Curing agent & Hardene                            |  | 3.00   |  |
|  | 1 0020  | g Total Mass   |  | ,   | ,,  |  |   | Total  | 100.00   | 1  |
| and 2002/53/EC (End-of-Life Vehicles (ELV) without execution of the second sec   | mption (zero)<br>ia internal design contro  | •  | ·  |   |   | 12.30  | Total (mg)  Doped Silicon                         | Chip (Die) 7440-21-3 Total   | % of Total Weight  | 0.62   |
| and 2002/53/EC (End-of-Life Vehicles (ELV) without exer-<br>bliance with the above EU Directives has been verified vi<br>nemical substance is absent from the list above, the che<br>porated's knowledge and belief as of the date of this doo   | mption (zero)  ia internal design contro  mical substance is NOT  cument, there is no credi   | ls, supplier declarations, and /or analytical test data. an intentional ingredient in the semiconductor device and ble reason to believe that the unavoidable impurity conce   | I, to the best of  | Microchip Tec   | hnology   | 12.30  | ,   | 7440-21-3  | 100.00   | 0.62   |
| and 2002/53/EC (End-of-Life Vehicles (ELV) without exer-<br>liance with the above EU Directives has been verified vi<br>emical substance is absent from the list above, the cher<br>porated's knowledge and belief as of the date of this doc<br>is not below the threshold of regulatory concern for any<br>ing compounds used by Microchip meet the UL94 V0 flar   | mption (zero) ia internal design contro mical substance is NOT cument, there is no credi regulatory scheme work mmability standard for p  | ls, supplier declarations, and /or analytical test data. an intentional ingredient in the semiconductor device and ble reason to believe that the unavoidable impurity conce   | I, to the best of<br>entration of the  | Microchip Tec<br>chemical subs  | hnology   | 0.79   | ,   | 7440-21-3  | 100.00   |  |
| and 2002/53/EC (End-of-Life Vehicles (ELV) without exei-<br>liance with the above EU Directives has been verified vi<br>emical substance is absent from the list above, the che-<br>iorated's knowledge and belief as of the date of this doc<br>not below the threshold of regulatory concern for any<br>ng compounds used by Microchip meet the UL94 V0 flar<br>ul.com/global/eng/pages/offerings/industries/chemicals<br>otective "tubes" in which the specific product is shippy   | mption (zero) ia internal design contro mical substance is NOT zument, there is no credi regulatory scheme work mmability standard for p /plastics/   | Is, supplier declarations, and /or analytical test data. an intentional ingredient in the semiconductor device and ble reason to believe that the unavoidable impurity conce 1-wide.   | I, to the best of<br>intration of the  | Microchip Tec<br>chemical subs<br>report at   | chnology<br>tance, if   |  | Doped Silicon                                     | 7440-21-3<br>Total   | 100.00   |  |
| and 2002/53/EC (End-of-Life Vehicles (ELV) without exer- pliance with the above EU Directives has been verified vinterical substance is absent from the list above, the che- porated's knowledge and belief as of the date of this doct is not below the threshold of regulatory concern for any ing compounds used by Microchip meet the UL94 V0 flat if (Licom/global/eng/pages/offerings/industries/chemicals in treels" may be made from PVC plastic.  Chip Technology Incorporated believes the information priginal packing materials is true and correct to the best leteness and accuracy of data in this form because it ha nation is often protected from disclosure as trade secret ded only as estimates of the average weight of these par  | mption (zero) is internal design contro mical substance is NOT cument, there is no credi regulatory scheme work mmability standard for p //plastics/ ed are made from polyvi in this form concerning of its knowledge and be is been compiled based t is and some information tts and the average weige   | Is, supplier declarations, and /or analytical test data.  an intentional ingredient in the semiconductor device and ble reason to believe that the unavoidable impurity concet-wide.  lastics. You can access the UL iQTM family of databases the strength of the inverse of the inv | I, to the best of<br>intration of the<br>to obtain a test<br>old the packing<br>incorporated's<br>gy Incorporate<br>vided by raw m<br>and raw mater  | Microchip Tec<br>chemical subs<br>report at<br>g slip on the ou<br>semiconductor<br>d cannot guara<br>naterial supplie<br>lal suppliers. In   | thnology<br>tance, if<br>atter box and<br>r devices in<br>intee the<br>rrs. Supplier<br>formation is  |  | Doped Silicon  (mg) Total                         | 7440-21-3 Total Wire Bond  | 100.00<br>100.00<br>% of Total Weight                    |  |
| and 2002/53/EC (End-of-Life Vehicles (ELV) without execution with the above EU Directives has been verified violence with the above EU Directives has been verified violenced's knowledge and belief as of the date of this doc not below the threshold of regulatory concern for anying compounds used by Microchip meet the UL94 V0 flar (vul.com/global/eng/pages/offerings/industries/chemicals rotective "tubes" in which the specific product is shippen "reeis" may be made from PVC plastic.  Chip Technology Incorporated believes the information or included in the pestilence sand accuracy of data in this form because it hanation is often protected from disclosure as trade secreted only as estimates of the average weight of these parants, metals, and non-metal materials contained within chip Technology Incorporated does not provide any wantees provided by Microchip Technology Incorporated a   | mption (zero) ia internal design contro mical substance is NOT tument, there is no credi regulatory scheme work mmability standard for p //plastics/ ed are made from polyvi in this form concerning of its knowledge and be is been compiled based it sand some information tts and the average weig silicon devices (silicon rranty, express or implie  | Is, supplier declarations, and /or analytical test data.  an intentional ingredient in the semiconductor device and ble reason to believe that the unavoidable impurity concet-wide.  lastics. You can access the UL iQTM family of databases the strength of the inverse of the inv | I, to the best of intration of the to obtain a test old the packing incorporated's gy Incorporate vided by raw mater and raw mater see estimates outlook.  | Microchip Tec<br>chemical subs<br>report at<br>g slip on the ou<br>semiconductor<br>d cannot guara<br>aterial supplie<br>ial suppliers. In<br>lo not include to   | thnology<br>tance, if<br>atter box and<br>r devices in<br>intee the<br>rs. Supplier<br>formation is<br>trace levels   |  | Doped Silicon  (mg) Total                         | 7440-21-3  Total  Wire Bond  7440-57-5   | 100.00<br>100.00<br>% of Total Weight                    | 0.04   |
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