Compliant with IEC 62474/ D9.00

Compliant to IEC 61249-2-21:2003

MICROCHIP Semiconductor Device Type: 8CA 007 SPAK MatteTin			Termination Base Alloy: Copper Alloy (Cu)			Package Homogeneous Materials: 8.1 Electronics (e.g. pc boards, displays)				J-STD-609A Product Marking and/or Pkg. Labeling e3
~		"Contained In"				348.16	(mg) Total	Mold Compound	% ot Total Weight	64.26
Basic Substance	CAS Number	Sub-Component	% Total Weight	mg/part	ppm	340.10		· ·		04.20
Fused Silica	60676-86-0	Mold Compound	55.546	300.950	555,463		Fused Silica	60676-86-0	86.44	
Epoxy Resin	Trade Secret	Mold Compound	5.109	27.679	51,087		Epoxy Resin	Trade Secret	7.95	
Phenol Resin Carbon Black	Trade Secret 1333-86-4	Mold Compound Mold Compound	3.509 0.096	19.010 0.522	35,086 964		Phenol Resin Carbon Black	Trade Secret 1333-86-4	5.46 0.15	
	7440-50-8	Lead Frame	33.080	179.229	330.803		Caldon black	1333-66-4 Total	100.00	
Copper Silver	7440-50-8	Lead Frame	0.166	0.902	1.665	180.37	(mg) Total	Lead Frame	% of Total Weight	
Iron	7440-22-4	Lead Frame	0.166	0.902	333	180.37		7440-50-8	% of Total Weight	33.29
Phosphorus	7723-14-0	Lead Frame	0.033	0.180	100		Copper Silver	7440-50-8	0.50	
Lead	7439-92-1	Die Attach	0.010	4.036	7.449		Iron	7440-22-4	0.50	
Silver	7440-22-4	Die Attach	0.020	0.106	195		Phosphorus	7723-14-0	0.03	
Tin	7440-31-5	Die Attach	0.020	0.106	156		1 Hospitorus	Total	100.00	
Copper	7440-50-8	Wire Bond 1	0.110	0.596	1,100	4.23	(mg) Total	Die Attach	% of Total Weight	
Silicon	7440-21-3	Chip (Die)	1.350	7.314	13,500	4.23	Lead	7439-92-1	95.50	0.76
Gold	7440-57-5	Wire Bond 2	0.010	0.054	100		Silver	7440-22-4	2.50	
Tin	7440-31-5	Plating on external leads (pins) - Matte Tin / annealed at 150°C for 1 hour	0.200	1.084	2,000		Tin	7440-31-5	2.00	
		TOTALS:	100.000	541.800	1,000,000			Total	100.00)
	0.5418	g Total Mass				0.60	(mg) Total	Wire Bond 1	% of Total Weight	t 0.11
and 200200120 (2110 of Life verifoles (LLV) using E0-Norto applicate	on exemption /(a): Lead in high melting temperature type solders (i.e. lead	d-based alloys conta	aining 85% by	weight or more		Copper	7440-50-8	100.00	
ead. Compliance with the above EU Directives has been verified via interr f a chemical substance is absent from the list above, the chemical s	nal design contro	ols, supplier declarations, and /or analytical test data.	d, to the best of Mic	rochip Techn	ology			Total	100.00	
ead. Compliance with the above EU Directives has been verified via interr	nal design contro ubstance is NOT	ols, supplier declarations, and /or analytical test data.	d, to the best of Mic	rochip Techn	ology	7.31	Copper Total (mg)	Total Chip (Die)		
ead. Compliance with the above EU Directives has been verified via interr f a chemical substance is absent from the list above, the chemical s ncorporated's knowledge and belief as of the date of this document	nal design contro ubstance is NOT , there is no crec e world-wide.	ols, supplier declarations, and /or analytical test data. an intentional ingredient in the semiconductor device an lible reason to believe that the unavoidable impurity conc	d, to the best of Mic entration of the che	rochip Techn mical substan	ology	7.31		Total	100.00 % of Total Weight	t 1.35
ead. Compliance with the above EU Directives has been verified via interr f a chemical substance is absent from the list above, the chemical s neorporated's knowledge and belief as of the date of this document below the threshold of regulatory concern for any regulatory scheme	nal design contro ubstance is NOT , there is no crec e world-wide. ity standard for	ols, supplier declarations, and /or analytical test data. an intentional ingredient in the semiconductor device an lible reason to believe that the unavoidable impurity concolastics. You can access the UL iQTM family of databases	d, to the best of Mic entration of the che to obtain a test rep	rochip Techn mical substan	ology ce, if any, is not	7.31	Total (mg)	Total Chip (Die)	100.00 % of Total Weight	t 1.35
ead. Compliance with the above EU Directives has been verified via interref a chemical substance is absent from the list above, the chemical s ncorporated's knowledge and belief as of the date of this document below the threshold of regulatory concern for any regulatory scheme Molding compounds used by Microchip meet the UL94 V0 flammabil (The protective "tubes" in which the specific product is shipped are refered.	nal design control ubstance is NOT , there is no cree world-wide. ity standard for made from polyv form concerning edge and belief, ad on the ranges y not have been ipated significan	ols, supplier declarations, and /or analytical test data. an intentional ingredient in the semiconductor device an lible reason to believe that the unavoidable impurity concuplastics. You can access the UL iQTM family of databases inyl chloride (PVC) plastic. "Window envelopes" used to I substances restricted by RoHS in Microchip Technology as of the date listed in this form. Microchip Technology in provided in Material Safety Data Sheets provided by raw provided by subcontract assemblers and raw material su	d, to the best of Mic entration of the che to obtain a test rep hold the packing slip Incorporated's sem ncorporated cannot material suppliers. S ppliers. Information	rochip Techn mical substan ort at o on the outer iconductor de guarantee the supplier inforr is provided o	ology ice, if any, is not box and certain evices in their completeness nation is often nly as estimates	7.31	Total (mg)	Total Chip (Die) 7440-21-3	100.00 % of Total Weight	1.35
compliance with the above EU Directives has been verified via interr f a chemical substance is absent from the list above, the chemical s normards are nowledge and belief as of the date of this document below the threshold of regulatory concern for any regulatory scheme dolding compounds used by Microchip meet the UL94 V0 flammabil the protective "tubes" in which the specific product is shipped are refreels" may be made from PVC plastic. Microchip Technology Incorporated believes the information in this priginal packing materials is true and correct to the best of its knowl and accuracy of data in this form because it has been compiled base protected from disclosure as trade secrets and some information may fit the average weight of these parts and the average weight of antici	nal design control ubstance is NOT , there is no crec world-wide. ity standard for made from polyv form concerning ledge and belief, ad on the ranges ly not have been lyated significan lyarts. express or impli	ols, supplier declarations, and /or analytical test data. an intentional ingredient in the semiconductor device an lible reason to believe that the unavoidable impurity concuplastics. You can access the UL iQTM family of databases inyl chloride (PVC) plastic. "Window envelopes" used to l substances restricted by RoHS in Microchip Technology as of the date listed in this form. Microchip Technology in provided in Material Safety Data Sheets provided by raw provided by subcontract assemblers and raw material su t toxic metals components. These estimates do not included, with respect to the information provided in this declared.	d, to the best of Mic entration of the che to obtain a test rep hold the packing slip Incorporated's sem ncorporated cannot material suppliers. S ppliers. Information le trace levels of dop	rochip Techn mical substan ort at o on the outer iconductor de guarantee the supplier inforr is provided o pants, metals,	ology ice, if any, is not box and certain evices in their completeness mation is often nly as estimates and non-metal uct warranties		Total (mg) Doped Silicon	Total Chip (Die) 7440-21-3 Total	100.00 % of Total Weight 100.00 100.00	1.35
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compliance with the above EU Directives has been verified via interr if a chemical substance is absent from the list above, the chemical s incorporated's knowledge and belief as of the date of this documen below the threshold of regulatory concern for any regulatory scheme Molding compounds used by Microchip meet the UL94 V0 flammabil the protective "tubes" in which the specific product is shipped are reles!" may be made from PVC plastic. Microchip Technology Incorporated believes the information in this original packing materials is true and correct to the best of its knowl and accuracy of data in this form because it has been compiled base protected from disclosure as trade secrets and some information may fit he average weight of these parts and the average weight of anticimaterials contained within silicon devices (silicon IC) in the finished Microchip Technology Incorporated does not provide any warranty, provided by Microchip Technology Incorporated and its subsidiaries order acknowledgement, and invoices. Microchip disclaims any duty to notify users of updates or changes suffered by users or third parties as a result of the users' reliance on of Compliance for semiconductor products.	nal design controvable in the control of the contro	ols, supplier declarations, and /or analytical test data. an intentional ingredient in the semiconductor device an lible reason to believe that the unavoidable impurity concolastics. You can access the UL iQTM family of databases inyl chloride (PVC) plastic. "Window envelopes" used to lead to the date listed in this form. Microchip Technology as of the date listed in this form. Microchip Technology in Material Safety Data Sheets provided by raw provided by subcontract assemblers and raw material sut toxic metals components. These estimates do not included, with respect to the information provided in this declar in Microchip's standard terms and conditions of sale. These ent Declarations and shall not be liable for any damages, in Material Content Declarations (MCD) or independent the	d, to the best of Mic entration of the che to obtain a test rep hold the packing slip Incorporated's sem ncorporated cannot material suppliers. S ppliers. Information le trace levels of dop ation. The exclusive se are provided in M direct or indirect, co	rochip Techn mical substan ort at o on the outer iconductor de guarantee the jupplier inforr is provided o pants, metals, , limited prod icrochip's que nsequential o	ology cce, if any, is not box and certain evices in their completeness nation is often nly as estimates and non-metal uct warranties otations, sales r otherwise,	0.05	Total (mg) Doped Silicon (mg) Total Gold	Total Chip (Die) 7440-21-3 Total Wire Bond 2 7440-57-5 Total Plating on external leads (pins) - Matte Tin / annealed at 150°C for	100.00 % of Total Weight 100.00 100.00 % of Total Weight 100.00	0.01

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