Compliant with IEC 62474/ D9.00

MICROCHIP				Termination Base Alloy: Copper Alloy (Cu)			Package Homogeneous Materials: 8.1 Electronics (e.g. pc boards, displays)			
Semiconductor Device Type:	os	(L9X) 005 TSOT Matte Tin								e3
Basic Substance	CAS Number	"Contained In" Sub-Component	% Total Weight	mg/part	ppm	7.99	(mg) Total	Mold Compound	% ot Total Weight	62.42
Silica, vitreous	60676-86-0	Mold Compound	53.057	6.791	530.570		Silica, vitreous	60676-86-0	85.00	
Epoxy Resin (No bromine, No diantimony trioxide)	Trade Secret	Mold Compound	3.823	0.489	38,232		Epoxy Resin	Trade Secret	6.13	
Phenolic Resin (No Br / CL SbO3, No diantimony trioxide)	Trade Secret	Mold Compound	3.823	0.489	38,232		Phenolic Resin	Trade Secret	6.13	
Epoxy, Cresol Novolac	29690-82-2	Mold Compound	1.529	0.196	15,293		Epoxy, Cresol Novolac	29690-82-2	2.45	
Carbon Black	1333-86-4	Mold Compound	0.187	0.024	1,873		Carbon Black	1333-86-4	0.30	
Copper	7440-50-8	Lead Frame	25.585	3.275	255,849			Total		•
Iron	7439-89-6	Lead Frame	0.629	0.081	6,293	3.43	(mg) Total	Lead Frame	% of Total Weight	26.78
Silver	7440-22-4	Lead Frame	0.510	0.065	5,102		Copper	7440-50-8	95.54	
Zinc	7440-66-6	Lead Frame	0.033	0.004	335		Iron	7439-89-6	2.35	
Phosphorous	7723-14-0	Lead Frame	0.022	0.003	221		Silver	7440-22-4	1.91	
Silver (Ag)	7440-22-4 Trade Secret	Die Attach Die Attach	1.531 0.361	0.196 0.046	15,308 3,608		Zinc	7440-66-6 7723-14-0	0.13 0.08	
Proprietary Resin	Trade Secret	Die Attach	0.059	0.046	585		Phosphorous	7723-14-0 Total		
Proprietary Curing agent & Hardener Silicon	7440-21-3		5.340	0.684	53,400	0.25	/ \ <del>-</del>			4.05
Silicon	7440-21-3	Chip (Die)	5.340	0.004	53,400	0.25	(mg) Total	Die Attach	% of Total Weight	1.95
Gold	7440-57-5	Wire Bond	0.400	0.051	4.000		Silver (Aa)	7440-22-4	78.50	
Tin		Plating on external leads (pins) - Matte Tin / annealed at 150°C for 1 hour	3.110	0.398	31.100		Proprietary Resin	Trade Secret	18.50	
		TOTALS:	100.000	12.800	1,000,000	Proprieta	ry Curing agent & Hardene		3.00	
	0.0128	a Total Mass						Total	100.00	J
		s, supplier declarations, and /or analytical test data.					Doped Silicon	7440-21-3 Total	100.00	
chemical substance is absent from the list above, the chemical su prporated's knowledge and belief as of the date of this document, is not below the threshold of regulatory concern for any regulato	there is no credi	an intentional ingredient in the semiconductor device and ble reason to believe that the unavoidable impurity conce					Doped Silicon			
prporated's knowledge and belief as of the date of this document,	there is no credi ory scheme world ity standard for p	an intentional ingredient in the semiconductor device and ble reason to believe that the unavoidable impurity conce I-wide.	ntration of the	chemical subs		0.05	Doped Silicon  (mg) Total			0.4
rporated's knowledge and belief as of the date of this document, is not below the threshold of regulatory concern for any regulato ding compounds used by Microchip meet the UL94 V0 flammabilit	there is no credi ory scheme world ity standard for p	an intentional ingredient in the semiconductor device and ble reason to believe that the unavoidable impurity conce devide.  I-wide.  Iastics. You can access the UL iQTM family of databases to the control of the cont	ntration of the	chemical subs	stance, if	0.05		Total	100.00	0.4
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proprated's knowledge and belief as of the date of this document, is not below the threshold of regulatory concern for any regulator ding compounds used by Microchip meet the UL94 V0 flammabilities//ul.com/global/eng/pages/offerings/industries/chemicals/plastics/protective "tubes" in which the specific product is shipped are main "reels" may be made from PVC plastic.  Tochip Technology Incorporated believes the information in this for original packing materials is true and correct to the best of its knipleteness and accuracy of data in this form because it has been cornation is often protected from disclosure as trade secrets and solded only as estimates of the average weight of these parts and tided only as estimates of the average weight of these parts and tided only as estimates of the average weight of these parts and tided only as estimates of the average weight of these parts and tided only as estimates of the average weight of these parts and tided only as estimates of the average weight of these parts and tides.	there is no credi ory scheme work ity standard for p s/ nade from polyvir orm concerning : nowledge and be compiled based o ome information the average weigl devices (silicon i	ble reason to believe that the unavoidable impurity concell-wide.  Ilastics. You can access the UL iQTM family of databases the strict of the substances restricted by RoHS in Microchip Technology lilef, as of the date listed in this form. Microchip Technology In the ranges provided in Material Safety Data Sheets promay not have been provided by subcontract assemblers int of anticipated significant toxic metals components. The C) in the finished parts.	ntration of the to obtain a test old the packing ncorporated's gy Incorporate vided by raw and raw materiese estimates of the column.	report at  y slip on the out semiconducto d cannot guara- taterial supplie al suppliers. Ir o not include	iter box and r devices in intee the rrs. Supplier iformation is trace levels	0.05	(mg) Total	Wire Bond 7440-57-5	100.00 % of Total Weight	3.11
proprated's knowledge and belief as of the date of this document, is not below the threshold of regulatory concern for any regulator concern for any regulator ding compounds used by Microchip meet the UL94 V0 flammabilities://ul.com/global/eng/pages/offerings/industries/chemicals/plastics protective "tubes" in which the specific product is shipped are main "reels" may be made from PVC plastic.  To compare the concern and correct to the best of its knowledge of the concern and correct to the best of its knowledge of the correct to the best of its knowledge of the correct to the concern and correct to the best of its knowledge of the correct to the correct to the best of its knowledge of the correct to the best of its knowledge of the correct to the correct	there is no credi ory scheme work ity standard for p s/ nade from polyvin orm concerning a nowledge and be compiled based o come information the average weigh devices (silicon l express or implies ubsidiaries are co	an intentional ingredient in the semiconductor device and ble reason to believe that the unavoidable impurity concell-wide.  lastics. You can access the UL iQTM family of databases the plant of the pl	ntration of the to obtain a test old the packing ncorporated's: gy Incorporate vided by raw and raw materi ase estimates d tion. The exclu sale. These are	report at  semiconducto d cannot guara aterial supplie al suppliers. Ir to not include sive, limited pp provided in N , consequentia	atter box and r devices in intee the res. Supplier information is trace levels roduct licrochip's		(mg) Total  Doped Gold	Total  Wire Bond  7440-57-5  Total  Plating on external leads (pins) - Matte Tin / annealed at 150°C for 1	100.00 % of Total Weight 100.00	

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