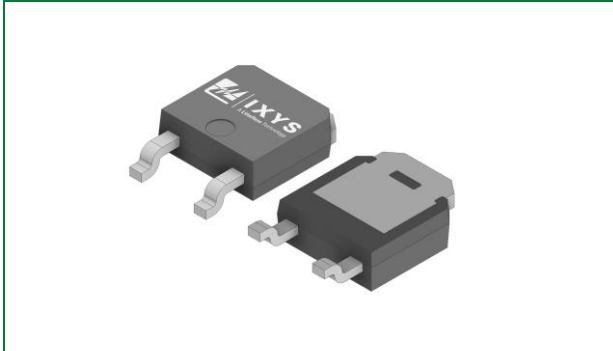


## LSIC2SD065C20A 650 V, 20 A SiC Schottky Barrier Diode

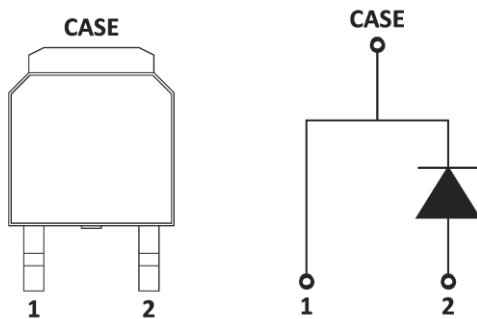


### Agency Approvals and Environmental

Environmental Approvals



### Circuit Diagram TO-252-2L



### Product Summary

Characteristic	Value	Unit
$V_{RRM}$	650	V
$I_F (T_C \leq 135^\circ\text{C})$	22	A
$Q_c (V_R: 0 - 400\text{ V})$	63	nC

### Features

- AEC-Q101 qualified
- MSL 1 Rated
- Positive temperature coefficient for safe operation and ease of paralleling
- 175 °C maximum operating junction temperature
- Excellent surge capability
- Extremely fast, temperature-independent switching behavior
- Dramatically reduced switching losses compared to Si bipolar diodes
- RoHS compliant, lead-free, and halogen-free

### Applications

- Boost diodes in PFC or DC/DC stages
- Switch-mode power supplies
- Solar inverters
- Uninterruptable power supplies
- Industrial motor drives
- Battery chargers
- High speed rectifier

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## 1. Maximum Ratings

Characteristic	Symbol	Conditions	Value	Unit
Repetitive Peak Reverse Voltage	$V_{RRM}$	-	650	V
DC Blocking Voltage	$V_R$	-	650	V
Continuous Forward Current	$I_F$	$T_C = 25\text{ }^\circ\text{C}$	48	A
		$T_C = 135\text{ }^\circ\text{C}$	22	
		$T_C = 140\text{ }^\circ\text{C}$	20	
Non-repetitive Forward Surge Current	$I_{FSM}$	$T_C = 25\text{ }^\circ\text{C}$ , $t_p = 10\text{ ms}$ , Half sine pulse	90	A
$I^2t$	$\int I^2 dt$	$T_C = 25\text{ }^\circ\text{C}$ , $t_p = 10\text{ ms}$ , Half sine pulse	40.5	A <sup>2</sup> s
Power Dissipation	$P_{Tot}$	$T_C = 25\text{ }^\circ\text{C}$	150	W
		$T_C = 110\text{ }^\circ\text{C}$	65	
Operating Junction Temperature	$T_J$	-	-55 to 175	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-	-55 to 150	$^\circ\text{C}$
Lead Temperature for Soldering (MSL 1 Rated)	$T_{SOLD}$	-	260	$^\circ\text{C}$

## 2. Thermal Characteristics

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance	$R_{thJC, max}$	1.0	$^\circ\text{C}/\text{W}$

## 3. Electrical Characteristics

Characteristic	Symbol	Conditions	Value			Unit
			Min	Typ	Max	
Forward Voltage	$V_F$	$I_F = 20\text{ A}$ , $T_J = 25\text{ }^\circ\text{C}$	-	1.5	1.8	V
		$I_F = 20\text{ A}$ , $T_J = 175\text{ }^\circ\text{C}$	-	1.7	-	
Reverse Current	$I_R$	$V_R = 650\text{ V}$ , $T_J = 25\text{ }^\circ\text{C}$	-	3	100	$\mu\text{A}$
		$V_R = 650\text{ V}$ , $T_J = 175\text{ }^\circ\text{C}$	-	70	-	
Total Capacitance	C	$V_R = 1\text{ V}$ , $f = 1\text{ MHz}$	-	960	-	pF
		$V_R = 200\text{ V}$ , $f = 1\text{ MHz}$	-	120	-	
		$V_R = 400\text{ V}$ , $f = 1\text{ MHz}$	-	86	-	
Total Capacitive Charge	$Q_C$	$V_R = 400\text{ V}$ , $Q_C = \int C(V) dV$	-	63	-	nC
Capacitance Stored Energy	$E_C$	$V_R = 400\text{ V}$	-	6.9	-	$\mu\text{J}$

4. Performance Curves

Figure 1. Typical Forward Characteristics

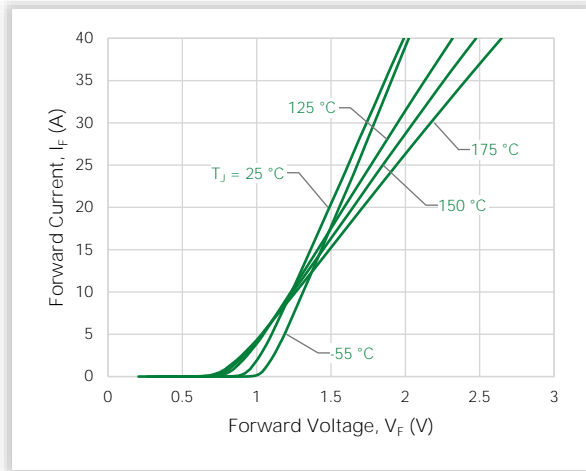


Figure 2. Typical Reverse Characteristics

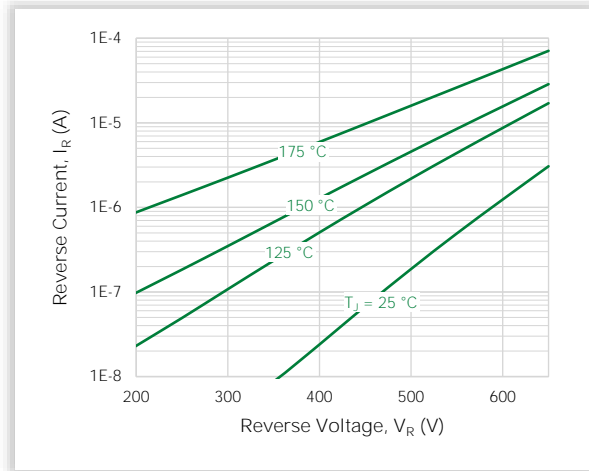


Figure 3. Power Derating

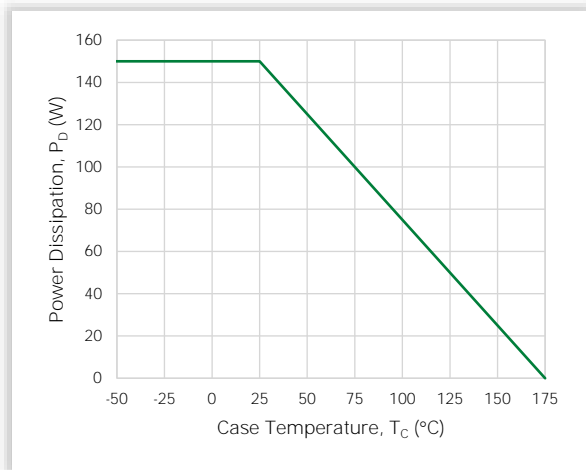


Figure 4. Current Derating

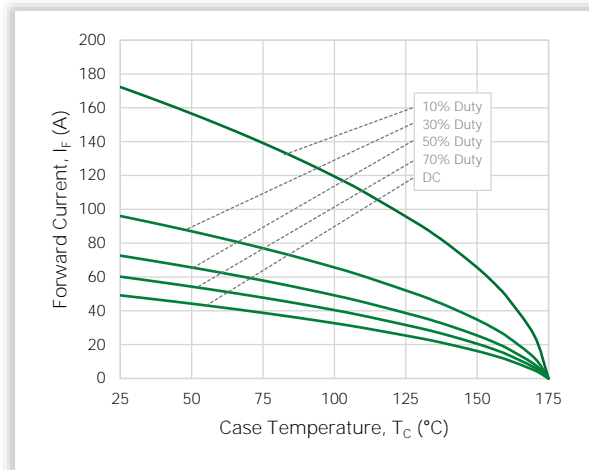


Figure 5. Capacitance vs. Reverse Voltage

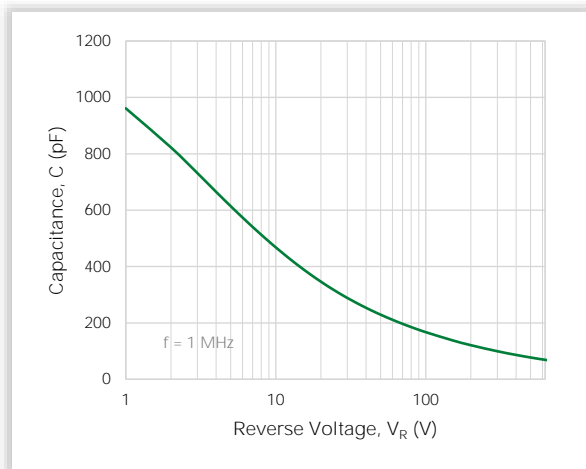


Figure 6. Capacitive Charge vs. Reverse Voltage

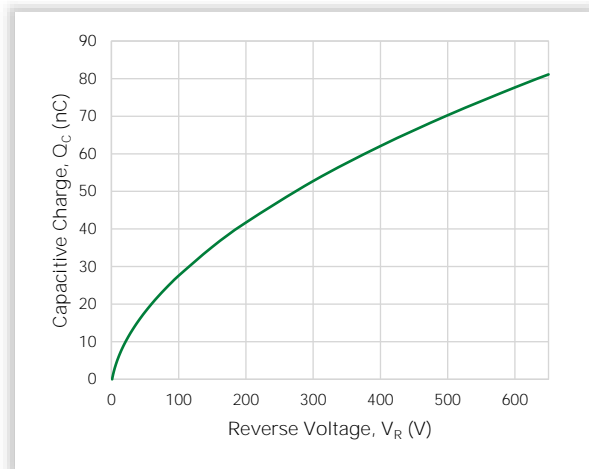


Figure 7. Stored Energy vs. Reverse Voltage

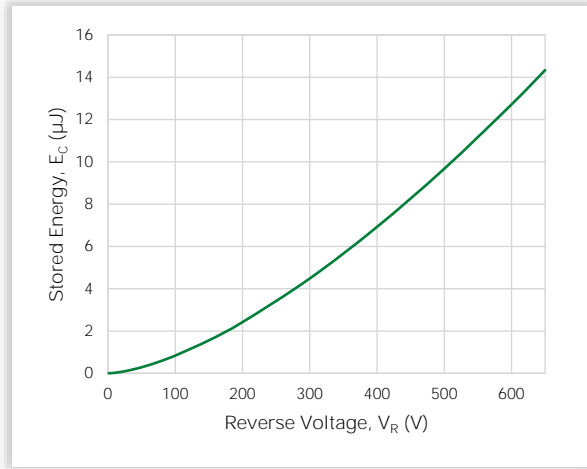
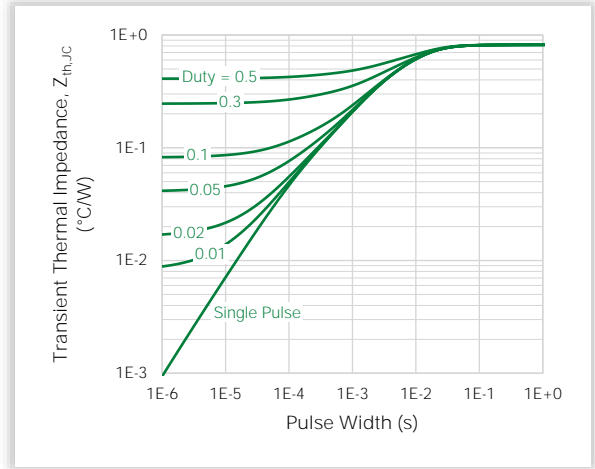
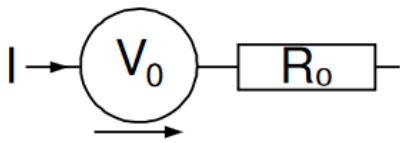


Figure 8. Transient Thermal Impedance



### 5. V<sub>F</sub> Model for Simulations



$$V_F(T_J) = V_0 + IR_0$$

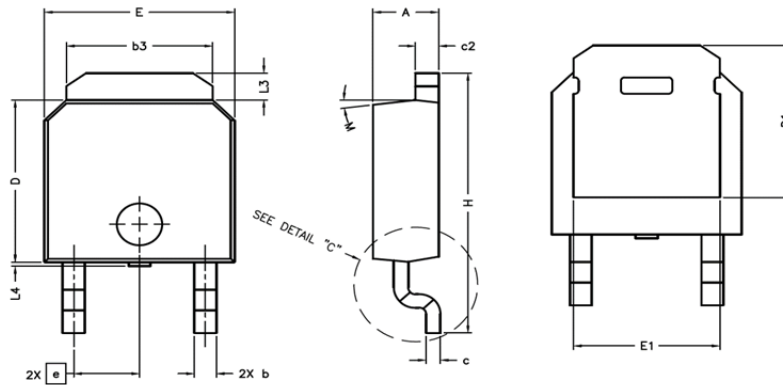
$$V_0 = -1.24 \times 10^{-3} \cdot T_J + 1.02 \times 10^0$$

$$R_0 = 5.64 \times 10^{-7} \cdot T_J^2 + 2.67 \times 10^{-5} \cdot T_J + 2.41 \times 10^{-2}$$

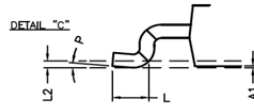
Notes:

- T<sub>J</sub> is junction temperature in °C
- Range valid from 25 °C to 175 °C
- Model represents performance of a typical part

6. Package Dimensions

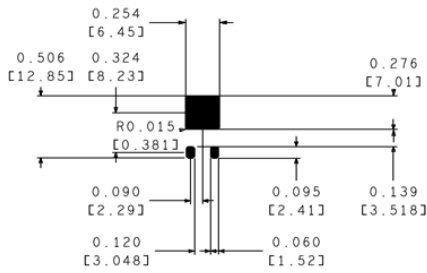


- NOTE:
1. L4 – MAXIMUM PLASTIC PROTRUSION.
  2. L2 – REFERENCE FOR FOOT LENGTH MEASUREMENT.
  3. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS B3, L3, D1, AND E1
  4. PACKAGE OUTLINE D AND E EXCLUSIVE OF ANY MOLD FLASHES DIMENSIONS.
  5. PACKAGE OUTLINE D AND E EXCLUSIVE OF ANY BURR DIMENSIONS.
  6. FOR SINGLE-GAUGE LEADFRAME.



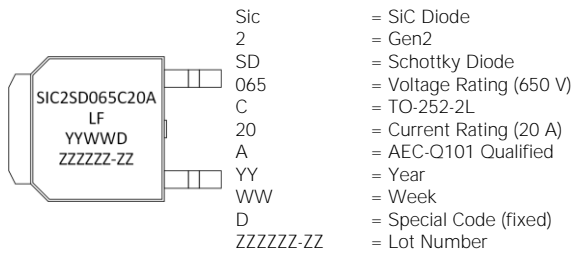
Symbol	Millimeters	
	Min	Max
A	2.159	2.413
A1	-	0.127
b	0.635	1.067
b3	4.953	5.461
c	0.457	0.610
c2	-	0.889
(c2)	0.457	0.610
D	5.969	6.223
D1	5.207	5.715
E	6.350	6.731
E1	4.318	5.207
e	2.29 REF.	
H	9.398	10.414
L	1.016	1.778
L2	0.25 REF.	
L3	0.889	1.270
L4	0.000	0.152
M	15°	
P	-	5°

Recommended Solder Pad Layout



UNIT: Inch [mm]

### 7. Part Numbering and Marking

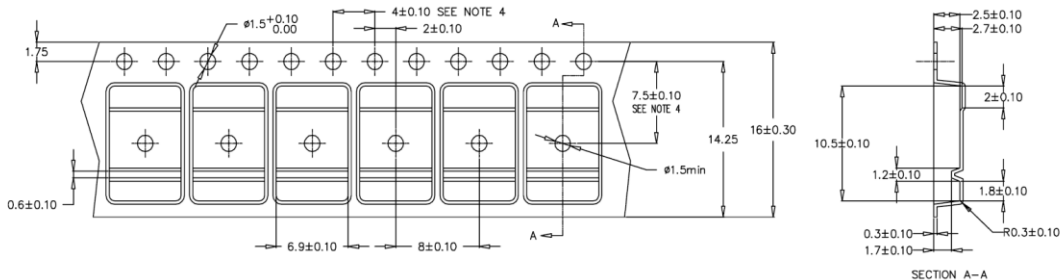


### 8. Packing Options

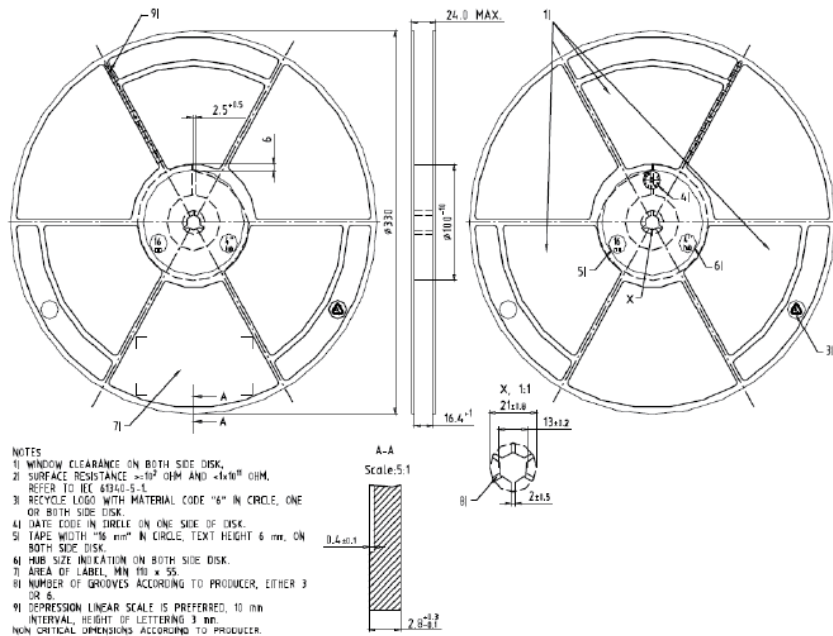
Part Number	Marking	Packing Mode	M.O.Q.
LSIC2SD065C20A	SIC2SD065C20A	Tape and Reel	2500

### 9. Packing Specifications

Carrier Tape and Reel Specification TO-252-2L



1. Material: Black Conductive Polyesterene
2. 10 sprocket hole pitch cumulative tolerance  $\pm 0.20$
3. Camber not to exceed 1 mm in 100 mm.
4. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.
5. Device orientation: TRL (leads perpendicular to the sprocket)
6. General tolerance is  $\pm 0.10$  mm unless otherwise specified.



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